

COMPARISON SYSTEM

ABSTRACT

Embodiments of the disclosure provide a comparison system including at least one comparison circuit, the comparison circuit including: a common module, connected to a power supply signal and a ground signal, and configured to control output of the power supply signal or the ground signal based on a first signal and a second signal which are inverted; a first logical unit, connected to the common module, and configured to receive a third signal and a fourth signal which are inverted, and output a first operation signal which is an exclusive OR (XOR) of the first signal and the third signal; and a second logical unit, connected to the common module, and configured to receive the third signal and the fourth signal, and output a second operation signal which is a not exclusive OR (XNOR) of the first signal and the third signal. The embodiments of the disclosure facilitate an operation speed of the comparison system to be improved.

[Fig. 2]