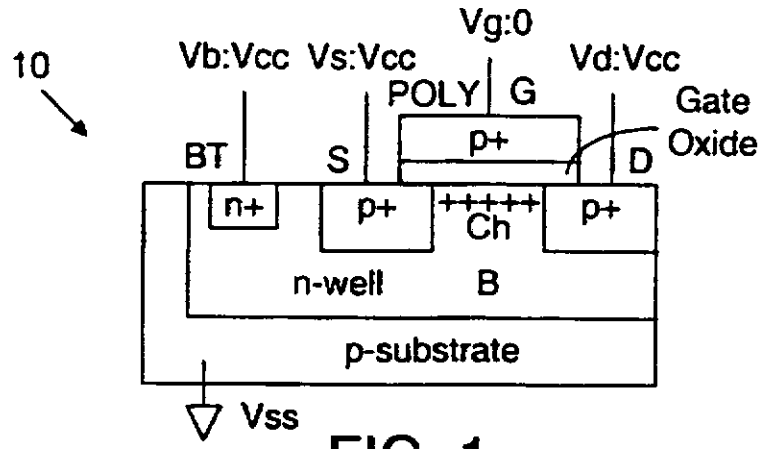


FIG. 1
(Prior Art)

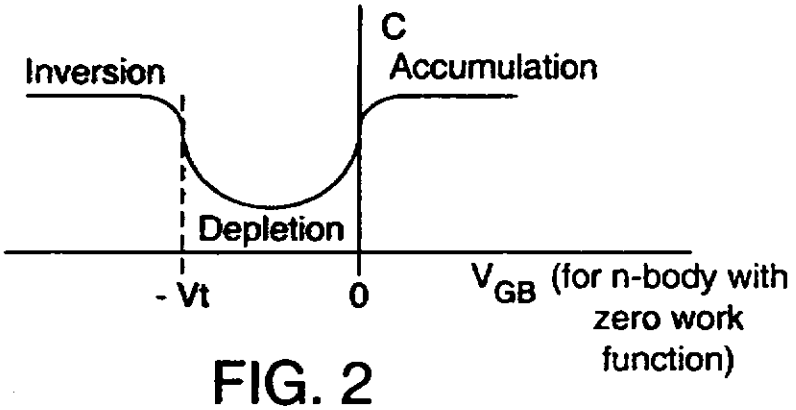


FIG. 2

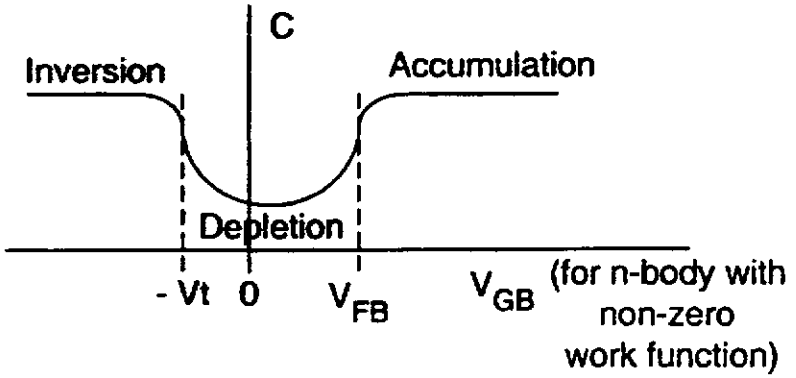


FIG. 3

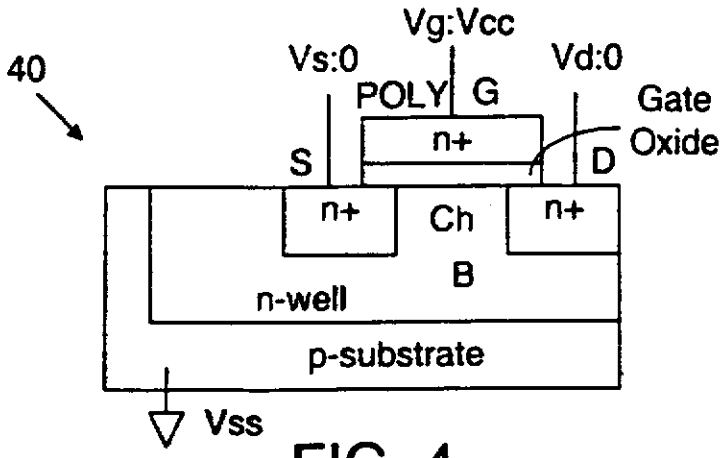


FIG. 4

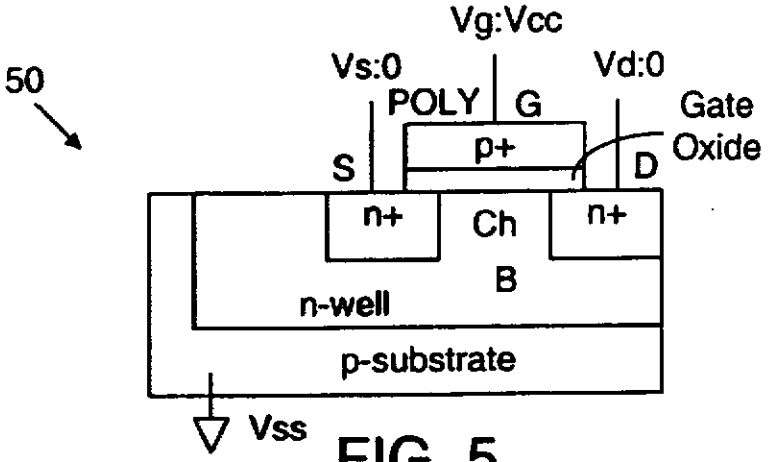


FIG. 5

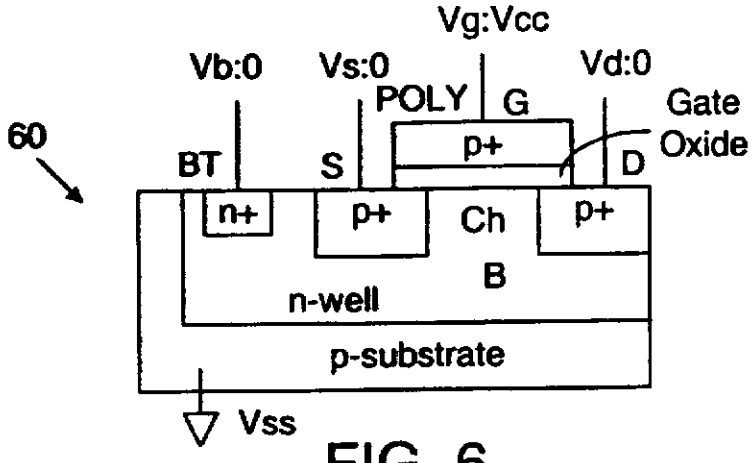


FIG. 6

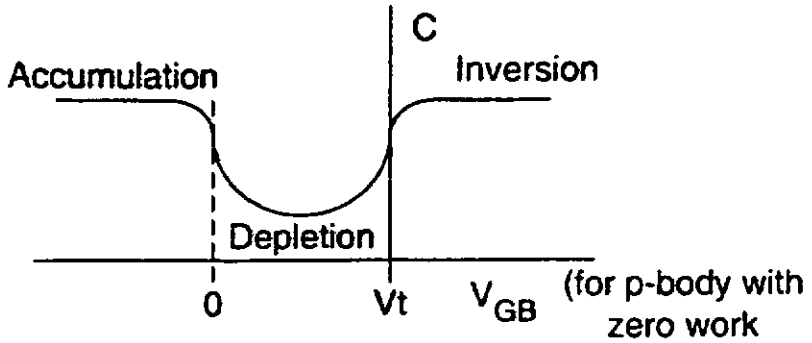


FIG. 7

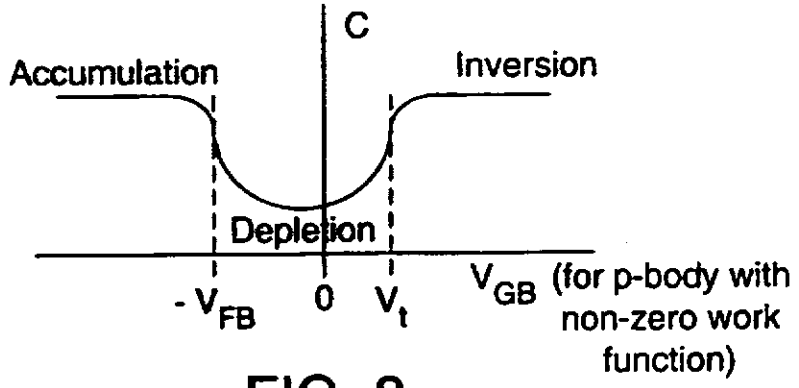


FIG. 8

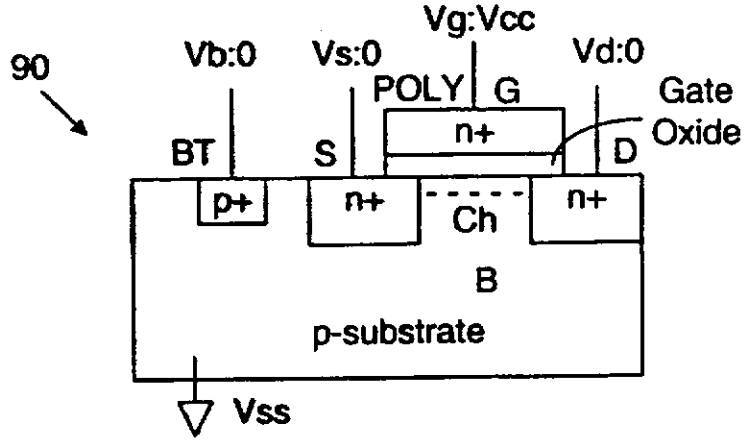


FIG. 9
(Prior Art)

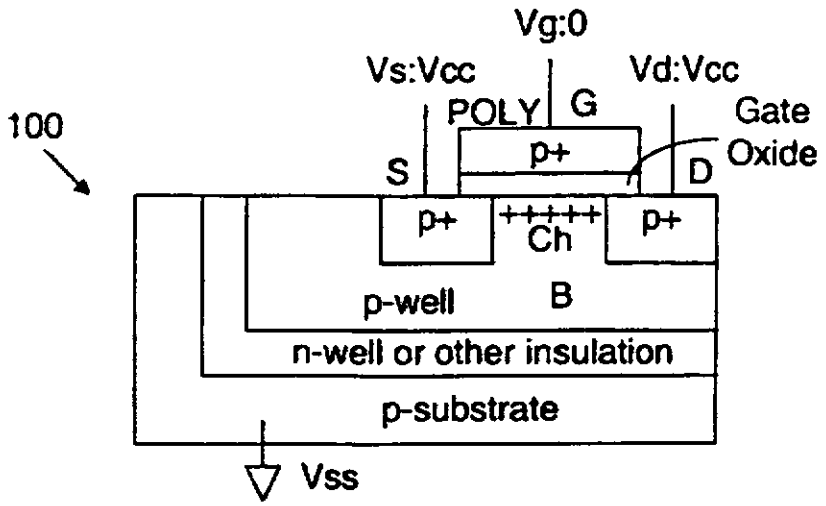

Vg:0
Vs:Vcc
Vd:Vcc
POLY
G
Gate
Oxide
100
S
p+
D
p+
Ch
p+
p-well
B
n-well or other insulation
p-substrate
Vss

FIG. 10

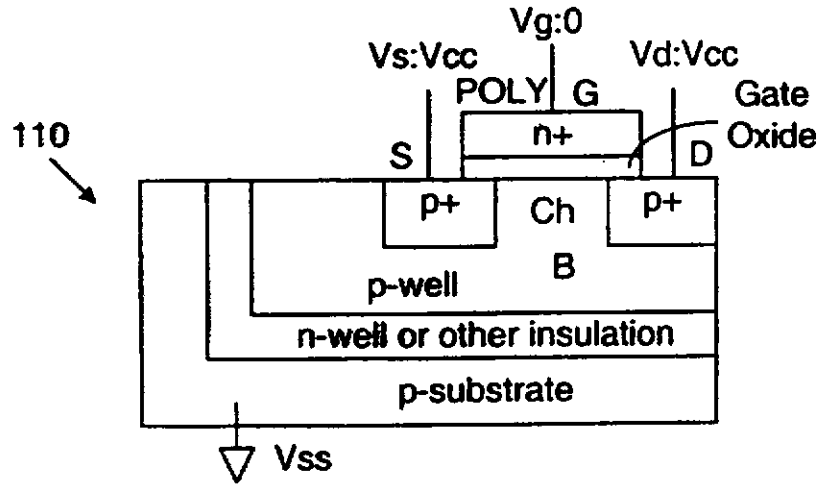

Vg:0
Vs:Vcc
Vd:Vcc
POLY
G
Gate
Oxide
110
S
n+
D
p+
Ch
p+
B
p-well
n-well or other insulation
p-substrate
Vss

FIG. 11

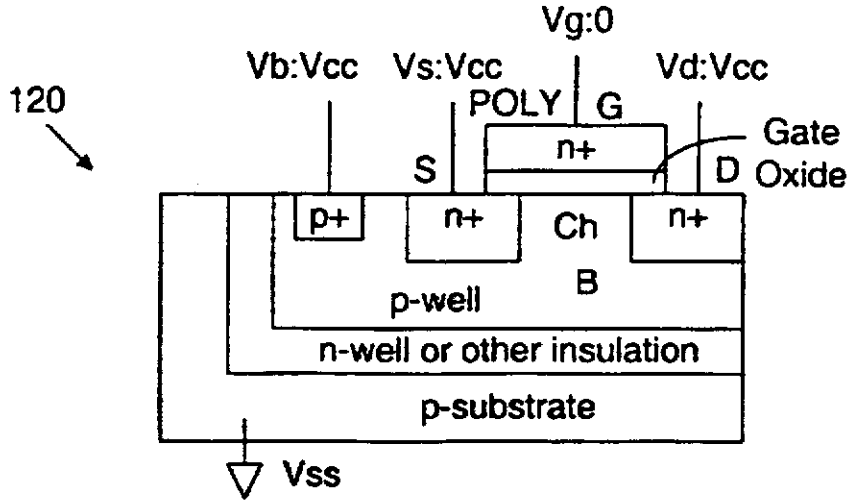


FIG. 12

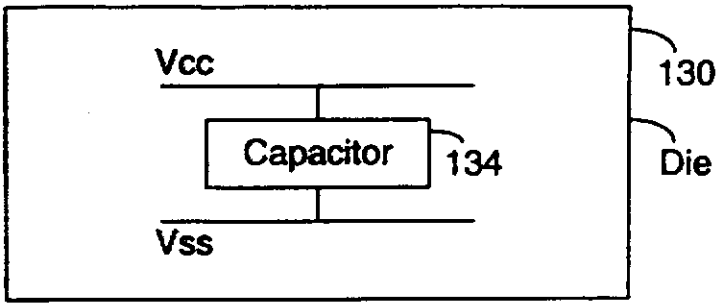


FIG. 13

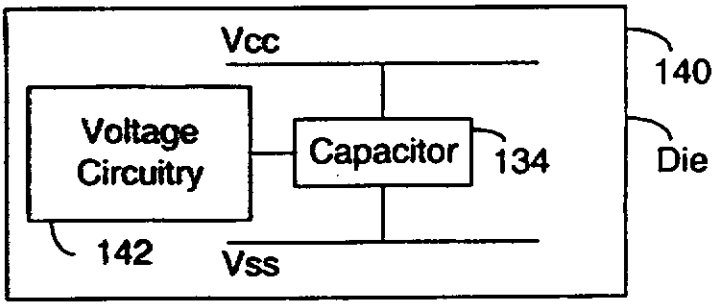


FIG. 14