

FIG. 1

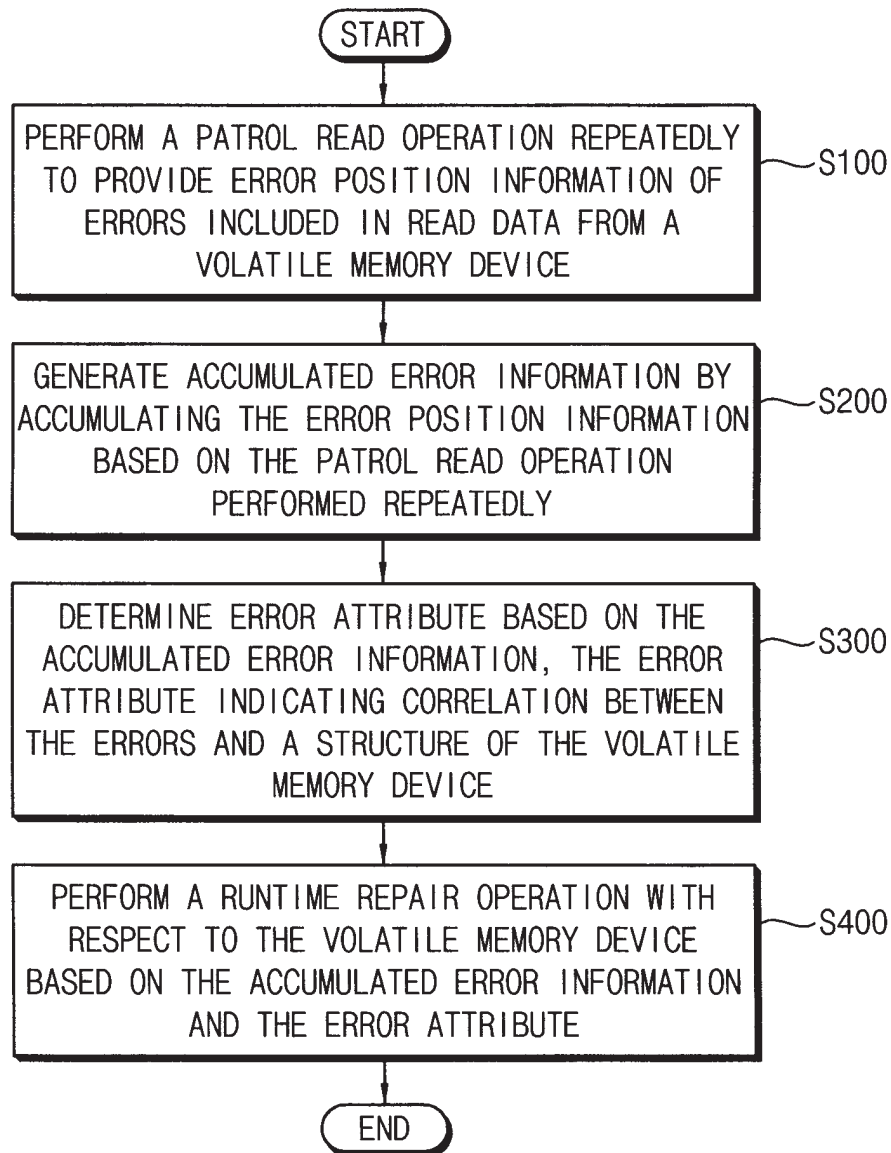


FIG. 2

20

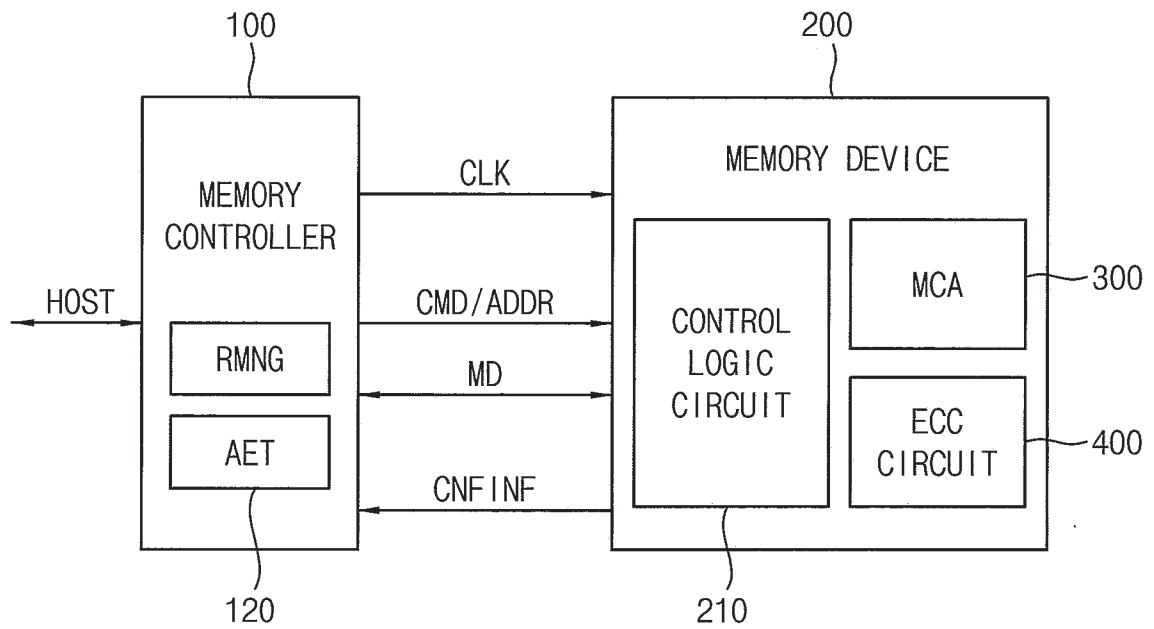


FIG. 3

DATA BITS	PARITY BITS			PARITY 0/H		
	SEC	SEC-DED	DEC	SEC	SEC-DED	DEC
8	4	5	8	50.0%	62.5%	100%
64	7	8	14	10.9%	12.5%	21.9%
128	8	9	16	6.3%	7.0%	12.5%
256	9	10	18	3.5%	3.9%	7.0%
512	10	11	20	2.0%	2.1%	3.9%

FIG. 4

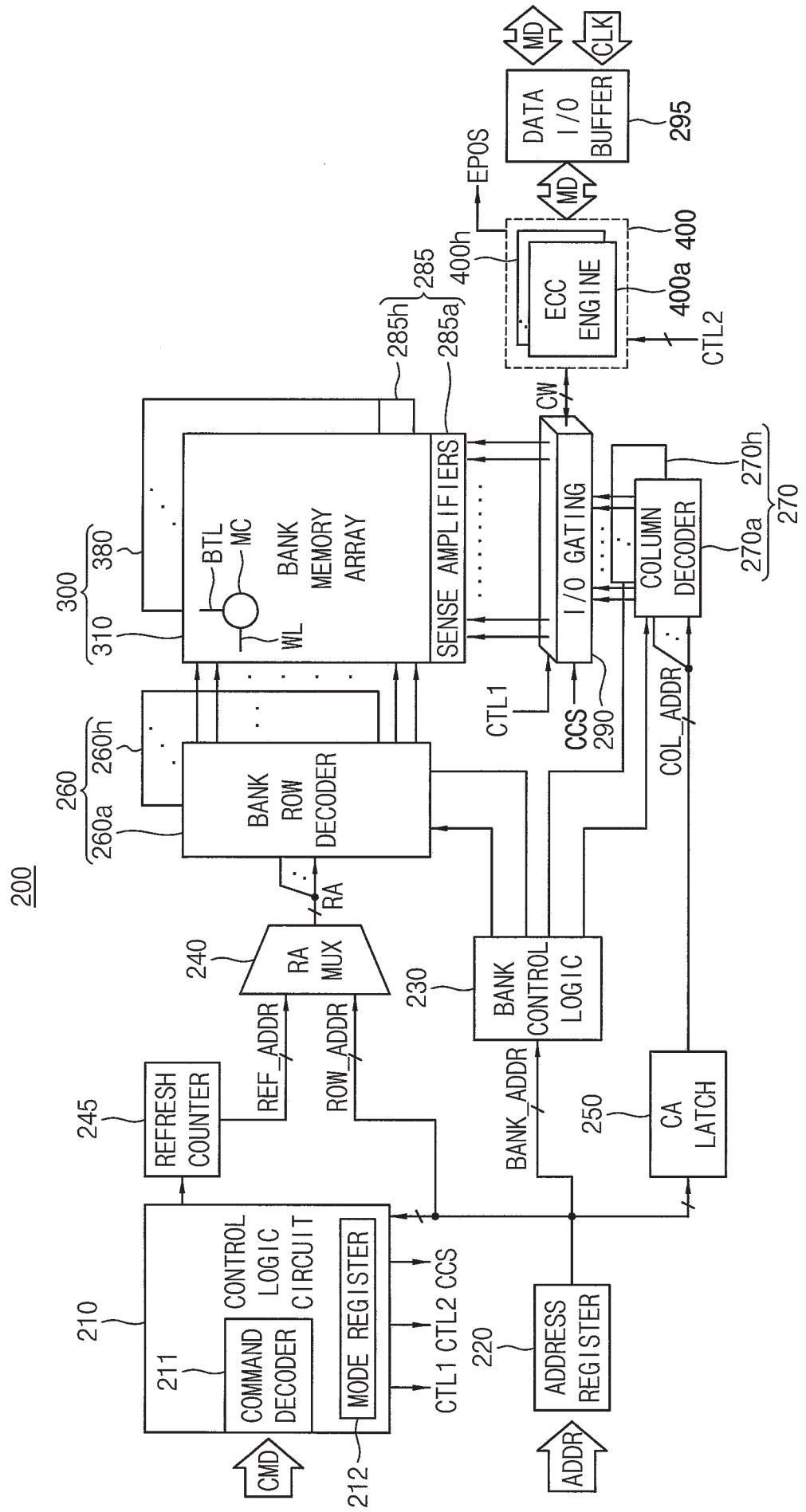


FIG. 5

200a

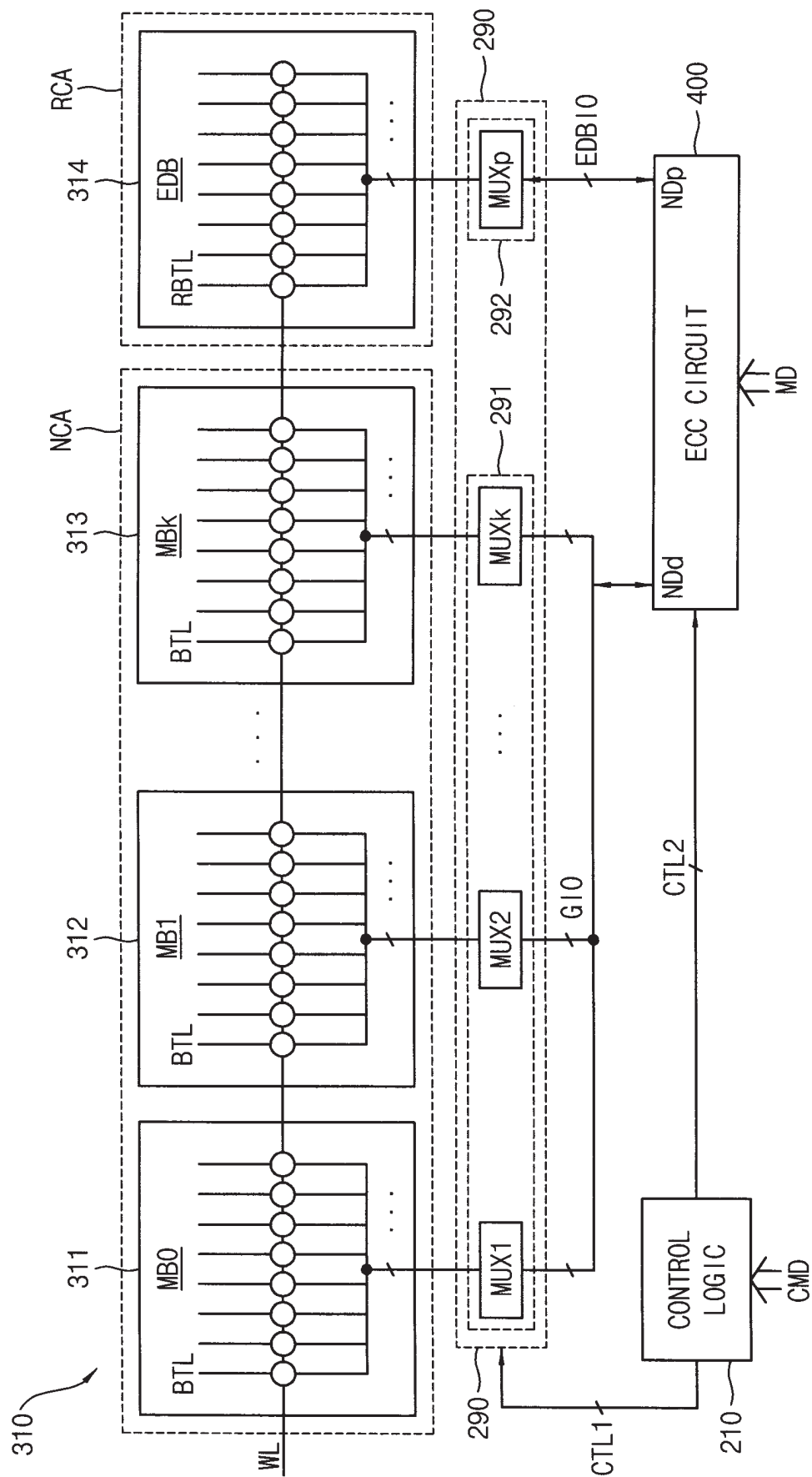


FIG. 6A

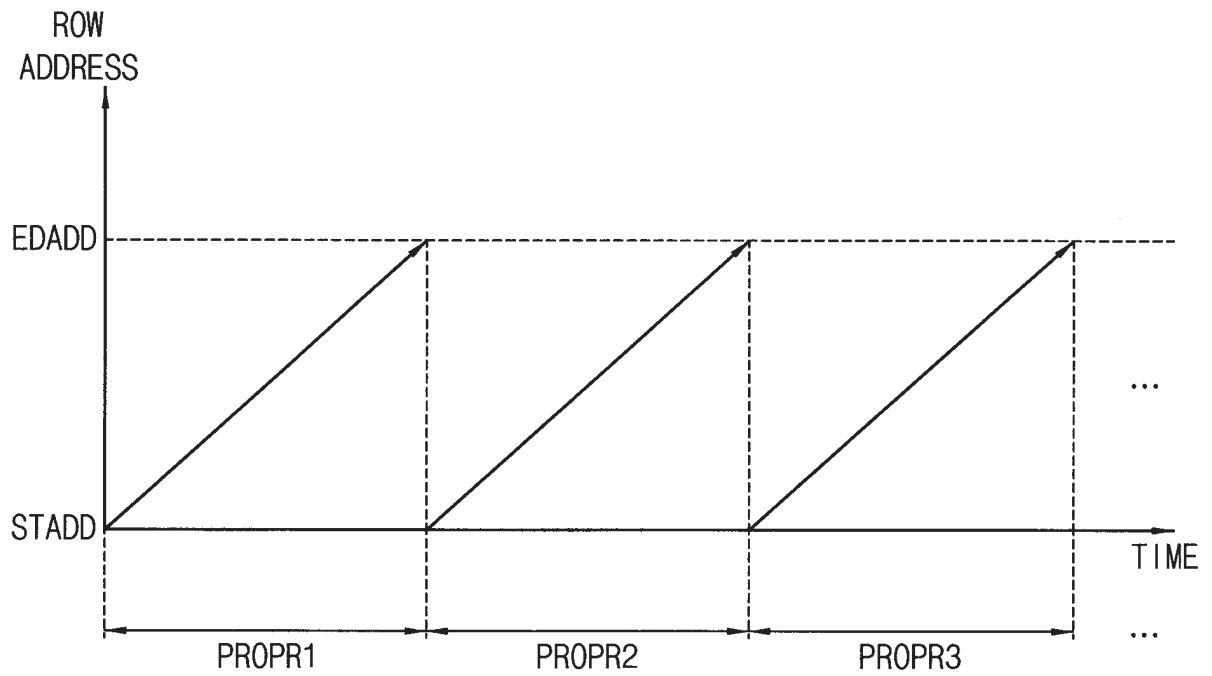


FIG. 6B

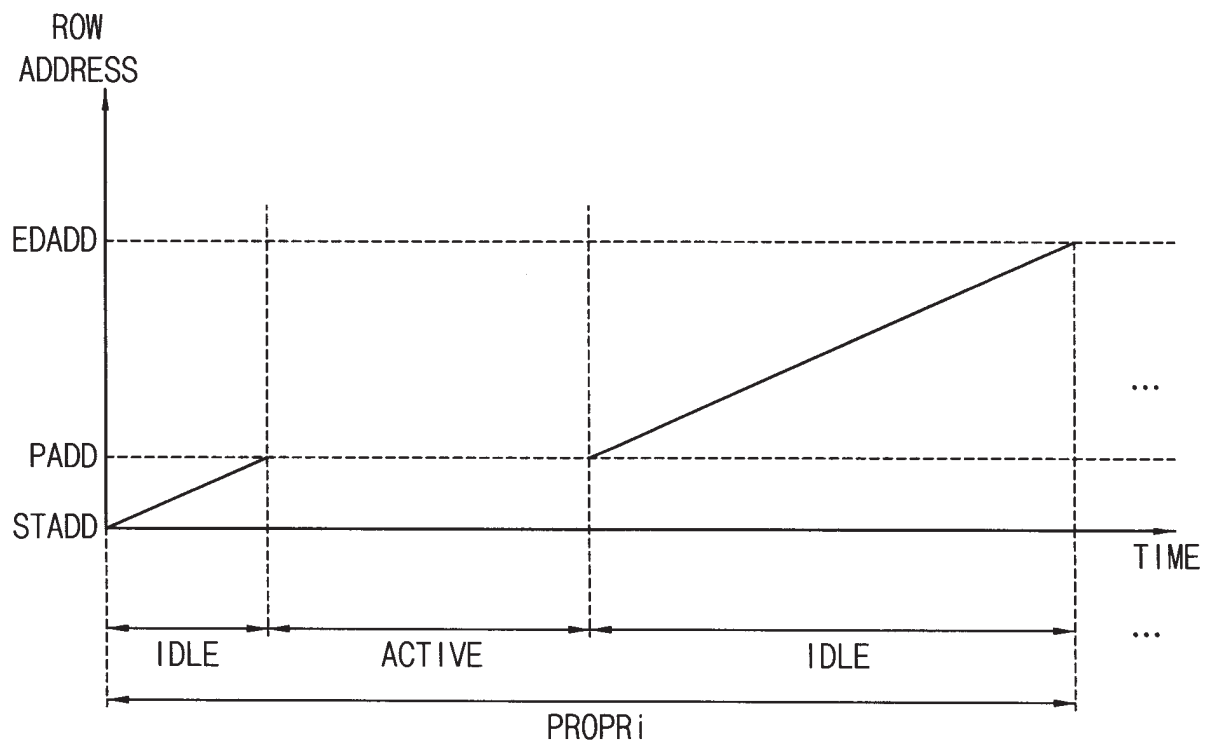


FIG. 7

AET1AET2AET3...

PFRADD	EN	ATT
RAa	2	N/A
RAb	1	N/A
RAc	5	ATT1
RAd	4	ATT1
RAe	0	ATT1
RAf	2	N/A
⋮	⋮	⋮

FIG. 8

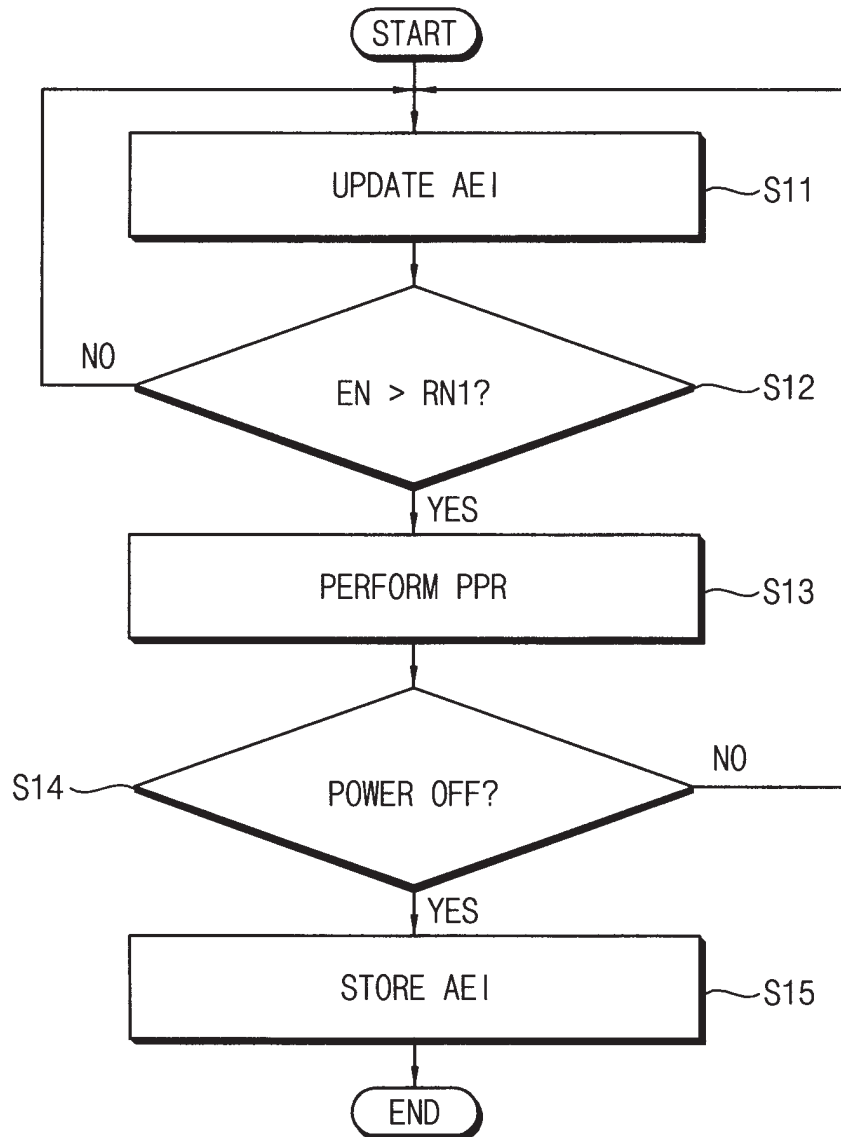


FIG. 9

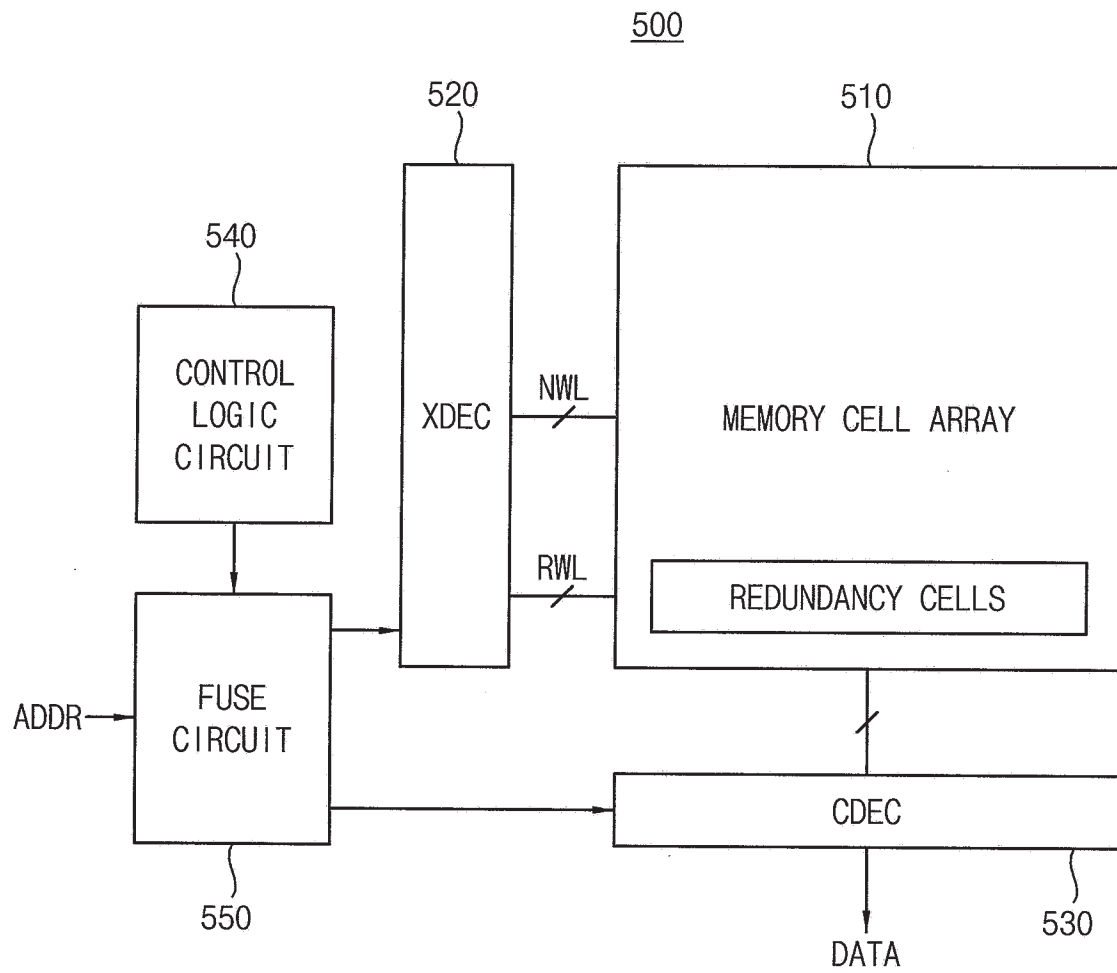


FIG. 10

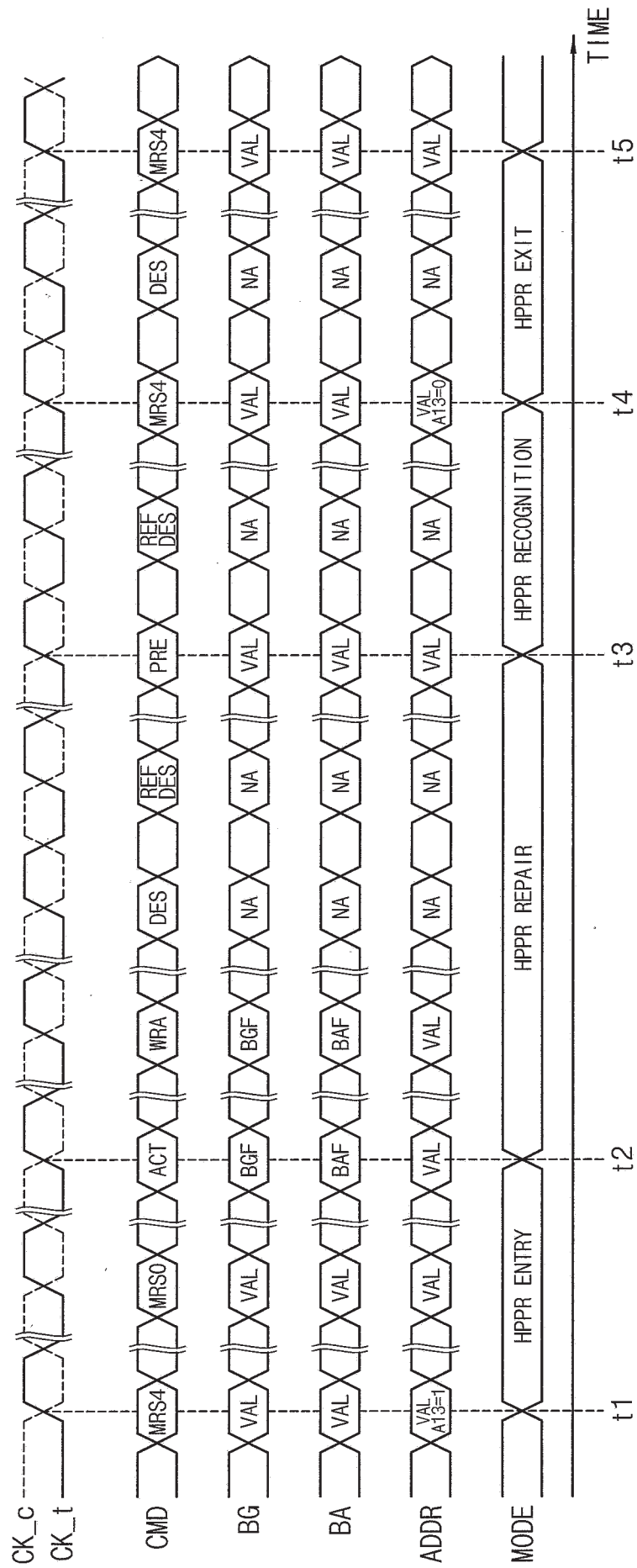


FIG. 11

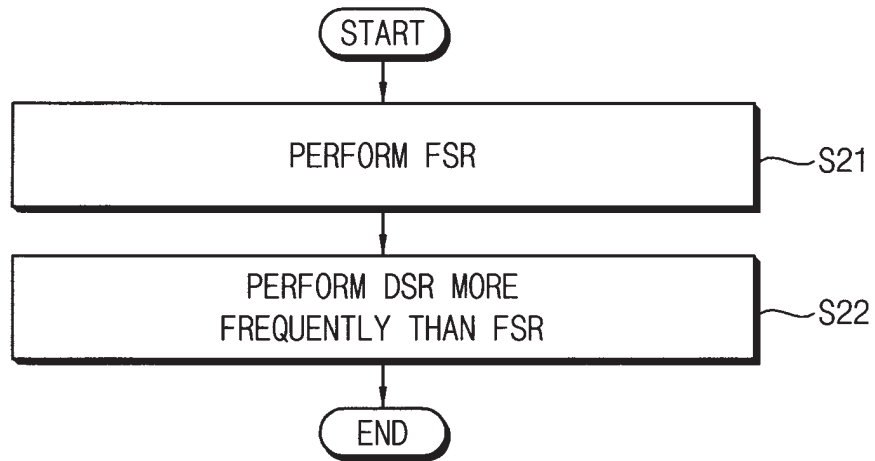


FIG. 12

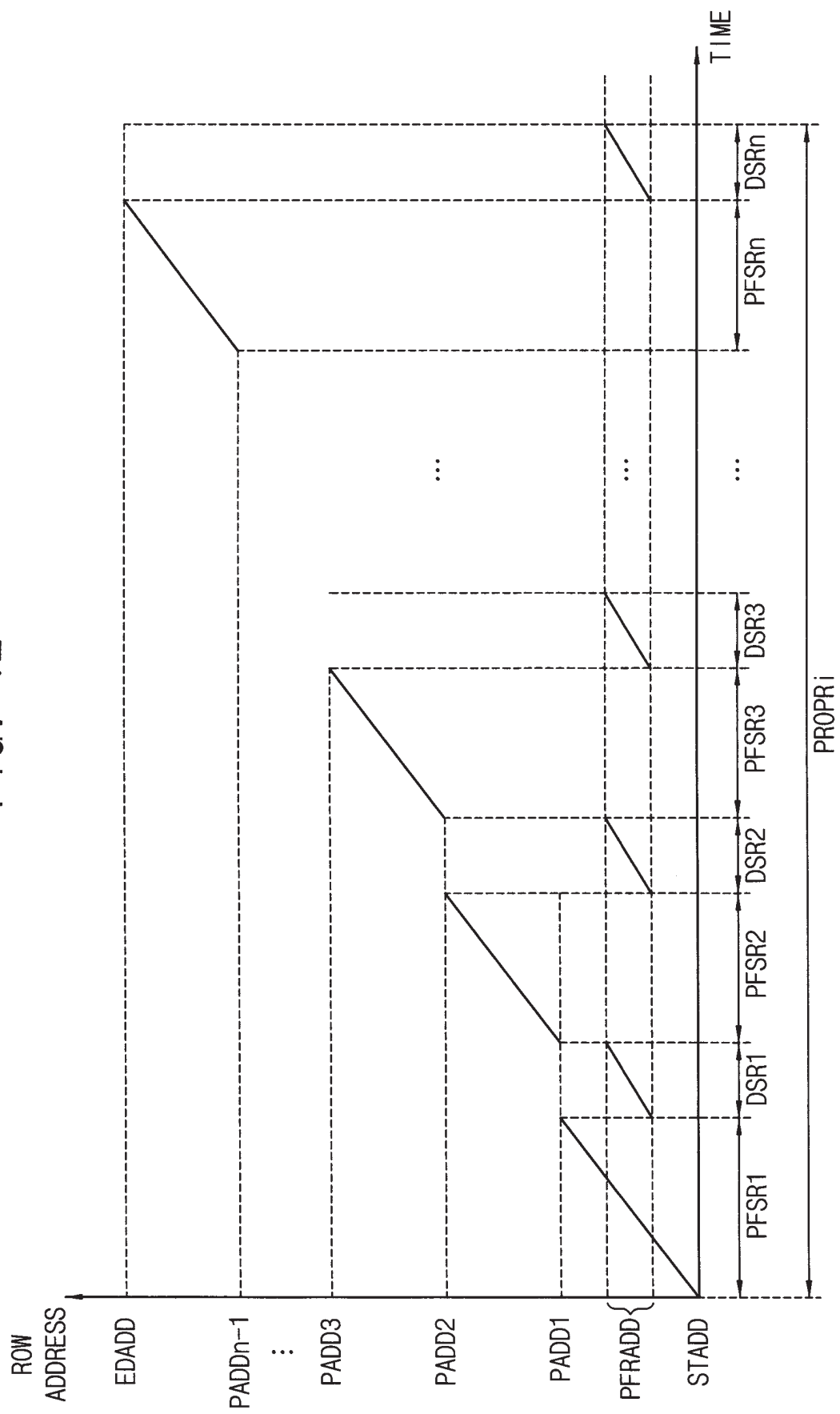


FIG. 13

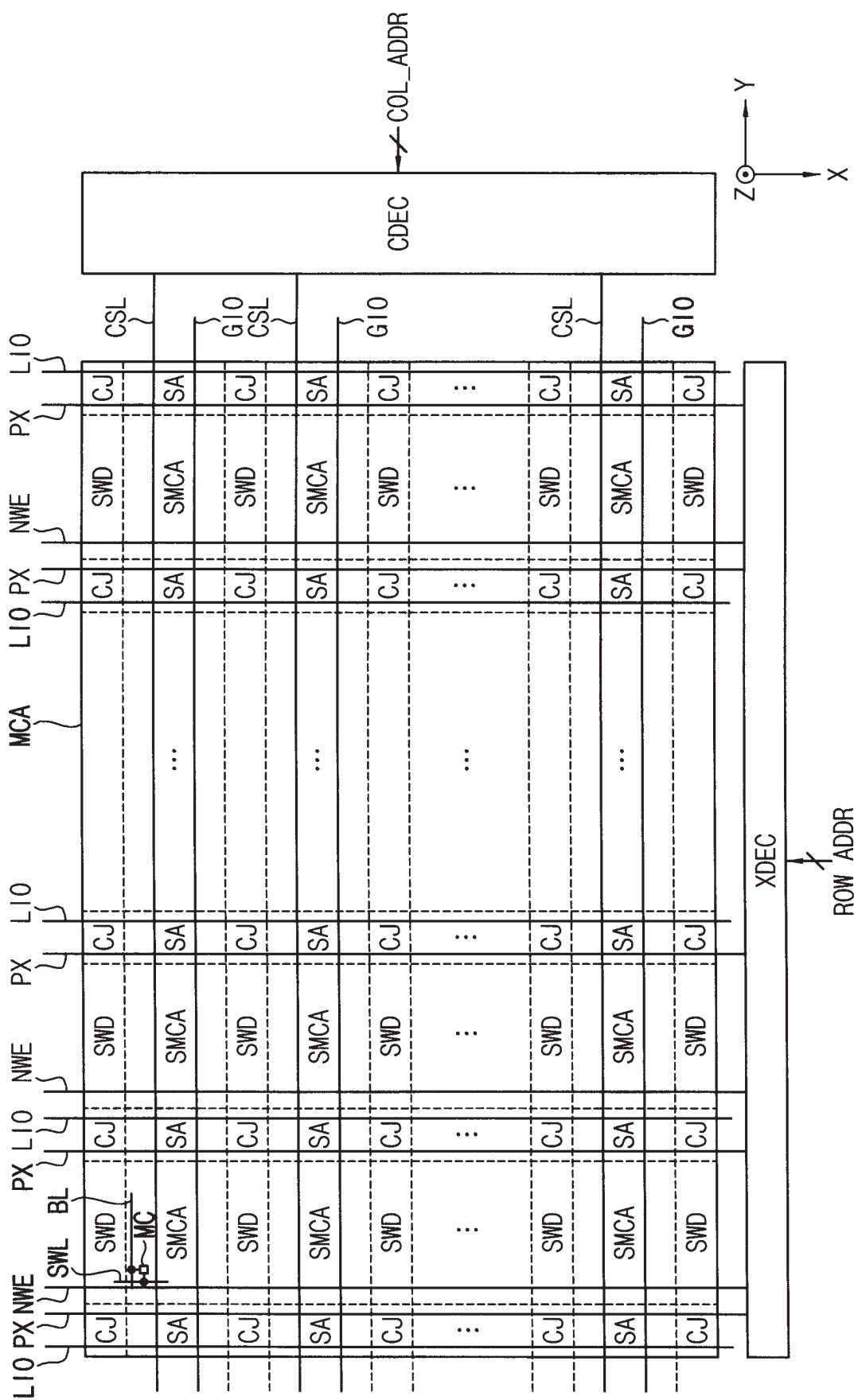


FIG. 14

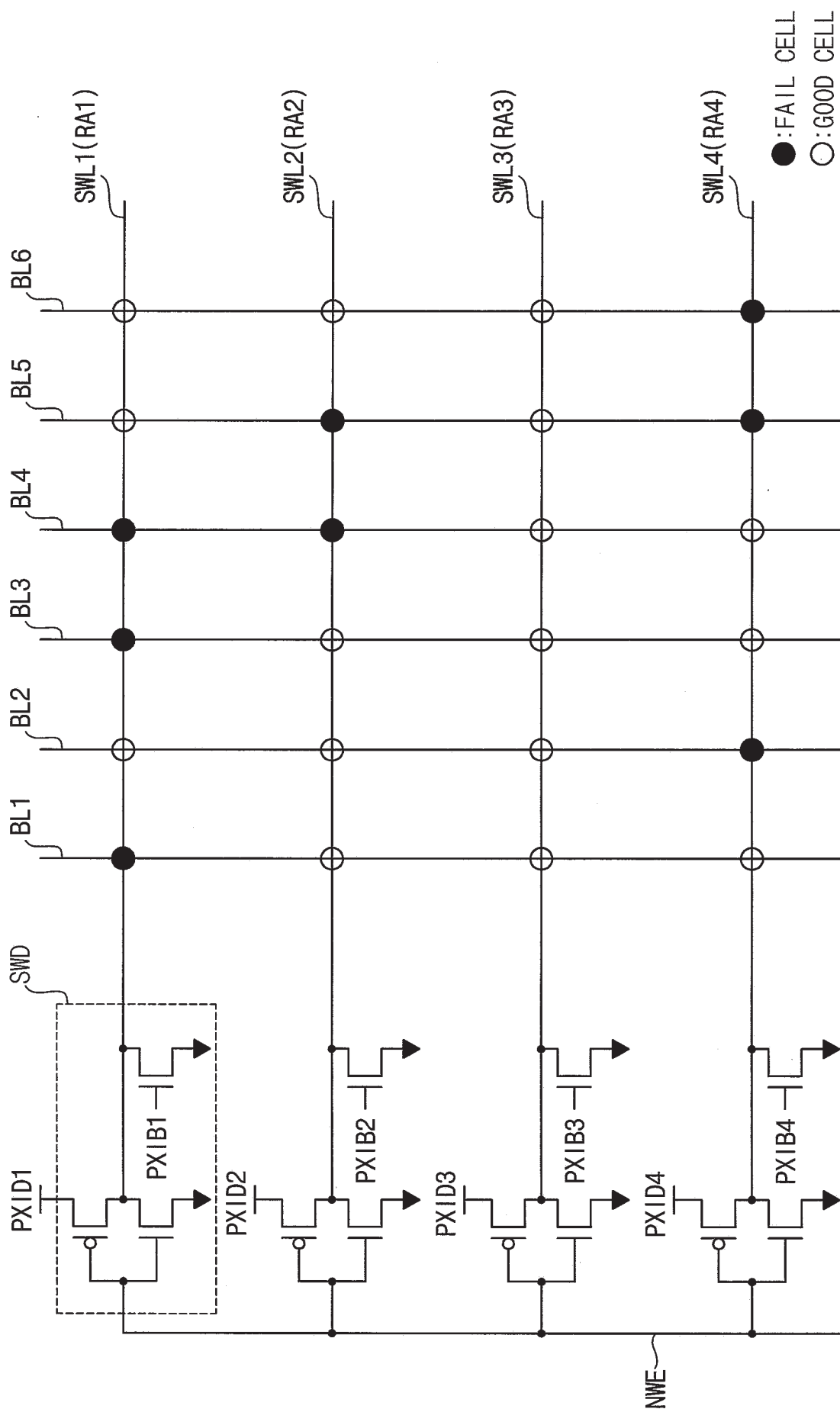


FIG. 15

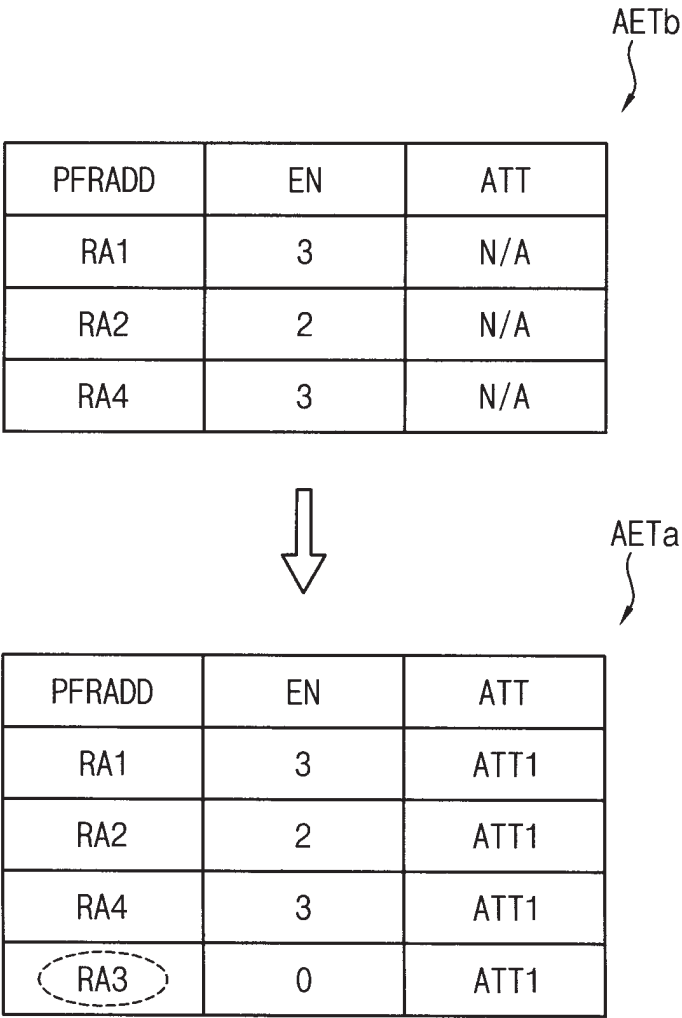


FIG. 16

AET
}

PFRADD	PFCADD		EN	ATT
RAa	CAa	(CAb)	2	N/A
RAb	CAC		1	N/A
RAC	(CAb)	CAd CAe CAf CAg	5	ATT1
RAd	(CAb)	CAi CAj CAk	4	ATT1
RAe			0	ATT1
RAf	(CAb)	CAh	2	N/A
⋮		⋮	⋮	⋮

FIG. 17

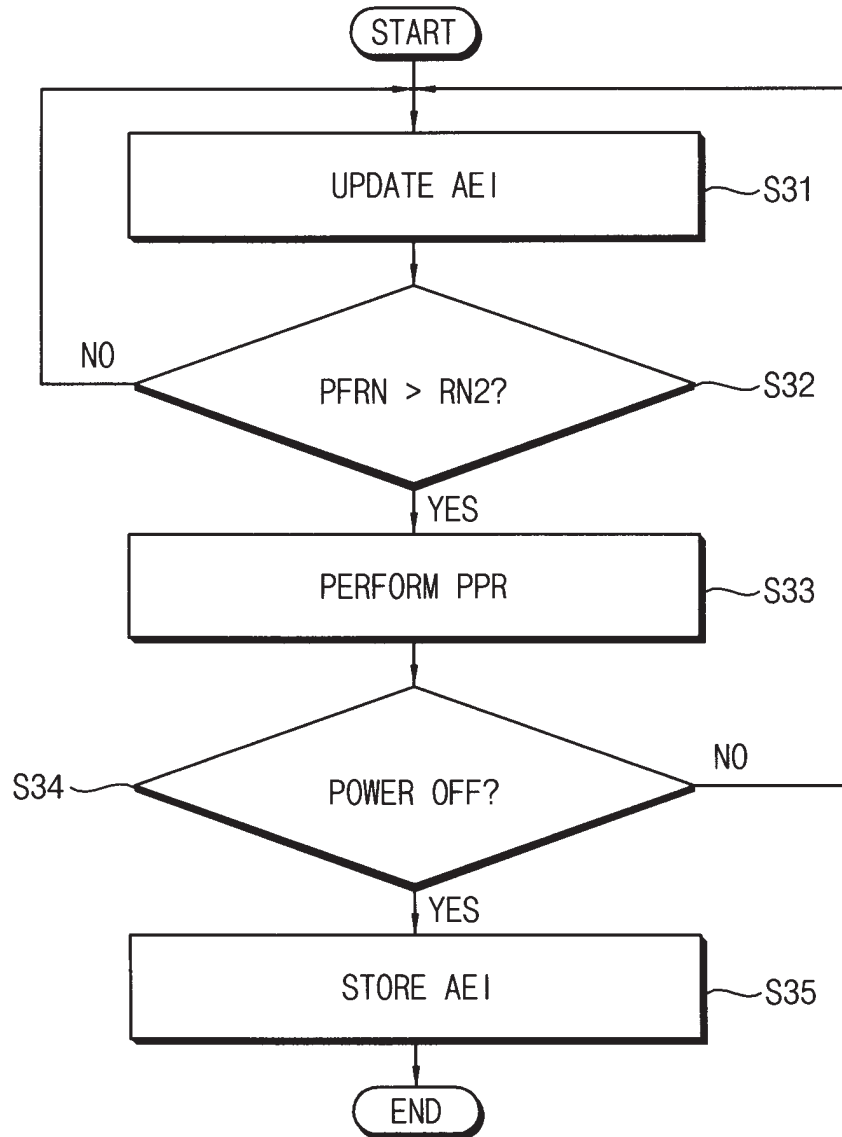


FIG. 18

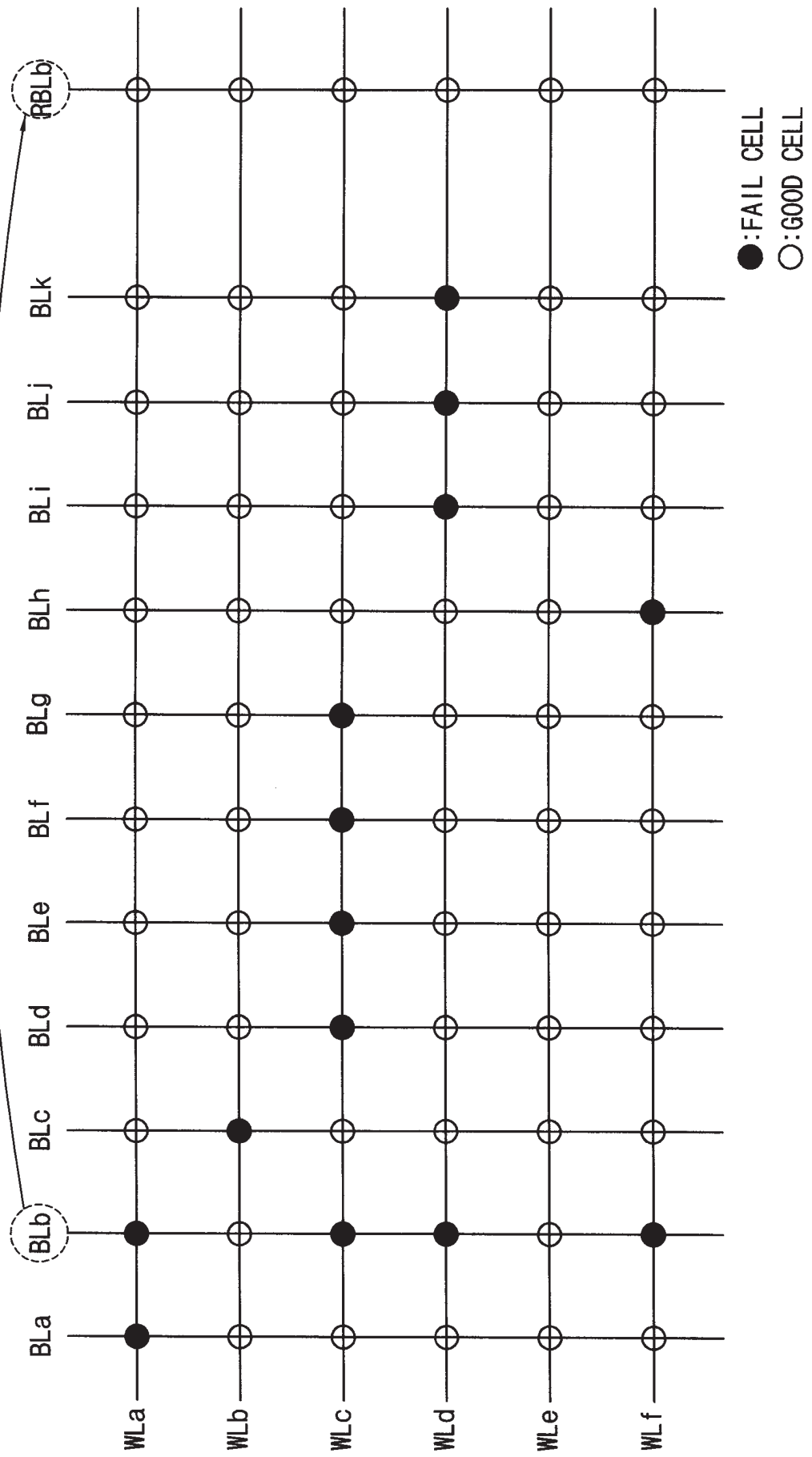


FIG. 19

AET



PFRADD	INX	PFCADD	EN	ATT
RAa	0	CAa CAb	2	N/A
	1	CAa CAb CAc	3	
RAb	0	CAd CAe CAf	3	N/A
	1	CAd CAf	2	
⋮		⋮	⋮	⋮

FIG. 20

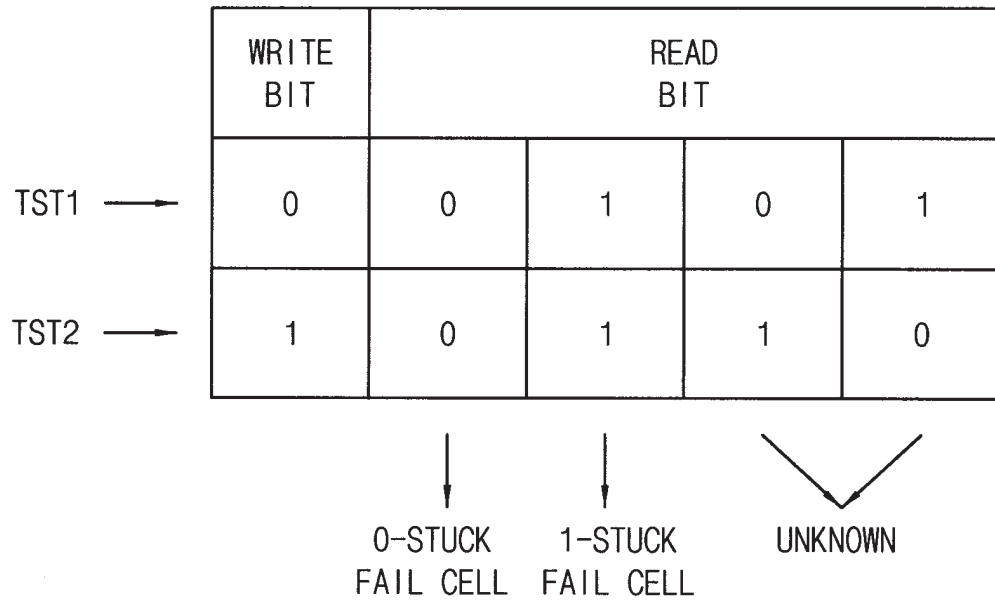


FIG. 21

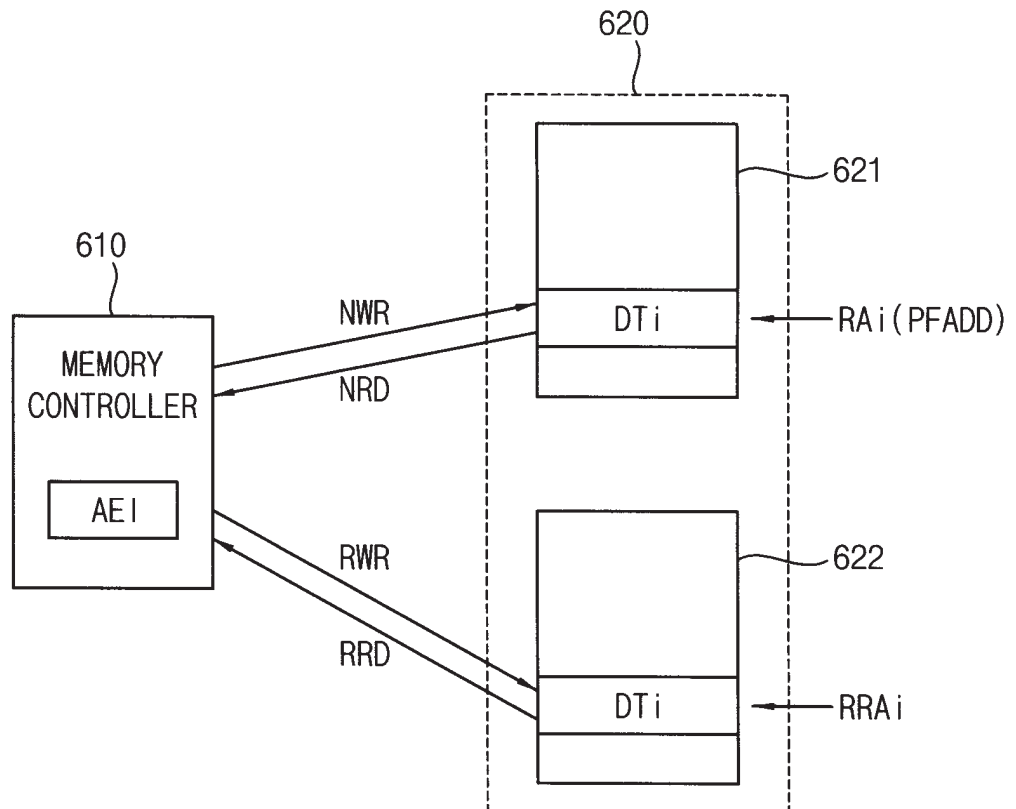


FIG. 22

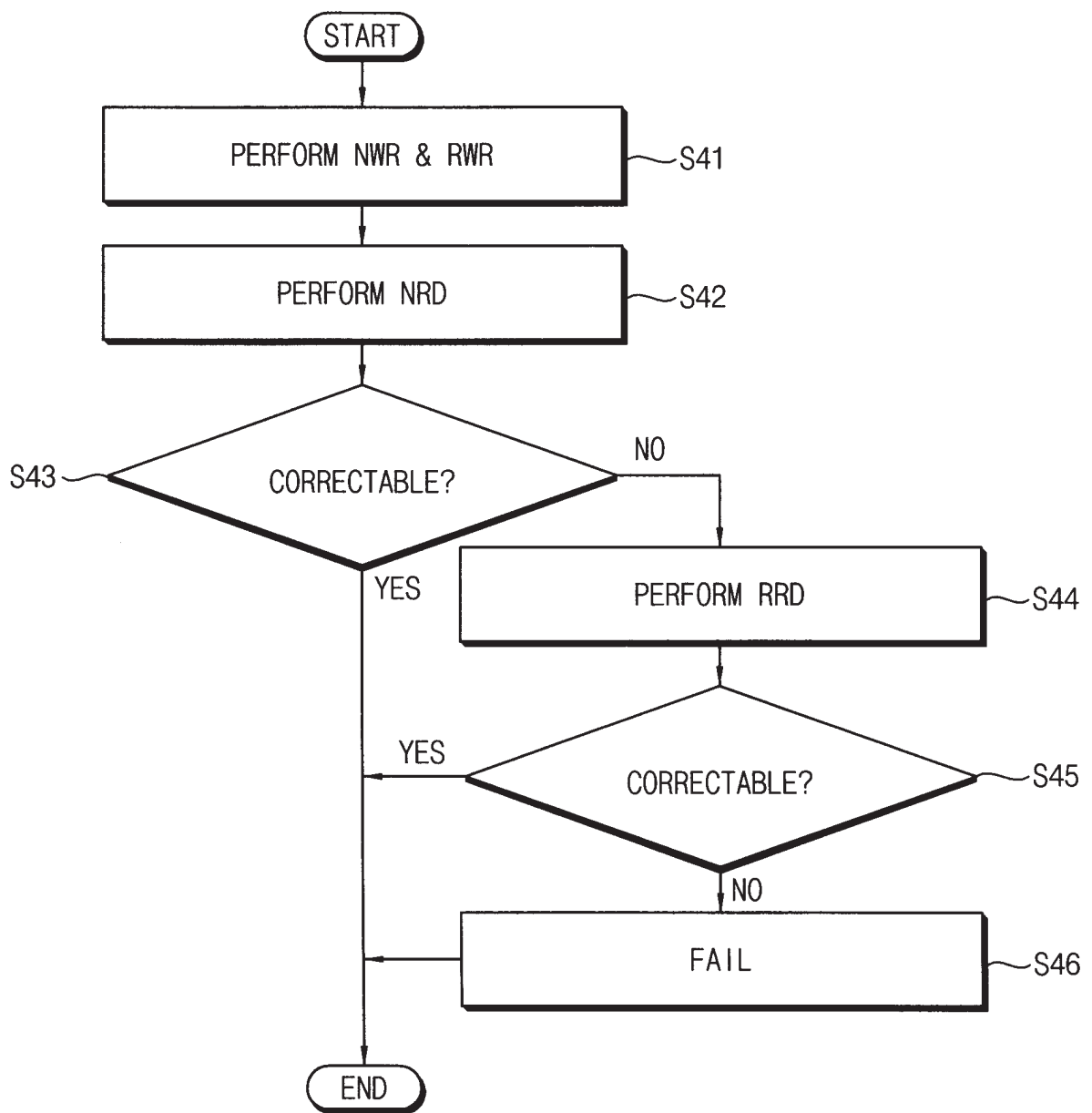


FIG. 23

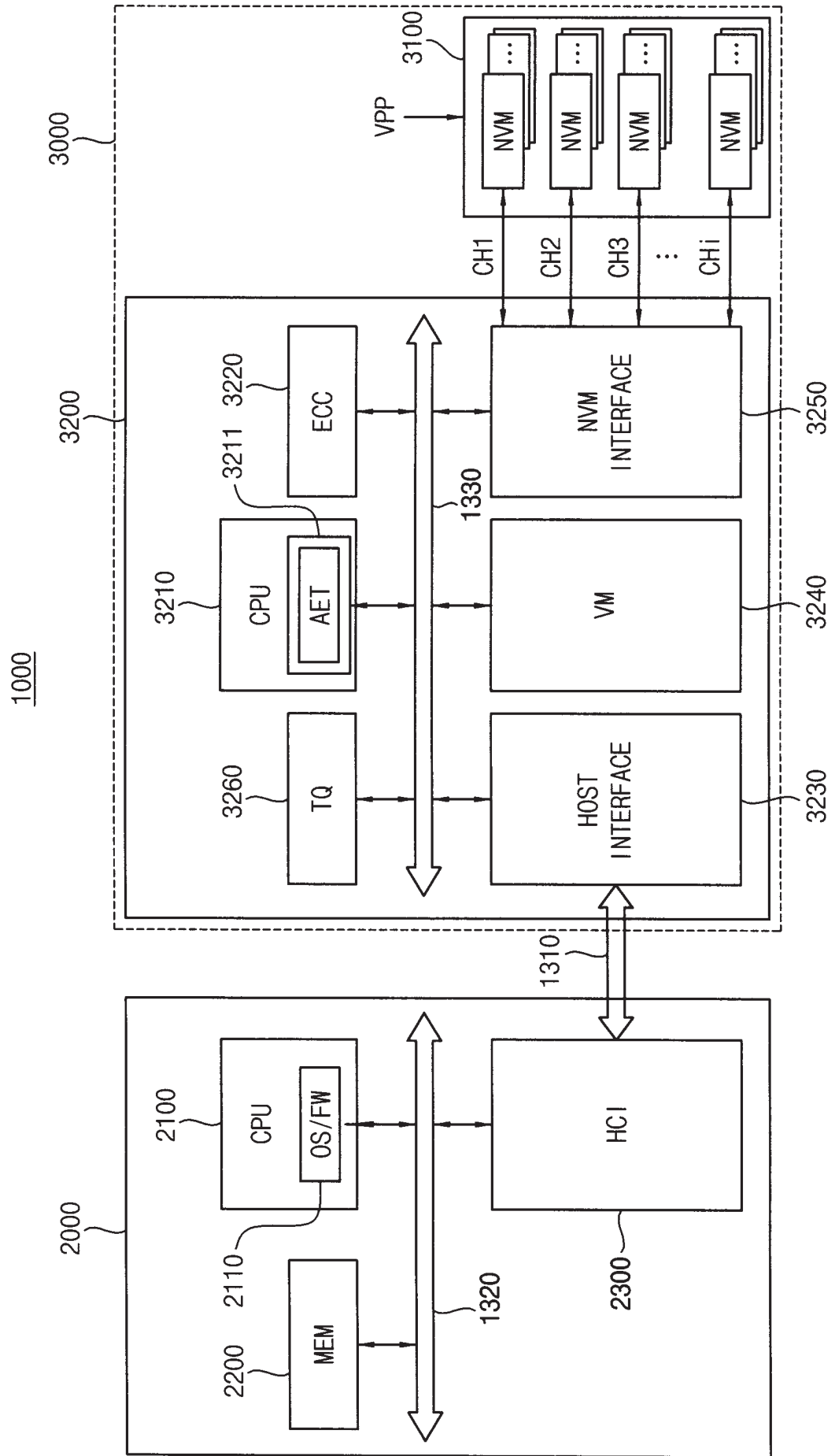


FIG. 24

