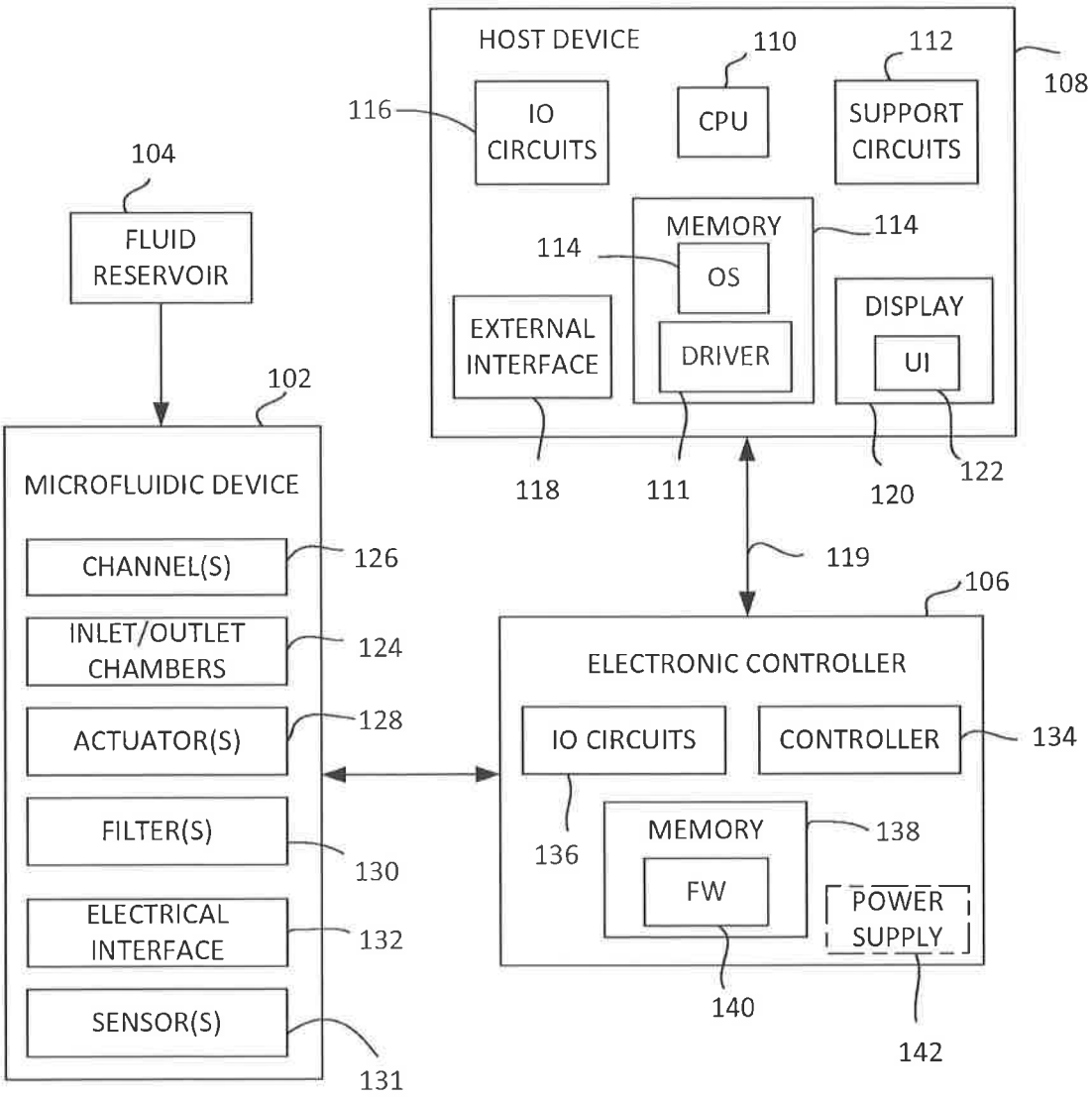
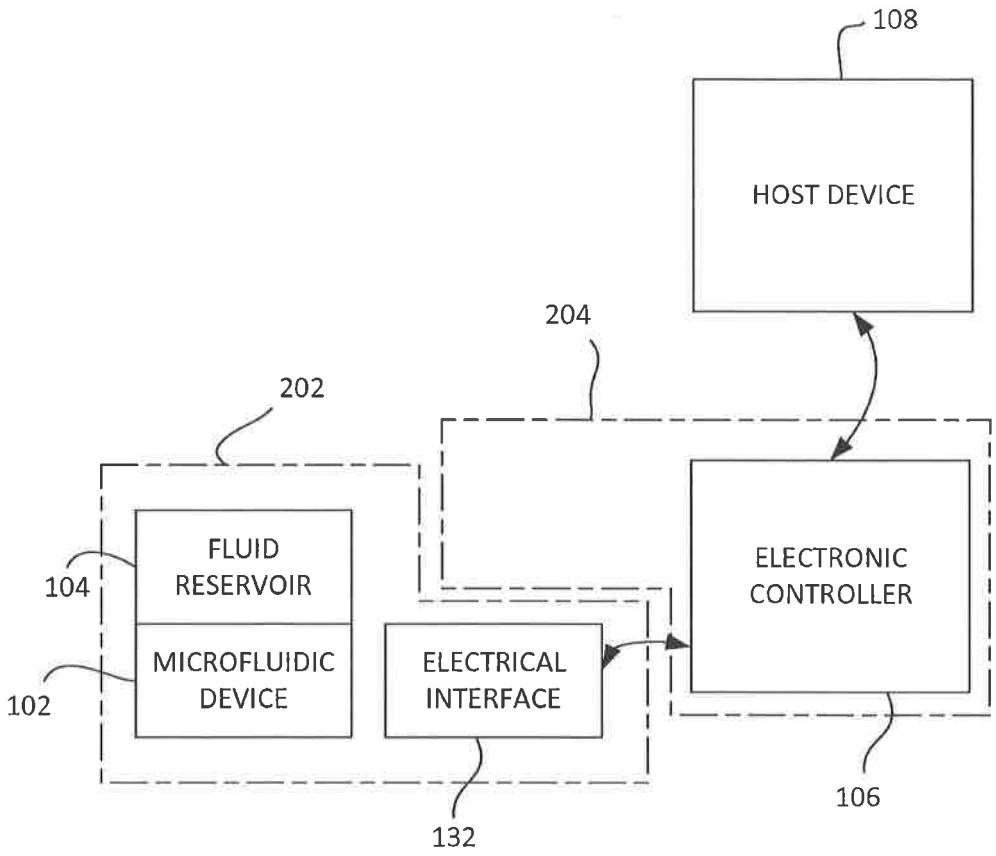

HOST DEVICE
110
112
108
116
IO CIRCUITS
CPU
SUPPORT CIRCUITS
104
FLUID RESERVOIR
MEMORY
114
114
OS
DISPLAY
EXTERNAL INTERFACE
DRIVER
UI
102
MICROFLUIDIC DEVICE
118
111
120
122
119
106
CHANNEL(S)
126
INLET/OUTLET CHAMBERS
124
ELECTRONIC CONTROLLER
ACTUATOR(S)
128
IO CIRCUITS
CONTROLLER
134
FILTER(S)
130
136
MEMORY
138
FW
ELECTRICAL INTERFACE
132
POWER SUPPLY
SENSOR(S)
140
142
131

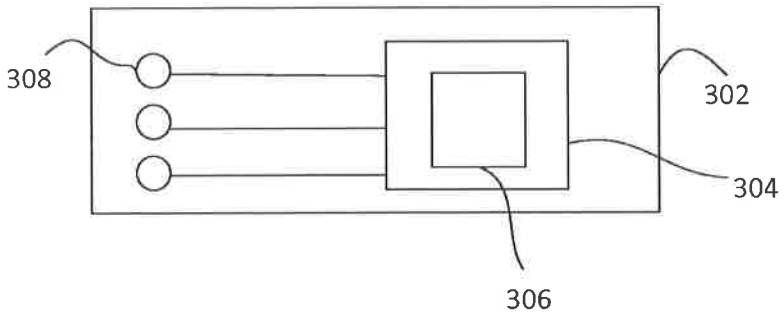
FIG. 1

100

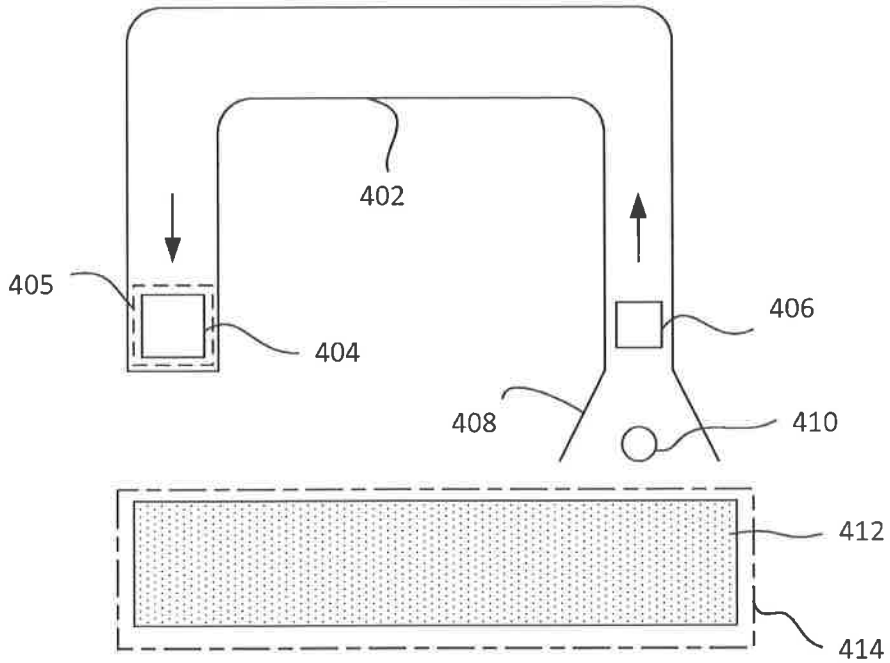

108
HOST DEVICE
204
202
FLUID RESERVOIR
104
MICROFLUIDIC DEVICE
102
ELECTRICAL INTERFACE
ELECTRONIC CONTROLLER
106
132

200

FIG. 2



300 FIG. 3



400 FIG. 4

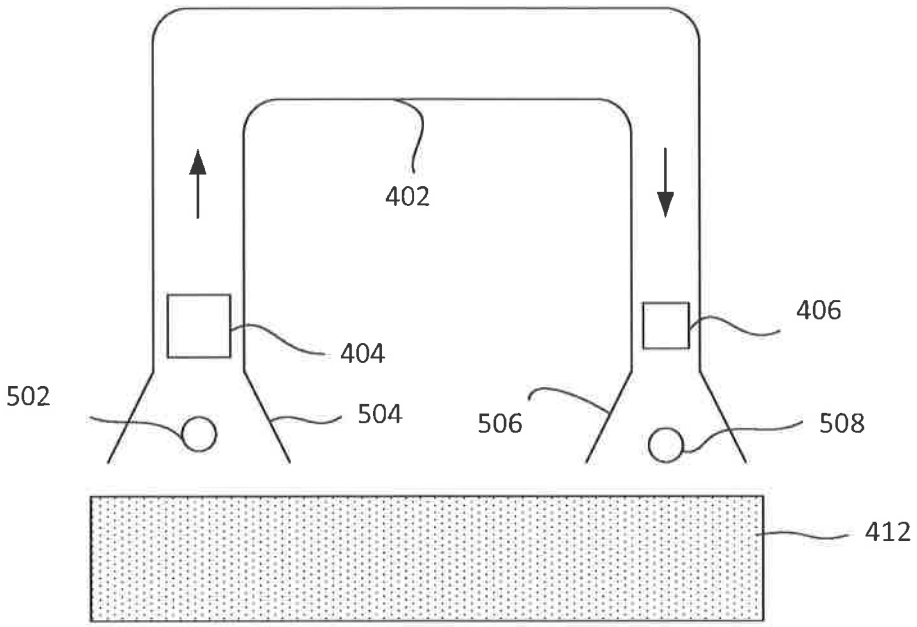
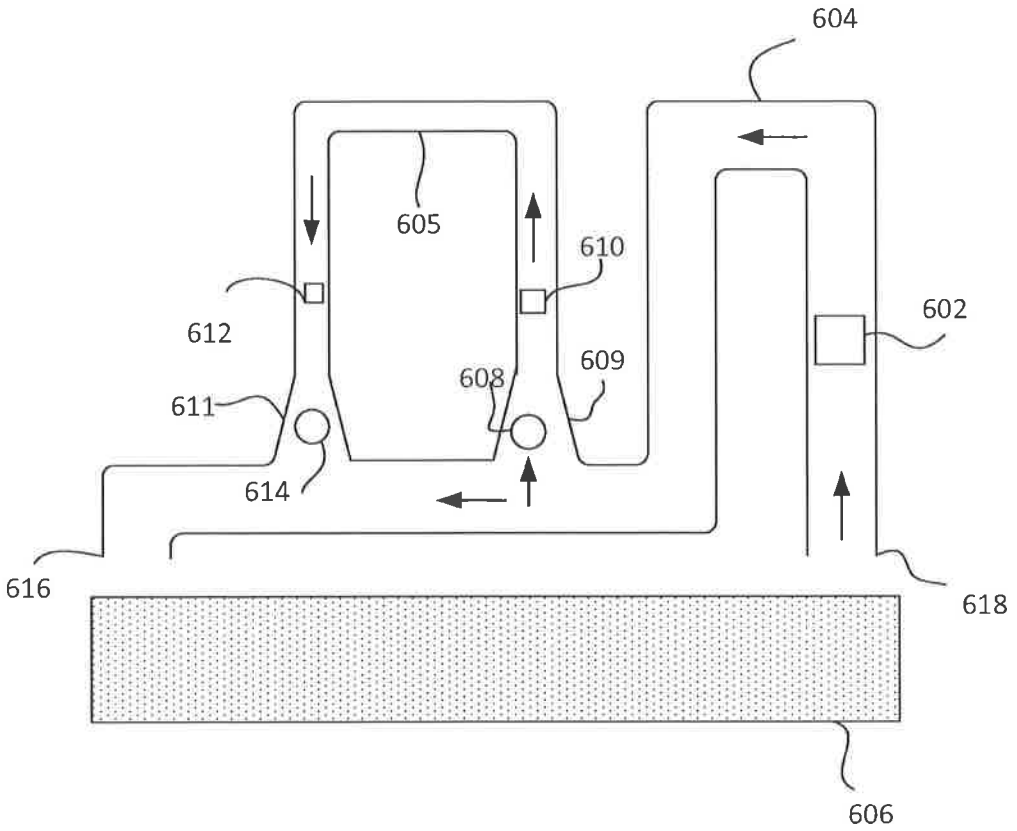


FIG. 5 500



600

FIG. 6