



- (51) **International Patent Classification:**  
*G06F 17/14* (2006.01) *G02B 27/00* (2006.01)
- (21) **International Application Number:**  
PCT/SG2023/050140
- (22) **International Filing Date:**  
07 March 2023 (07.03.2023)
- (25) **Filing Language:** English
- (26) **Publication Language:** English
- (30) **Priority Data:**  
10202202841R 21 March 2022 (21.03.2022) SG
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(81) **Designated States** (*unless otherwise indicated, for every kind of national protection available*): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BN, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CV, CZ, DE, DJ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IQ, IR, IS, IT, JM, JO, JP, KE, KG, KH, KN, KP, KR, KW, KZ, LA, LC, LK, LR, LS, LU, LY, MA, MD, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PA, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SA, SC, SD, SE, SG, SK, SL, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, WS, ZA, ZM, ZW.

(84) **Designated States** (*unless otherwise indicated, for every kind of regional protection available*): ARIPO (BW, CV, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SC, SD, SL, ST, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, ME, MK, MT, NL, NO, PL, PT, RO, RS, SE,

- (54) **Title:** A TWO-DIMENSIONAL DISCRETE FOURIER TRANSFORM HARDWARE ACCELERATOR

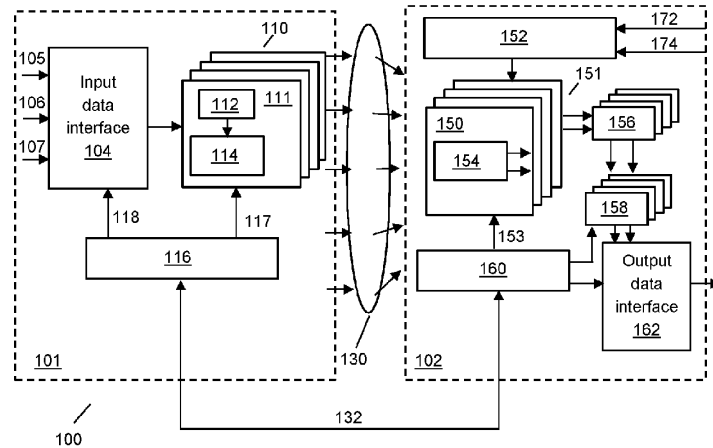


FIGURE 1

- (57) **Abstract:** This document describes a two-dimensional discrete Fourier transform (DFT) hardware accelerator comprising an ultrasonic transmitter configured to convert input signals to I/Q ultrasonic waves which are then transmitted to a lens, and an ultrasonic receiver configured to receive and convert ultrasonic waves to baseband signals, whereby the lens is provided between the transmitter and the receiver, and whereby the I/Q ultrasonic waves transmitted by the transmitter will superimpose on the lens before being received by the receiver.

SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN,  
GQ, GW, KM, ML, MR, NE, SN, TD, TG).

**Published:**

- *with international search report (Art. 21(3))*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments (Rule 48.2(h))*

**(88) Date of publication of the international search report:**

02 November 2023 (02.11.2023)