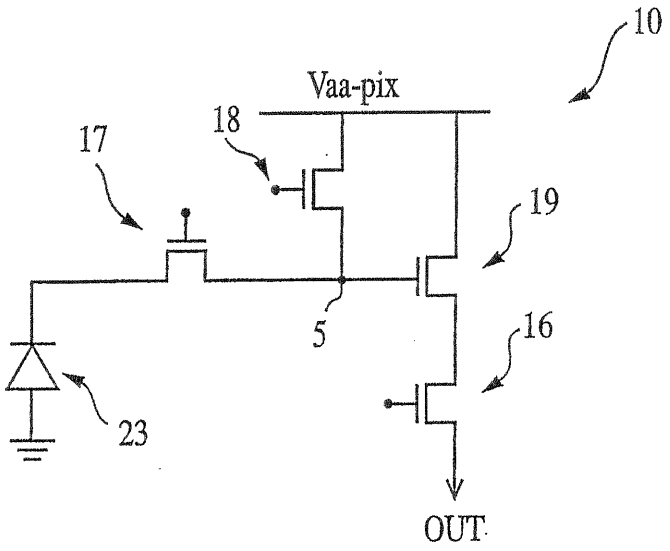


FIG. 1
(PRIOR ART)

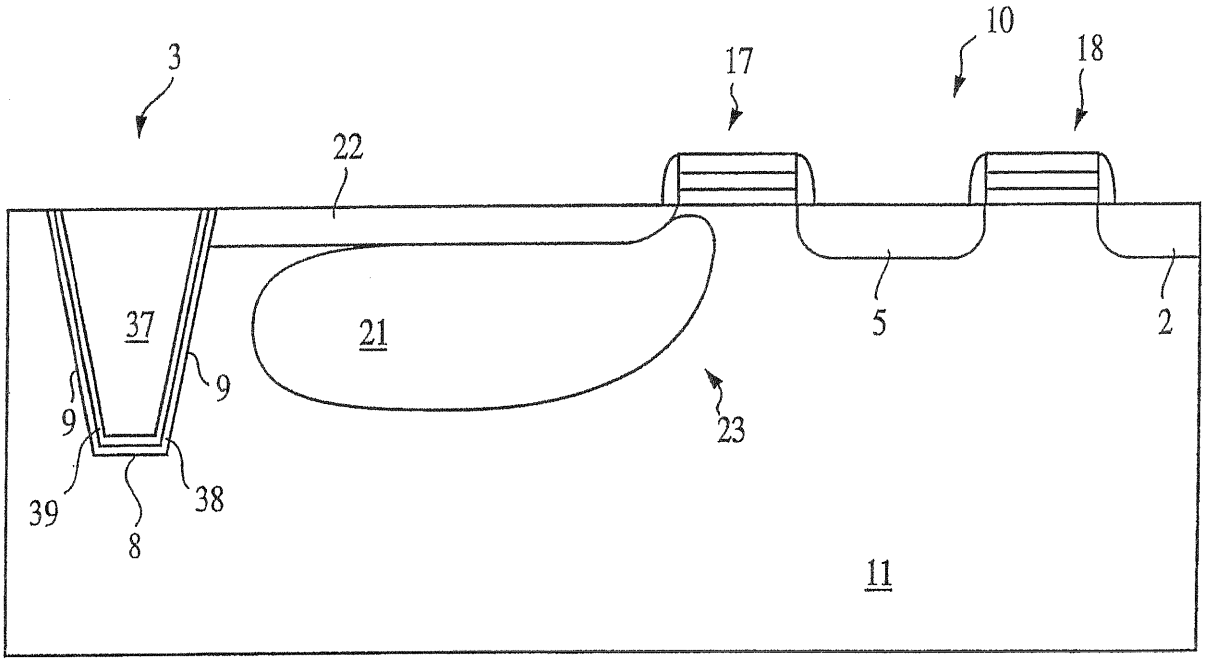


FIG. 2
(PRIOR ART)

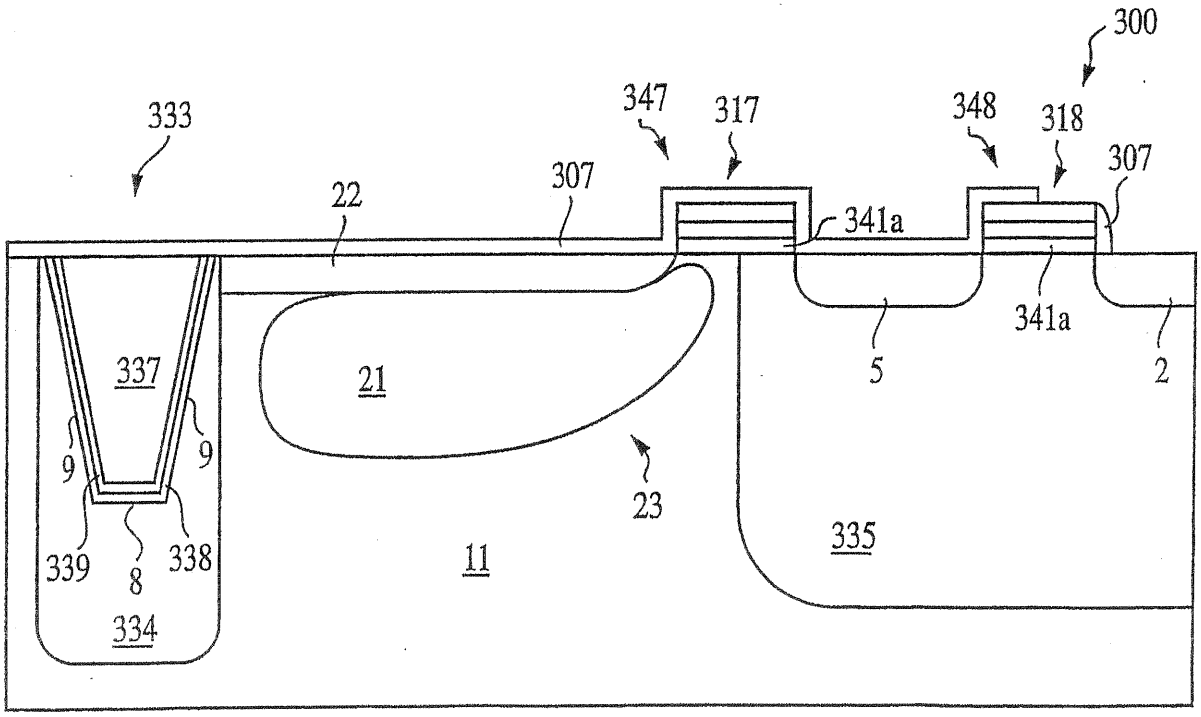


FIG. 3

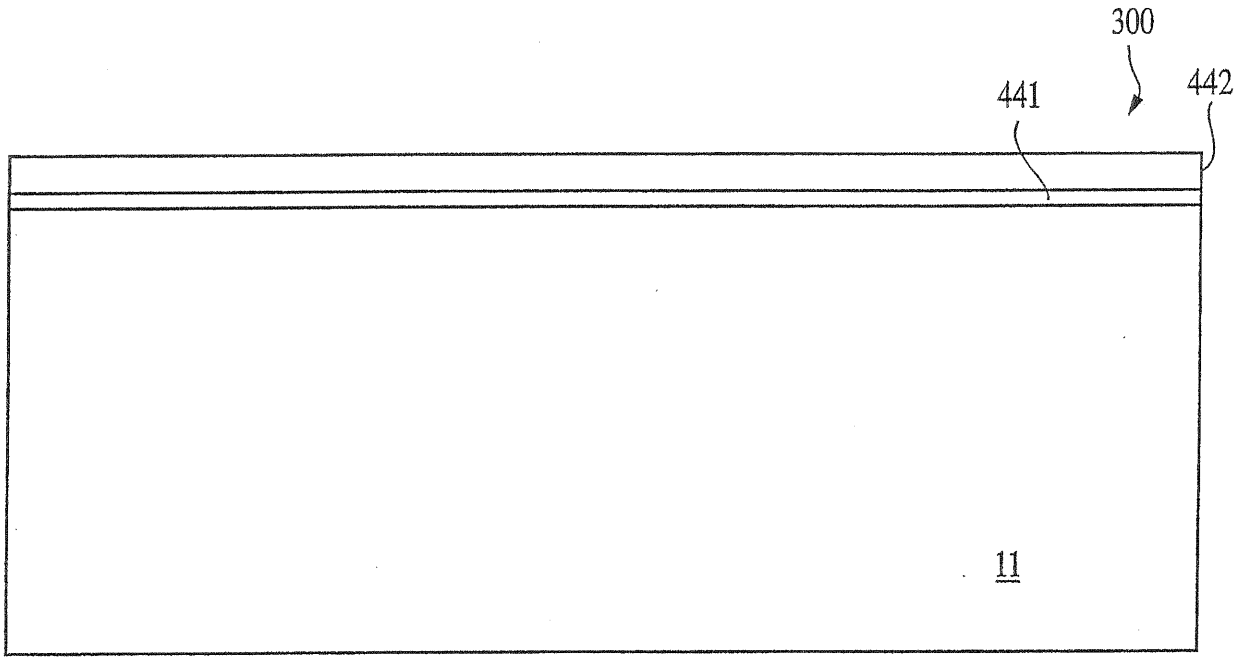


FIG. 4A

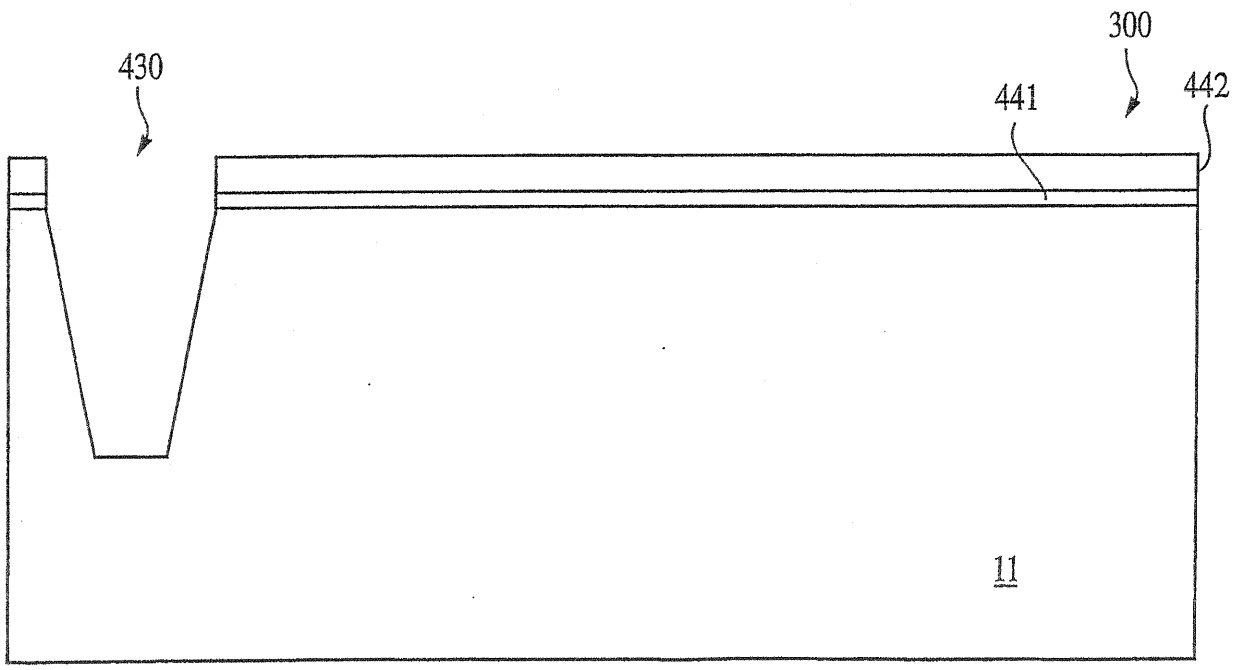


FIG. 4B

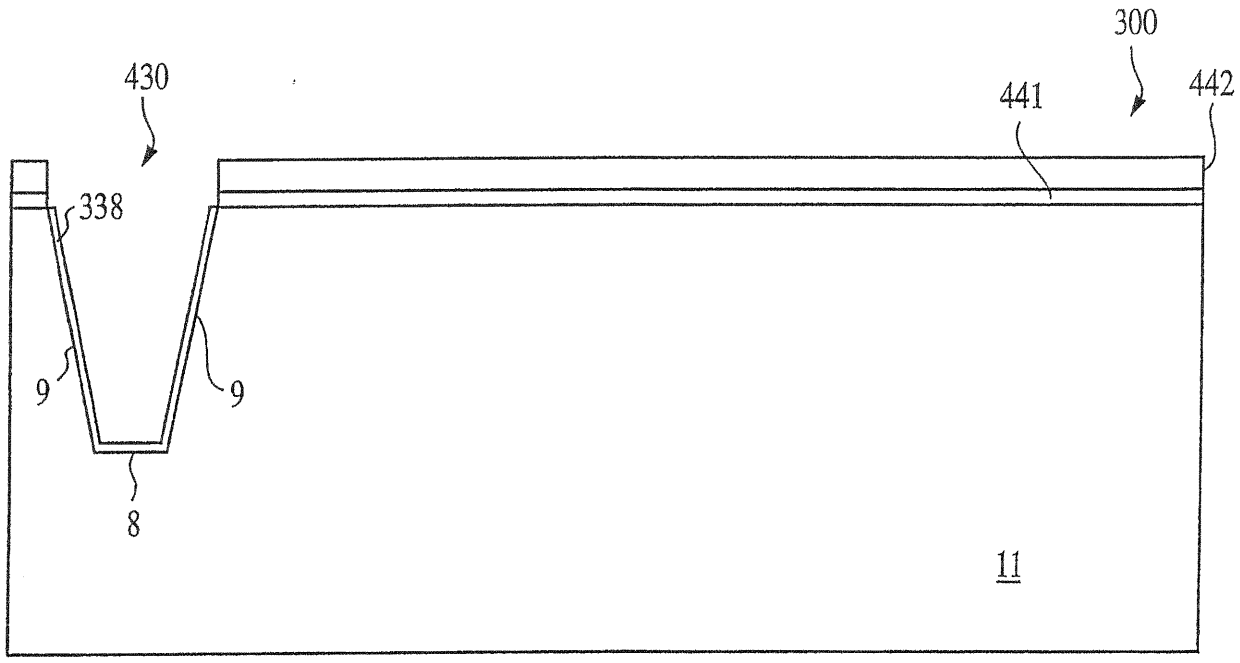


FIG. 4C

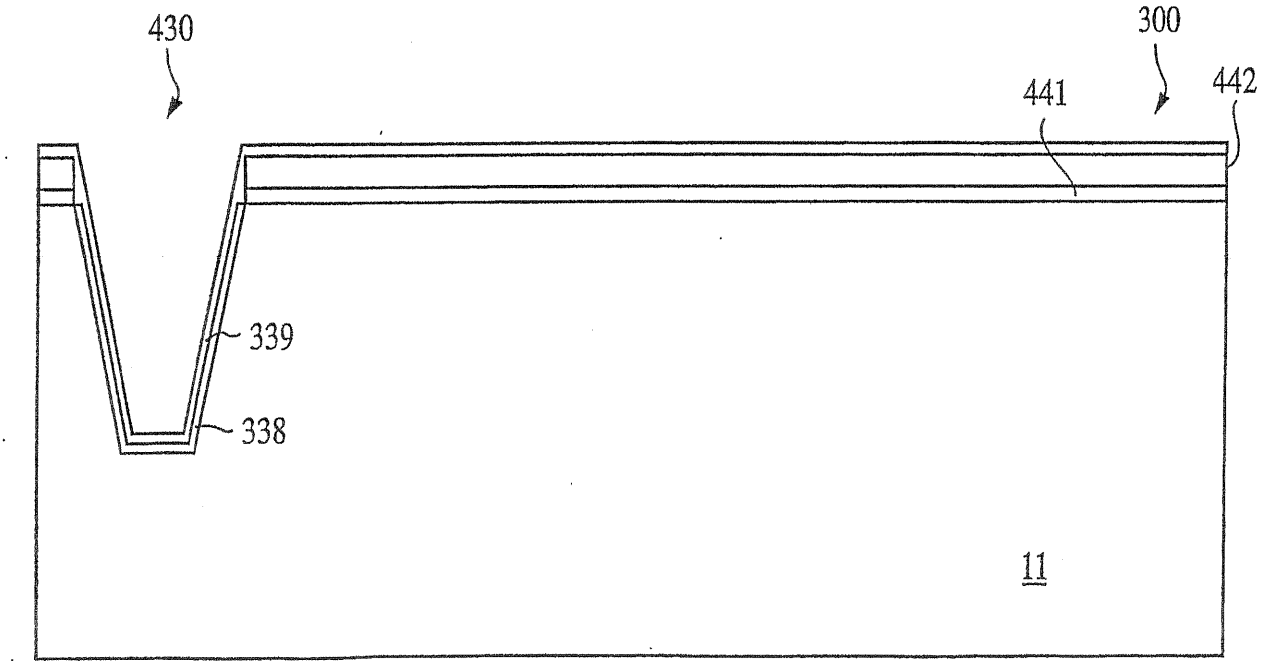


FIG. 4D

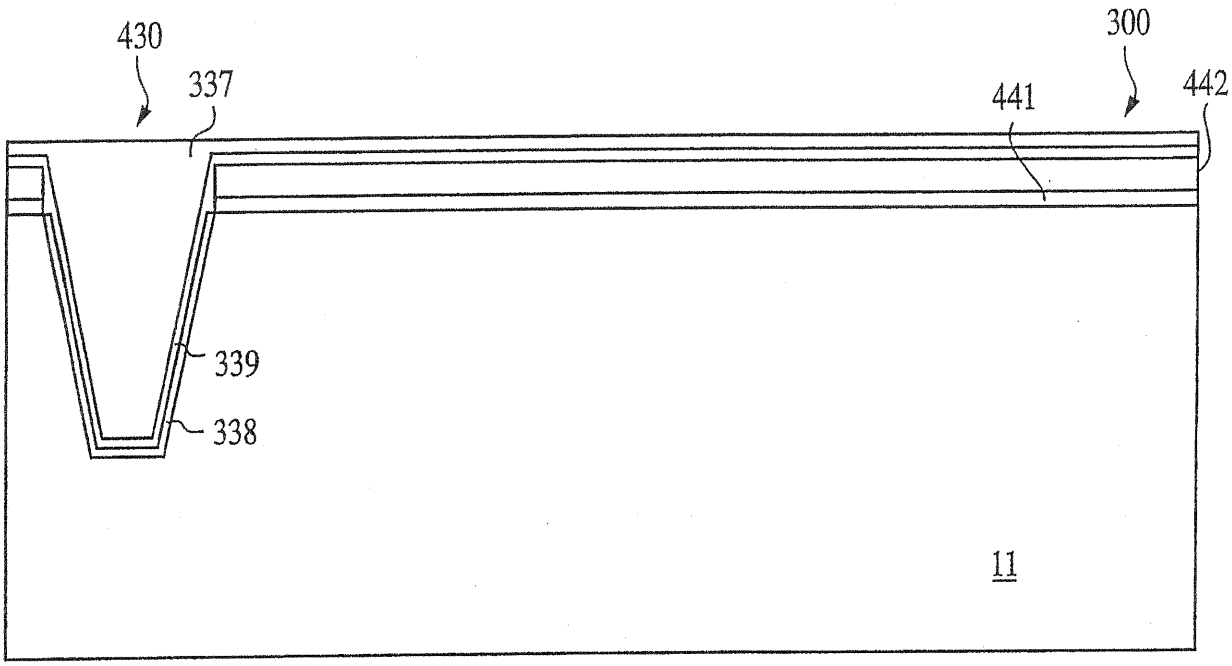


FIG. 4E

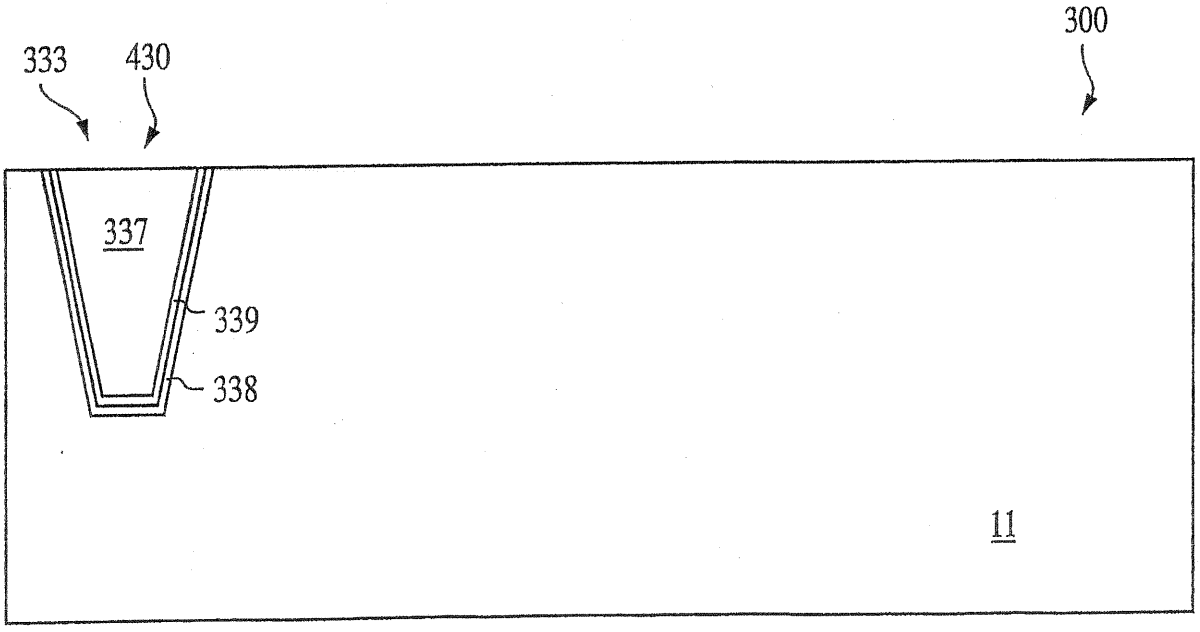


FIG. 4F

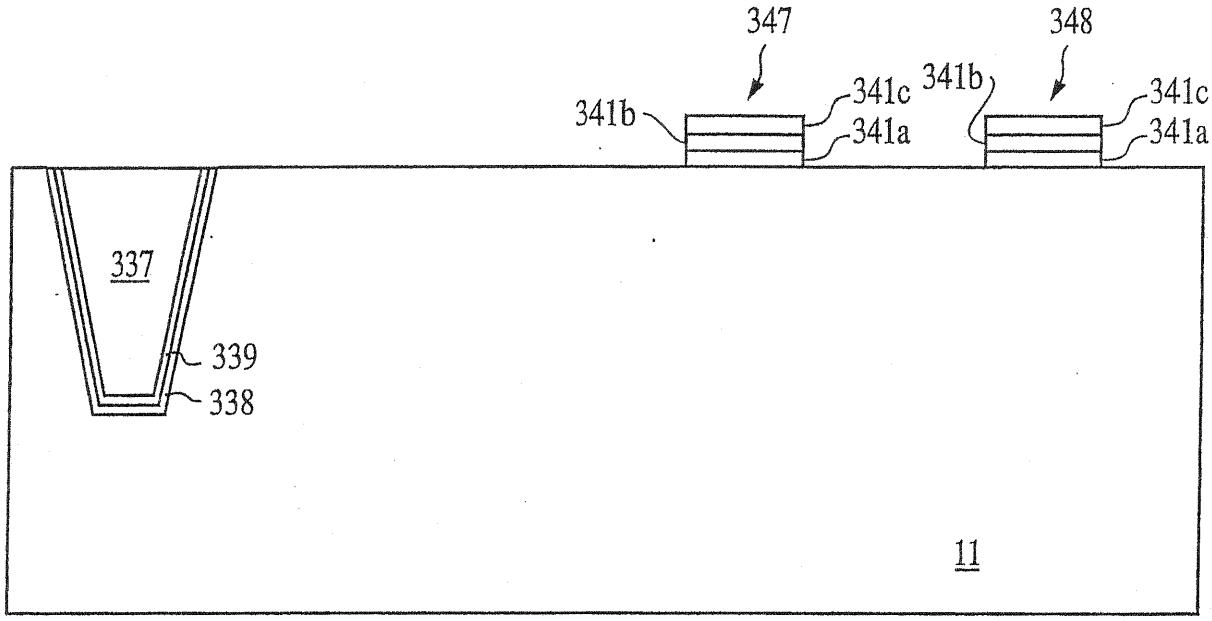

347
348
341b
341c
341a
341b
341c
341a
337
339
338
11

FIG. 4G

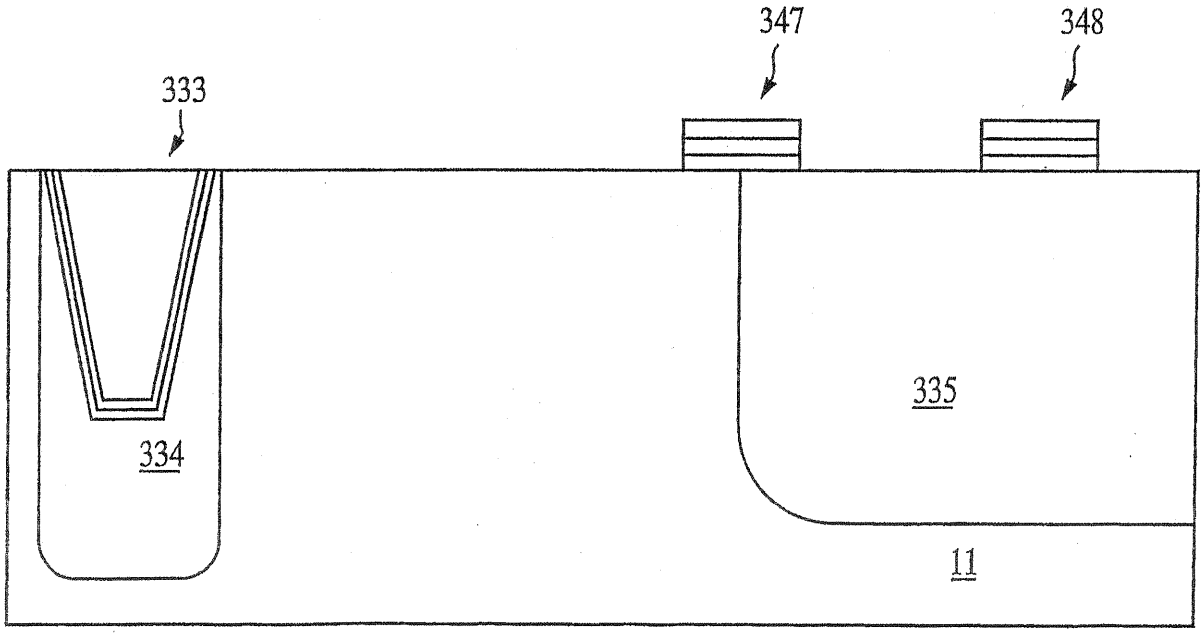

347
348
333
335
334
11

FIG. 4H

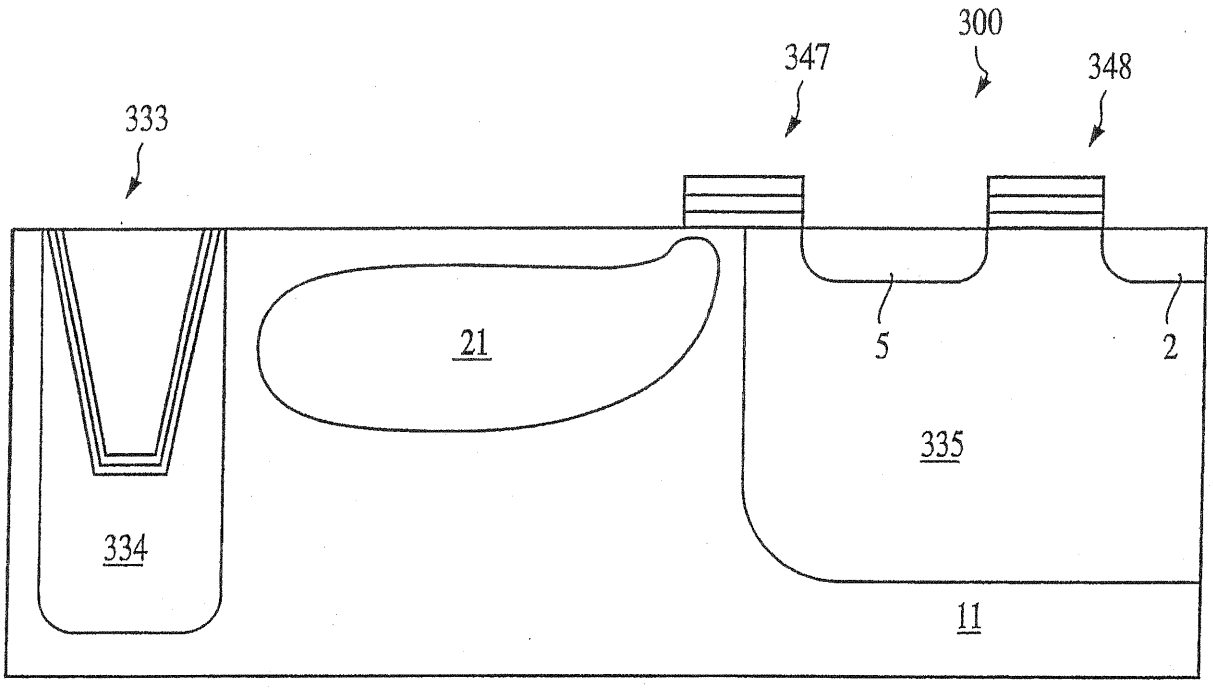


FIG. 4I

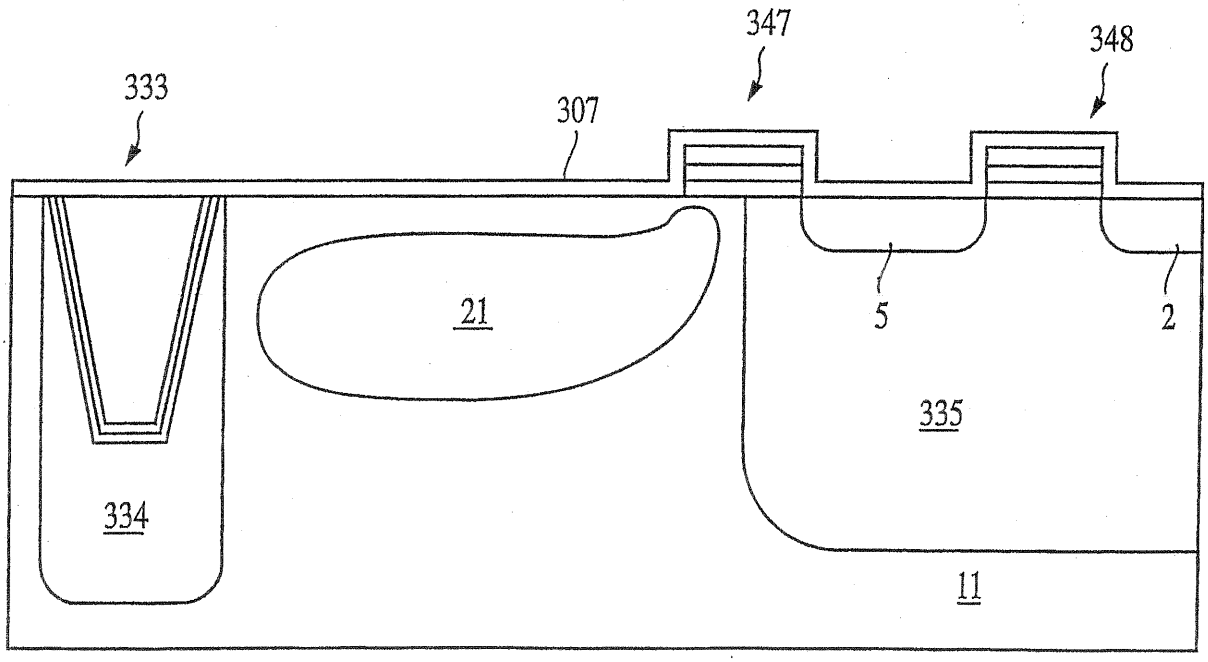


FIG. 4J

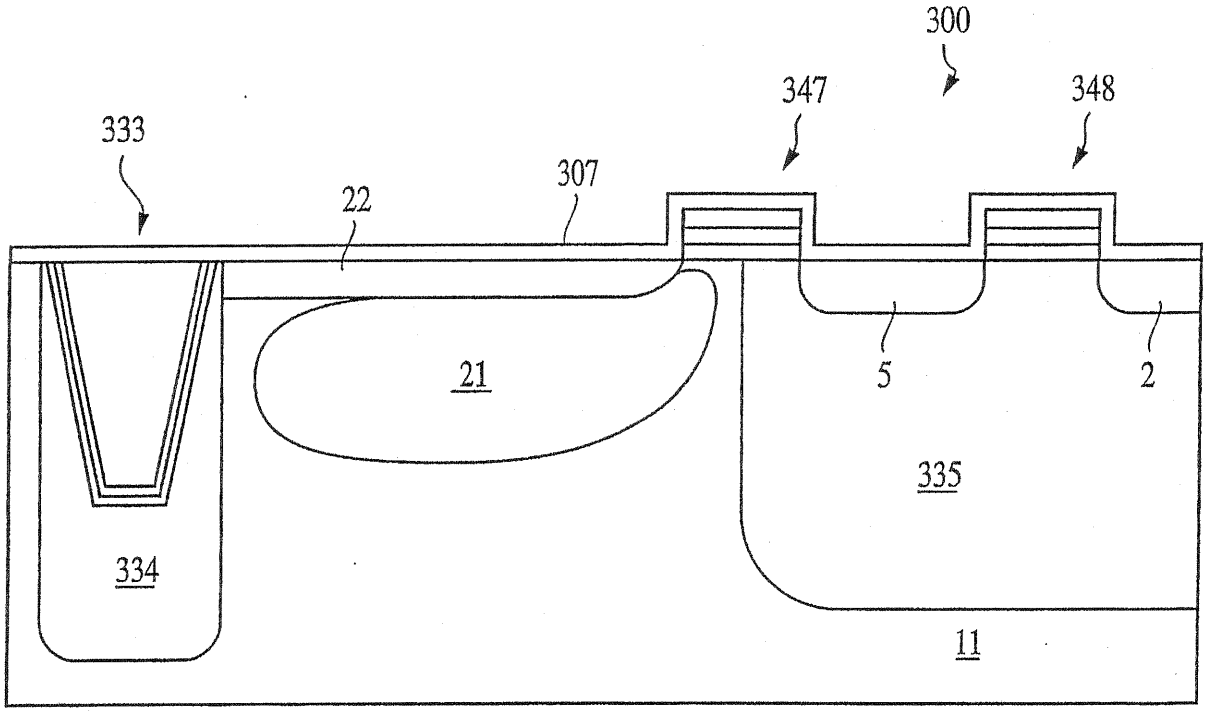


FIG. 4K

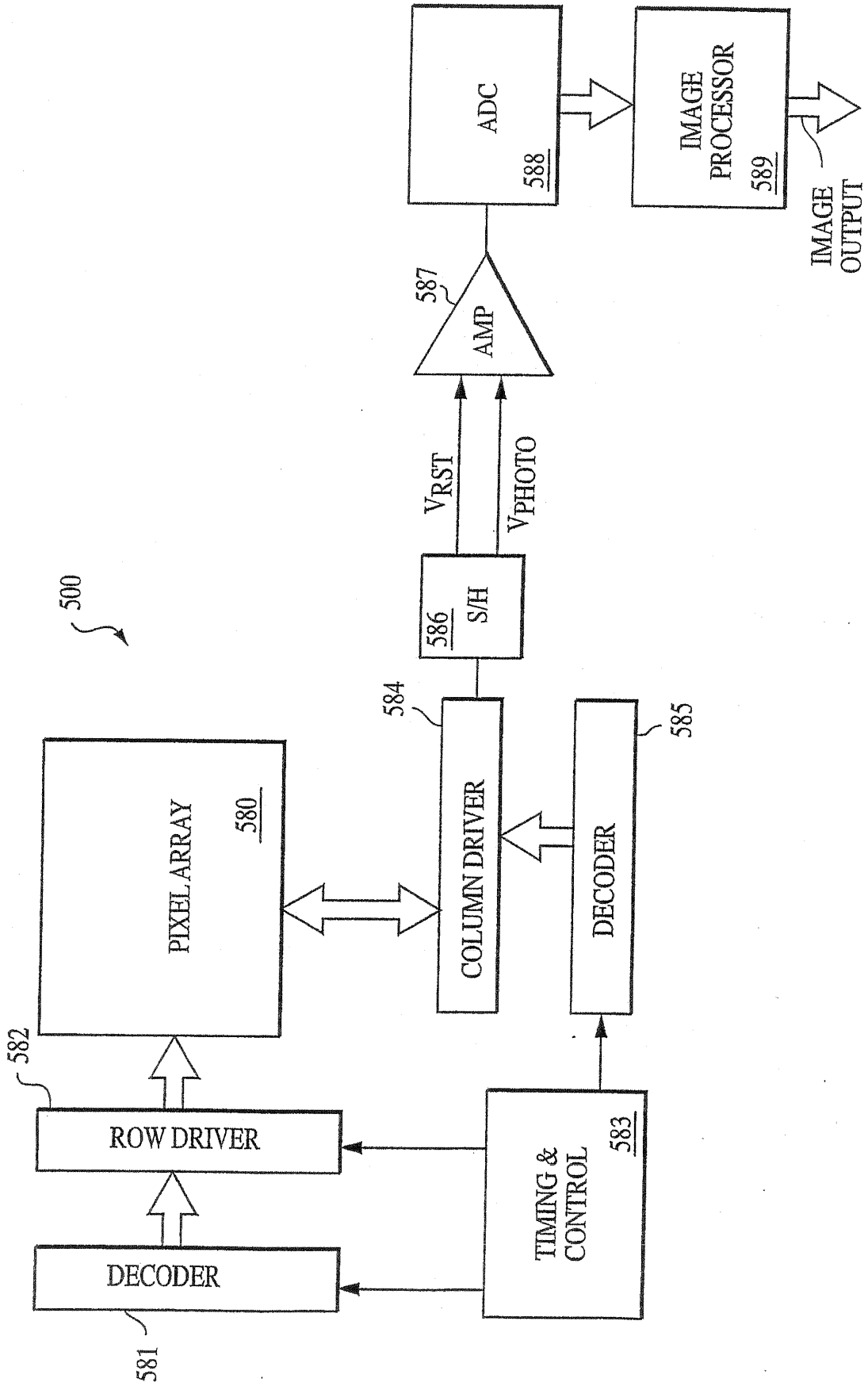

500
PIXEL ARRAY
580
582
ROW DRIVER
DECODER
581
584
COLUMN DRIVER
585
DECODER
TIMING & CONTROL
583
586
S/H
V_{RST}
V_{PHOTO}
587
AMP
ADC
588
IMAGE PROCESSOR
589
IMAGE OUTPUT

FIG. 5

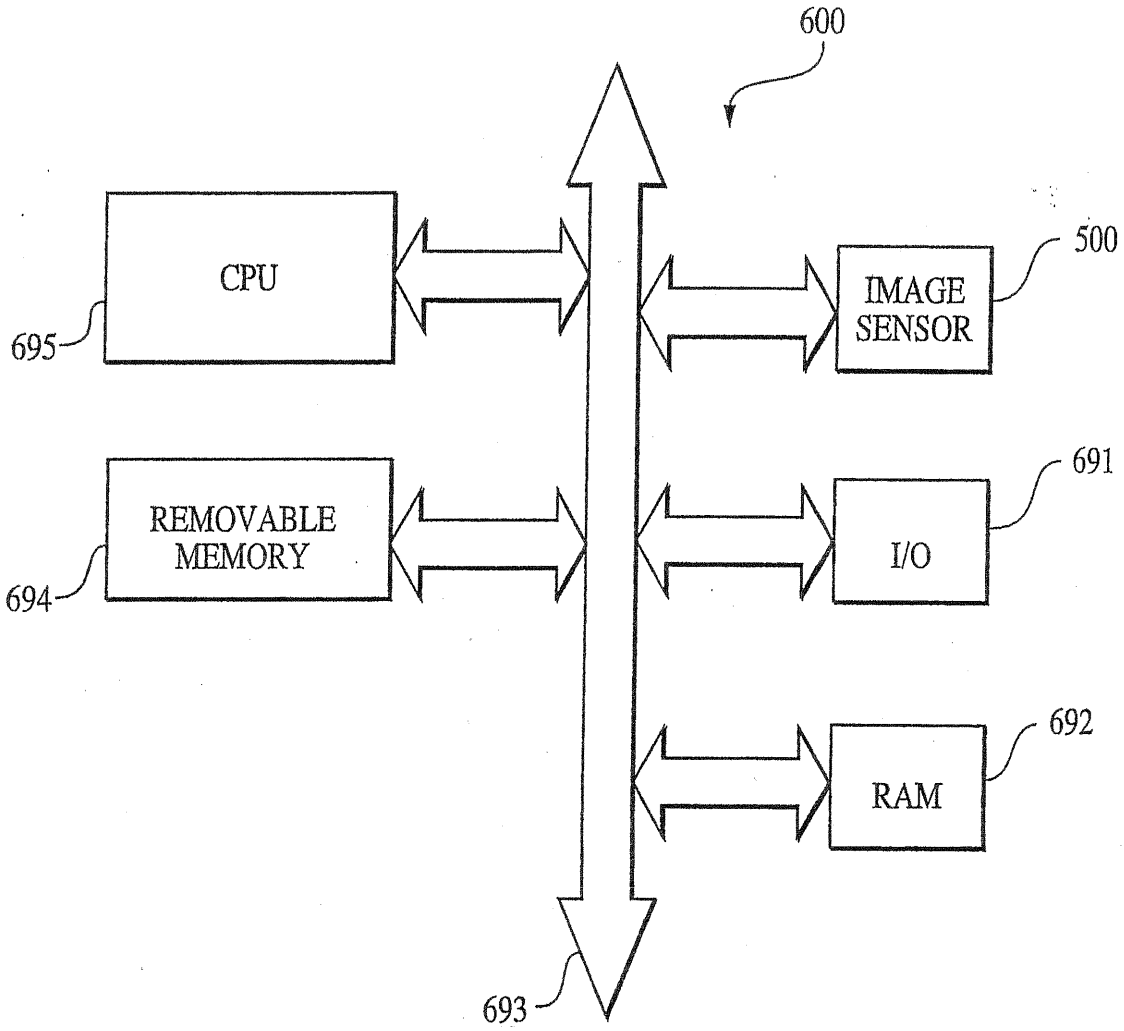

600
CPU
695
IMAGE SENSOR
500
REMOVABLE MEMORY
694
I/O
691
RAM
692
693

FIG. 6