

Abstract

Retrace pulses (F) are integrated by an R-C network (R1, C3) to produce a ramp signal (S). The resistive member of the R-C network is bypassed by a diode (CR3) to provide rapid retrace at the conclusion of the ramp portion of the integrated signal. A voltage divider (R3, R4) at the output of the integrator adjusts the DC level of the ramp signal (S) without affecting the constant of integration of the R-C network.

This Sheet Has Been Replaced

