

SYSTEM AND METHOD FOR VERIFYING INTEGRITY OF AN ELECTRONIC DEVICE**Abstract**

This document discloses a system and method for verifying system integrity of an electronic device. The electronic device includes a verifier module provided within a secure environment of the electronic device and a scanner module provided within a normal environment of the electronic device whereby the secure environment comprises hardware that is isolated from the hardware in the normal environment, i.e. these two environments are hardware isolated.

FIGURE 2