

METHOD AND TERMINAL FOR USING POWER SAVING SIGNALING PATTERN

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application is a national phase of International Application No. PCT/CN2019/074988, filed on February 13, 2019, the entire contents of which are incorporated herein by reference.

TECHNICAL FIELD

[0002] The disclosure relates to the field of communications, and more particularly, to a method and an apparatus for using a power saving signaling pattern, and a terminal.

BACKGROUND

[0003] A Wake-Up Signal (WUS) is introduced for a Narrow Band Internet of Things (NB-IoT) project and an enhanced Machine Type Communication (eMTC) project in version R15 of a Third Generation Partnership Project (3GPP).

[0004] WUS is a low-power detection signal usually placed in a signaling cycle before a time for monitoring a Physical Downlink Control Channel (PDCCH). When a UE (User Equipment) detects WUS, it is necessary to monitor PDCCH; when UE doesn't detect WUS, it may skip monitoring PDCCH. WUS may further occur in a certain cycle, and UE wakes up and monitors WUS in a fixed cycle.

SUMMARY

[0005] A method and an apparatus for using a power saving signaling pattern, and a terminal, may be provided in embodiments of the disclosure, which may solve a problem about how the power saving signaling and Discontinuous Reception (DRX) work together. The technical solutions may be as follows.

[0006] According to one aspect of the disclosure, a method for using a power saving signaling pattern is disclosed. The method includes: determining, by a terminal, a DRX timer in an operating state, the DRX timer being one of: an on duration timer with a long DRX cycle, an inactivity timer, an on duration timer with a short DRX cycle, and a retransmission

timer; determining, by the terminal, whether a pattern for monitoring the power saving signaling is used based on the DRX timer; and monitoring, by the terminal, the power saving signaling based on the pattern for monitoring the power saving signaling in response to determining that the pattern for monitoring the power saving signaling is used.

[0007] In a possible implementation, determining by the terminal whether the pattern for monitoring the power saving signaling is used based on the DRX timer, includes: determining that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle; and determining that a second pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the short DRX cycle; in which the first pattern for monitoring the power saving signaling is same with the second pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the second pattern for monitoring the power saving signaling.

[0008] In a possible implementation, determining by the terminal whether the pattern for monitoring the power saving signaling is used based on the DRX timer, includes: determining that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the on duration timer with the short DRX cycle.

[0009] In a possible implementation, determining by the terminal whether the pattern for monitoring the power saving signaling is used based on the DRX timer, includes: determining that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle; and determining that a third pattern for monitoring the power saving signaling is used in response to the DRX timer being the inactivity timer; in which the first pattern for monitoring the power saving signaling is same with the third pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the third pattern for monitoring the power saving signaling.

[0010] In a possible implementation, determining by the terminal whether the pattern for monitoring the power saving signaling is used based on the DRX timer, includes: determining that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the inactivity timer.

[0011] In a possible implementation, determining by the terminal whether the pattern for monitoring the power saving signaling is used based on the DRX timer, includes: determining that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle; and determining that a fourth pattern for monitoring the power saving signaling is used in response to the DRX timer being the retransmission timer; in which the first pattern for monitoring the power saving signaling is same with the fourth pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the fourth pattern for monitoring the power saving signaling.

[0012] In a possible implementation, determining by the terminal whether the pattern for monitoring the power saving signaling is used based on the DRX timer, includes: determining that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the retransmission timer.

[0013] In a possible implementation, a method for using the pattern for monitoring the power saving signaling is preconfigured; or a method for using the pattern for monitoring the power saving signaling is predefined.

[0014] In a possible implementation, the pattern for monitoring the power saving signaling includes a cycle of the power saving signaling; the cycle of the power saving signaling is $1/N$ times or N times of the DRX cycle; the DRX cycle is the long DRX cycle or the short DRX cycle; or the cycle of the power saving signaling is $1/N$ times or N times of the DRX on duration timer, the DRX cycle is the long DRX cycle or the short DRX cycle in which, N is an integer greater than or equal to 1.

[0015] In a possible implementation, the pattern for monitoring the power saving signaling includes a cycle of the power saving signaling; the cycle of the power saving signaling is configured by an absolute time duration.

[0016] In a possible implementation, the pattern for monitoring the power saving signaling includes a start time offset of the power saving signaling; the start time offset of the power saving signaling is an offset value configured with reference to an on duration of the DRX cycle.

[0017] According to another aspect of the disclosure, an apparatus for using a power

saving signaling pattern is disclosed. The apparatus includes: a processing module, configured to determine a DRX timer in an operating state, the DRX timer being one of: an on duration timer with a long DRX cycle, an inactivity timer, an on duration timer with a short DRX cycle, and a retransmission timer; the processing module, configured to determine whether a pattern for monitoring the power saving signaling is used based on the DRX timer; and a receiving module, configured to monitor the power saving signaling based on the pattern for monitoring the power saving signaling in response to determining that the pattern for monitoring the power saving signaling is used.

[0018] In a possible implementation, the processing module is configured to, determine that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle; and the processing module is configured to, determine that a second pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the short DRX cycle; in which the first pattern for monitoring the power saving signaling is same with the second pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the second pattern for monitoring the power saving signaling.

[0019] In a possible implementation, the processing module is configured to, determine that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the on duration timer with the short DRX cycle.

[0020] In a possible implementation, the processing module is configured to, determine that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle; and the processing module is configured to, determine that a third pattern for monitoring the power saving signaling is used in response to the DRX timer being the inactivity timer; in which the first pattern for monitoring the power saving signaling is same with the third pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the third pattern for monitoring the power saving signaling.

[0021] In a possible implementation, the processing module is configured to, determine that the pattern for monitoring the power saving signaling is not used in response

to the DRX timer being the inactivity timer.

[0022] In a possible implementation, the processing module is configured to, determine that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle; and the processing module is configured to, determine that a fourth pattern for monitoring the power saving signaling is used in response to the DRX timer being the retransmission timer; in which the first pattern for monitoring the power saving signaling is same with the fourth pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the fourth pattern for monitoring the power saving signaling.

[0023] In a possible implementation, the processing module is configured to, determine that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the retransmission timer.

[0024] In a possible implementation, a method for using the pattern for monitoring the power saving signaling is preconfigured; or a method for using the pattern for monitoring the power saving signaling is predefined.

[0025] In a possible implementation, the pattern for monitoring the power saving signaling includes a cycle of the power saving signaling; the cycle of the power saving signaling is $1/N$ times or N times of the DRX cycle; the DRX cycle is the long DRX cycle or the short DRX cycle; or, the cycle of the power saving signaling is $1/N$ times or N times of the DRX on duration timer, the DRX cycle is the long DRX cycle or the short DRX cycle; in which N is an integer greater than or equal to 1.

[0026] In a possible implementation, the pattern for monitoring the power saving signaling includes a cycle of the power saving signaling; the cycle of the power saving signaling is configured by an absolute time duration.

[0027] In a possible implementation, the pattern for monitoring the power saving signaling includes a start time offset of the power saving signaling; the start time offset of the power saving signaling is an offset value configured with reference to an on duration of the DRX cycle.

[0028] According to another aspect of the disclosure, a terminal is disclosed. The terminal includes: a processor; a transceiver coupled to the processor; and a memory

configured to store instructions executable by the processor. The processor is configured to load and execute the executable instructions to implement the method for using the power saving signaling pattern as described in the first aspect.

[0029] According to another aspect of the disclosure, a computer readable storage medium is disclosed. The computer readable storage medium stores at least one instruction, at least one program, a code set, or an instruction set. The at least one instruction, the at least one program, the code set, or the instruction set is loaded and executed by a processor to implement the method for using the power saving signaling pattern as described in the first aspect.

[0030] It should be noted that, the power saving signaling may be referred to as an energy-saving signaling or a wake up signaling. The power saving signaling pattern may be referred to as a power saving signaling mode, an energy-saving signaling pattern, an energy-saving signaling mode, a wake up signaling pattern, and a wake up signaling mode.

[0031] The technical solutions provided in embodiments of the disclosure may at least include the following beneficial effect.

[0032] It may be determined whether to use the power saving signaling pattern based on the DRX timer. When the power saving signaling pattern is used, the power saving signaling pattern may be used for monitoring. Different methods for using the power saving signaling may be selected based on types of different DRX timers, for example, using a power saving signaling pattern with a high monitoring density, using a power saving signaling pattern with a low monitoring density, not using the power saving signaling pattern in a scenario with low delay requirements. Therefore, the problem about how the power saving signaling and the DRX work together may be solved.

BRIEF DESCRIPTION OF THE DRAWINGS

[0033] The accompanying drawings herein, which are incorporated in and constitute a part of this specification, illustrate embodiments consistent with the disclosure and, together with the description, serve to explain the principles of the disclosure.

[0034] FIG. 1 is a schematic diagram illustrating a structure of a communications system according to an example embodiment in the disclosure.

[0035] FIG. 2 is a schematic diagram illustrating a DRX cycle related in the disclosure.

[0036] FIG. 3 is a schematic diagram illustrating a long cycle and a short cycle of a DRX related in the disclosure.

[0037] FIG. 4 is a flowchart illustrating a method for using a power saving signaling pattern according to an example embodiment.

[0038] FIG. 5 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment.

[0039] FIG. 6 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment.

[0040] FIG. 7 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment.

[0041] FIG. 8 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment.

[0042] FIG. 9 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment.

[0043] FIG. 10 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment;

[0044] FIG. 11 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment.

[0045] FIG. 12 is a block diagram illustrating an apparatus for using a power saving signaling pattern according to an example embodiment.

[0046] FIG. 13 is a block diagram illustrating a terminal according to an example embodiment.

DETAILED DESCRIPTION

[0047] Reference will now be made in detail to exemplary embodiments, examples of which are illustrated in the accompanying drawings. The following description refers to the accompanying drawings in which the same numbers in different drawings represent the same or similar elements unless otherwise represented. The implementations set forth in the

following description of exemplary embodiments do not represent all implementations consistent with the disclosure. Instead, they are merely examples of apparatuses and methods consistent with aspects related to the disclosure as recited in the appended claims.

[0048] FIG. 1 is a schematic diagram illustrating a structure of a communications system according to an example embodiment in the disclosure. As illustrated in FIG. 1, the communications system may include an access network 12 and a terminal 13.

[0049] The access network 12 includes several access network devices 120. The access network device 120 and the core network device 110 (not shown) communicate with each other through some kind of interface technology, for example, an S1 interface in the LTE (Long Term Evolution) system, an NG (New Generation) interface in the 5G NR (New Radio) system. The access network device 120 may be a base station (BS), in which the base station is an apparatus deployed in the access network to provide wireless communication functions for terminals. The base station may include various forms of macro base stations, micro base stations, relay stations, access points, etc. In the systems adopting different wireless access technologies, names of the devices with the base station function may vary. For example, in the LTE system, it is referred to as eNodeB or eNB; in the 5G NR system, it is referred to as gNodeB or gNB. With the evolution of communications technologies, the name of “base station” may vary. For convenience of description, in embodiments of the disclosure, the above apparatus that provides wireless communication functions for the terminals is collectively referred to as the access network device.

[0050] The terminal 13 may include all kinds of handheld devices, vehicle devices, wearable devices, and computing devices, with wireless communication function, or other processing devices coupled to wireless modems, as well as various forms of user equipment (UE), mobile stations (MSs), terminal devices, etc. For convenience of description, the device mentioned above is collectively referred to as the terminal. The access network device 120 and the terminal 13 communicate with each other through some kind of air interface technology, for example, a Uu interface.

[0051] DRX may make UE, for example, terminal 13, enter an Opportunity for DRX (a sleep mode) periodically at some time points without monitoring scheduling information of PDCCH (a subframe of PDCCH). When UE needs to monitor the scheduling information of

PDCCH, UE wakes up from the Opportunity for DRX, so that UE may achieve a purpose of power saving.

[0052] The basic mechanism of DRX is to configure a DRX cycle for UE in an RRC_CONNECTED state. The DRX cycle includes an on duration and an opportunity for DRX: in the on duration, UE monitors and receives scheduling information of PDCCH; in the opportunity for DRX, UE does not receive data of a downlink channel to save power consumption. As illustrated in FIG. 2, in a time domain, there are continuous DRX cycles provided. When UE receives a scheduling message during the on duration, UE may start a DRX-inactivity timer and monitor scheduling information of PDCCH in each subframe of the on duration; when the DRX-inactivity timer is running, even if the originally-configured on duration has ended, UE still needs to continue monitoring the downlink subframe of PDCCH till the DRX-inactivity timer times out.

[0053] The DRX cycle equals to a sum of a wake-up duration and an Opportunity for DRX duration of UE, in which, the wake up duration is a duration of the on duration in the cycle, and the Opportunity for DRX duration is a duration of the Opportunity for DRX in the cycle. In the communications system, the system may configure a short DRX cycle as illustrated in FIG. 2 or a long DRX cycle as illustrated in FIG. 3 for UE according to different scenarios. The Opportunity for DRX duration of the long DRX cycle is longer than the Opportunity for DRX duration of the short DRX cycle, or a proportion of the Opportunity for DRX duration of the long DRX cycle is larger than a proportion of the Opportunity for DRX duration of the short DRX cycle.

[0054] There is no suitable solution when the power saving signaling and DRX work together.

[0055] In a possible implementation in the related art, in a scenario where DRX is configured, the power saving signaling is usually configured before the DRX on duration. If UE does not detect the power saving signaling, it needs to skip the entire active state. In this way, it is equivalent that UE may use the pattern for monitoring the power saving signaling before entering the active state duration. In this case, the subsequent inactive state and the short period may not be turned on, which may cause a larger delay.

[0056] The disclosure provides a solution to how the power saving signaling and

DRX work together. That is, after UE enters the on duration, it is determined whether to use the pattern for monitoring the power saving signaling during different DRX timer operation periods (an on duration timer with a long DRX cycle, an inactivity timer, an on duration timer with a short DRX cycle, and a retransmission timer) and which pattern for monitoring the power saving signaling may be used.

[0057] A power saving signaling, referred to as an energy-saving signaling, includes a Wake-Up Signal (WUS). The following takes WUS for an example for further description. However, the power saving signaling may be not limited to WUS, and may be a ZC (Zadoff-chu) sequence, a predetermined bit in downlink control information (DCI), and other possible implementations.

[0058] FIG. 4 is a flowchart illustrating a method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[0059] At block 101, the terminal determines a DRX timer in an operating state.

[0060] The DRX timer is a timer related to a DRX mechanism.

[0061] The type of the DRX timer determined by the terminal may include any one of: an on duration timer with a long DRX cycle, a DRX-inactivity timer, an on duration timer with a short DRX cycle, and a retransmission timer.

[0062] At block 102, the terminal determines whether a pattern for monitoring the power saving signaling is used based on the DRX timer. The embodiments take using a pattern for monitoring WUS as an example.

[0063] The terminal determines whether to perform a monitoring pattern using WUS based on the type of the DRX timer.

[0064] At block 103, the terminal monitors WUS based on the pattern for monitoring WUS in response to determining that the pattern for monitoring WUS is used.

[0065] The terminal determines whether to use the pattern for monitoring WUS based on different states and which pattern for monitoring WUS is used when determining to use the pattern for monitoring WUS.

[0066] The pattern for monitoring WUS includes a cycle of WUS and a start time offset of WUS.

[0067] In summary, according to the method provided in the embodiments, it may be determined whether to use the power saving signaling pattern based on the DRX timer. When the power saving signaling pattern is used, the power saving signaling pattern may be used for monitoring. Different methods for using the power saving signaling may be selected based on types of different DRX timers, for example, using a power saving signaling pattern with a high monitoring density, using a power saving signaling pattern with a low monitoring density, not using the power saving signaling pattern in a scenario with low delay requirements. Therefore, the problem about how the power saving signaling and the DRX work together may be solved.

[0068] FIG. 5 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[0069] At block 201, the terminal determines a DRX timer in an operating state.

[0070] In some embodiments, the DRX timer may include the on duration timer with the long DRX cycle or the on duration timer with the short DRX cycle.

[0071] When the DRX timer is the on duration timer with the long DRX cycle, an action in block 202 is executed.

[0072] When the DRX timer is the on duration timer with the short DRX cycle, an action in block 203 is executed.

[0073] At block 202, it is determined that a first pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[0074] At block 203, it is determined that a second pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the short DRX cycle.

[0075] The first pattern for monitoring WUS is same with the second pattern for monitoring WUS, or the first pattern for monitoring WUS is different from the second pattern for monitoring WUS. The monitoring pattern is not limited in some embodiments.

[0076] Optionally, when the different first pattern for monitoring WUS and second pattern for monitoring WUS are used, a time delay of the second pattern for monitoring WUS is less than a time delay of the first pattern for monitoring WUS. Or, a monitoring granularity

of the second pattern for monitoring WUS is higher than a monitoring granularity of the first pattern for monitoring WUS.

[0077] In summary, in the method provided in some embodiments, the terminal determines which pattern for monitoring WUS is used based on the type of the DRX timer, that is, different patterns for monitoring WUS may be used or the same pattern for monitoring WUS may be used when the DRX timer is in different operating states.

[0078] When different patterns for monitoring WUS are used, the time delay of the second pattern for monitoring WUS is less than the time delay of the first pattern for monitoring WUS. Therefore, the terminal reduces the influence on data transmission due to the time delay caused by WUS under the short DRX cycle.

[0079] FIG. 6 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[0080] At block 301, the terminal determines a DRX timer in an operating state.

[0081] The DRX timer may include the on duration timer with the long DRX cycle or the on duration timer with the short DRX cycle.

[0082] When the DRX timer is the on duration timer with the long DRX cycle, an action in block 302 is executed.

[0083] When the DRX timer is the on duration timer with the short DRX cycle, an action in block 303 is executed.

[0084] At block 302, it is determined that the pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[0085] At block 303, it is determined that the pattern for monitoring WUS is not used in response to the DRX timer being the on duration timer with the short DRX cycle.

[0086] In summary, in the method provided in some embodiments, the terminal determines whether the pattern for monitoring WUS is used based on the type of the DRX timer. When the DRX timer is the on duration timer with the short cycle, it is determined that the pattern for monitoring WUS is not used. Therefore, the terminal avoids the influence on data transmission due to the time delay caused by WUS under the short DRX cycle.

[0087] FIG. 7 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[0088] At block 401, the terminal determines a DRX timer in an operating state.

[0089] The DRX timer may include the on duration timer with the long DRX cycle or the inactivity timer.

[0090] When the DRX timer is the on duration timer with the long DRX cycle, an action in block 402 is executed.

[0091] When the DRX timer is the inactivity timer, an action in block 403 is executed.

[0092] At block 402, it is determined that a first pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[0093] At block 403, it is determined that a third pattern for monitoring WUS is used in response to the DRX timer being the inactivity timer.

[0094] The first pattern for monitoring WUS is same with the third pattern for monitoring WUS, or the first pattern for monitoring WUS is different from the third pattern for monitoring WUS. The monitoring pattern is not limited in some embodiments.

[0095] Optionally, when the different first pattern for monitoring WUS and third pattern for monitoring WUS are used, a time delay of the third pattern for monitoring WUS is less than a time delay of the first pattern for monitoring WUS. Or, a monitoring granularity of the third pattern for monitoring WUS is higher than a monitoring granularity of the first pattern for monitoring WUS.

[0096] In summary, in the method provided in some embodiments, the terminal determines which pattern for monitoring WUS is used based on the type of the DRX timer, that is, different patterns for monitoring WUS may be used or the same pattern for monitoring WUS may be used when the DRX timer is in different operating states.

[0097] When different patterns for monitoring WUS are used, the time delay of the third pattern for monitoring WUS is less than the time delay of the first pattern for monitoring WUS. Therefore, the terminal reduces the influence on data transmission due to the time

delay caused by WUS under the inactivity timer.

[0098] FIG. 8 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[0099] At block 501, the terminal determines a DRX timer in an operating state.

[00100] The DRX timer may include the on duration timer with the long DRX cycle or the inactivity timer.

[00101] When the DRX timer is the on duration timer with the long DRX cycle, an action in block 502 is executed.

[00102] When the DRX timer is the inactivity timer, an action in block 503 is executed.

[00103] At block 502, it is determined that the pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00104] At block 503, it is determined that the pattern for monitoring WUS is not used in response to the DRX timer being the inactivity timer.

[00105] In summary, in the method provided in some embodiments, the terminal determines whether the pattern for monitoring WUS is used based on the type of the DRX timer. When the DRX timer is the inactivity timer, it is determined that the pattern for monitoring WUS is not used. Therefore, the terminal avoids the influence on data transmission due to the time delay caused by WUS under the inactivity timer.

[00106] FIG. 9 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[00107] At block 601, the terminal determines a DRX timer in an operating state.

[00108] The DRX timer may include the on duration timer with the long DRX cycle or the retransmission timer.

[00109] When the DRX timer is the on duration timer with the long DRX cycle, an action in block 602 is executed.

[00110] When the DRX timer is the retransmission timer, an action in block 603 is executed.

[00111] At block 602, it is determined that a first pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00112] At block 603, it is determined that a fourth pattern for monitoring WUS is used in response to the DRX timer being the retransmission timer.

[00113] The first pattern for monitoring WUS is same with the fourth pattern for monitoring WUS, or the first pattern for monitoring WUS is different from the fourth pattern for monitoring WUS. The monitoring pattern is not limited in some embodiments.

[00114] Optionally, when the different first pattern for monitoring WUS and fourth pattern for monitoring WUS are used, a time delay of the fourth pattern for monitoring WUS is less than a time delay of the first pattern for monitoring WUS. Or, a monitoring granularity of the fourth pattern for monitoring WUS is higher than a monitoring granularity of the first pattern for monitoring WUS.

[00115] In summary, in the method provided in some embodiments, the terminal determines which pattern for monitoring WUS is used based on the type of the DRX timer, that is, different patterns for monitoring WUS may be used or the same pattern for monitoring WUS may be used when the DRX timer is in different operating states.

[00116] When different patterns for monitoring WUS are used, the time delay of the fourth pattern for monitoring WUS is less than the time delay of the first pattern for monitoring WUS. Therefore, the terminal reduces the influence on data transmission due to the time delay caused by WUS under the retransmission timer.

[00117] FIG. 10 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[00118] At block 701, the terminal determines a DRX timer in an operating state.

[00119] The DRX timer may include the on duration timer with the long DRX cycle or the retransmission timer.

[00120] When the DRX timer is the on duration timer with the long DRX cycle, an

action in block 402 is executed.

[00121] When the DRX timer is the retransmission timer, an action in block 403 is executed.

[00122] At block 702, it is determined that the pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00123] At block 703, it is determined that the pattern for monitoring WUS is not used in response to the DRX timer being the retransmission timer.

[00124] In summary, in the method provided in some embodiments, the terminal determines whether the pattern for monitoring WUS is used based on the type of the DRX timer. When the DRX timer is the retransmission timer, it is determined that the pattern for monitoring WUS is not used. Therefore, the terminal avoids the influence on data transmission due to the time delay caused by WUS under the retransmission timer.

[00125] The foregoing embodiments may also be freely combined, and the embodiments of the disclosure do not limit the combination form of the foregoing embodiments. For example, the disclosure provides the following embodiments.

[00126] FIG. 11 is a flowchart illustrating another method for using a power saving signaling pattern according to an example embodiment. The method may be executed by the terminal in the communications system as illustrated in FIG. 1. The method may include following.

[00127] At block 801, the terminal determines a DRX timer in an operating state. The operating state of the terminal may be one of the following four states.

[00128] At block 802, it is determined that a first pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00129] At block 803, it is determined that a second pattern for monitoring WUS is used in response to the DRX timer being the on duration timer with the short DRX cycle.

[00130] At block 804, it is determined that a third pattern for monitoring WUS is used in response to the DRX timer being the inactivity timer.

[00131] At block 805, it is determined that a fourth pattern for monitoring WUS is used in response to the DRX timer being the retransmission timer.

[00132] It should be noted that, the pattern for monitoring WUS corresponding to each

operating state of the DRX timer may be the same or different.

[00133] In an alternative embodiment based on any of the above embodiments, a method for using the pattern for monitoring the power saving signaling is preconfigured; or, a method for using the pattern for monitoring WUS is predefined. That is, the pattern for monitoring WUS may inform UE through system information or a dedicated signaling, or predefined according to a manner specified by the agreement.

[00134] In an alternative embodiment based on any of the above embodiments, the cycle of the power saving signaling is $1/N$ times or N times of the DRX cycle; the DRX cycle is the long DRX cycle or the short DRX cycle.

[00135] In an alternative embodiment based on any of the above embodiments, the cycle of the power saving signaling is $1/N$ times or N times of the DRX on duration timer; the DRX cycle is the long DRX cycle or the short DRX cycle; in which, N is an integer greater than or equal to 1.

[00136] The larger the value of N is, the smaller the monitoring granularity is and the higher the monitoring activity is. On the contrary, the smaller the value of N is, the larger the monitoring granularity is, and the lower the monitoring activity is.

[00137] In an alternative embodiment based on any of the above embodiments, the cycle of WUS is configured by an absolute time duration. The cycle of the WUS may be configured as a fixed time length, for example, 10ms or 4ms.

[00138] In an alternative embodiment based on any of the above embodiments, the pattern for monitoring WUS includes a start time offset of WUS; the start time offset of WUS is an offset configured with reference to an on duration of the DRX cycle.

[00139] The below are apparatus embodiments of the disclosure, which may be configured to implement the method embodiments of the disclosure. For details not disclosed in the apparatus embodiments of the disclosure, reference should be made to the method embodiments of the disclosure.

[00140] FIG. 12 is a block diagram illustrating an apparatus for using a power saving signaling pattern according to an example embodiment. The apparatus has the function of implementing the above method for using the power saving signaling pattern. The function may be achieved by hardware, or by hardware executing corresponding software. The

apparatus 700 may include a processing module 710 and a receiving module 720.

[00141] The processing module 710 is configured to determine a Discontinuous Reception (DRX) timer in an operating state, the DRX timer being one of: an on duration timer with a long DRX cycle, an inactivity timer, an on duration timer with a short DRX cycle, and a retransmission timer.

[00142] The processing module 710 is further configured to determine whether a pattern for monitoring the power saving signaling is used based on the DRX timer.

[00143] The receiving module 720 is configured to monitor the power saving signaling based on the pattern for monitoring the power saving signaling in response to determining that the pattern for monitoring the power saving signaling is used.

[00144] Optionally, the processing module 710 is configured to, determine that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00145] The processing module 710 is configured to, determine that a second pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the short DRX cycle.

[00146] The first pattern for monitoring the power saving signaling is same with the second pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the second pattern for monitoring the power saving signaling.

[00147] Optionally, the processing module 710 is configured to, determine that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the on duration timer with the short DRX cycle.

[00148] Optionally, the processing module 710 is configured to, determine that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00149] The processing module 710 is configured to, determine that a third pattern for monitoring the power saving signaling is used in response to the DRX timer being the inactivity timer.

[00150] The first pattern for monitoring the power saving signaling is same with the

third pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the third pattern for monitoring the power saving signaling.

[00151] Optionally, the processing module 710 is configured to, determine that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the inactivity timer.

[00152] Optionally, the processing module 710 is configured to, determine that a first pattern for monitoring the power saving signaling is used in response to the DRX timer being the on duration timer with the long DRX cycle.

[00153] The processing module 710 is configured to, determine that a fourth pattern for monitoring the power saving signaling is used in response to the DRX timer being the retransmission timer.

[00154] The first pattern for monitoring the power saving signaling is same with the fourth pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the fourth pattern for monitoring the power saving signaling.

[00155] Optionally, the processing module 710 is configured to, determine that the pattern for monitoring the power saving signaling is not used in response to the DRX timer being the retransmission timer.

[00156] Optionally, a method for using the pattern for monitoring the power saving signaling is preconfigured; or a method for using the pattern for monitoring the power saving signaling is predefined.

[00157] Optionally, the pattern for monitoring the power saving signaling includes a cycle of the power saving signaling; the cycle of the power saving signaling is $1/N$ times or N times of a DRX cycle; the DRX cycle is the long DRX cycle or the short DRX cycle; or the cycle of the power saving signaling is $1/N$ times or N times of a DRX on duration timer, the DRX cycle is the long DRX cycle or the short DRX cycle.

[00158] N is an integer greater than or equal to 1.

[00159] Optionally, the pattern for monitoring the power saving signaling includes a cycle of the power saving signaling; the cycle of the power saving signaling is configured by

an absolute time duration.

[00160] Optionally, the pattern for monitoring the power saving signaling includes a start time offset of the power saving signaling; the start time offset of the power saving signaling is an offset value configured with reference to an on duration of a DRX cycle.

[00161] FIG. 13 is a block diagram illustrating a terminal according to an example embodiment. The terminal includes a processor 101, a receiver 102, a transmitter 103, a memory 104, and a bus 105.

[00162] The processor 101 include one or more processing cores, and the processor 101 executes various function applications and information processing by running a software program or a module.

[00163] The receiver 102 and the transmitter 103 may be implemented as a communication component, which may be a communication chip.

[00164] The memory 104 is communicated to the processor 101 through the bus 105.

[00165] The memory 104 may be configured to store at least one instruction, and the processor 101 is configured to execute the at least one instruction to implement various actions in the above embodiments.

[00166] In addition, the memory 104 may be implemented by any type of volatile or non-volatile storage devices or their combination. The volatile or non-volatile storage device includes but not limited to a magnetic disk or an optical disk, an electrically erasable programmable read-only memory (EEPROM), an erasable programmable read-only memory (EPROM), a static random access memory (SRAM), a read-only memory (ROM), a magnetic memory, a flash memory and a programmable read-only memory (PROM).

[00167] In an exemplary embodiment, a non-transitory computer readable storage medium is further provided which includes instructions, such as the memory including instructions, in which the instructions may be executed by the processor of the terminal to complete the method for using the power saving signaling pattern. For example, the non-transitory computer readable storage medium may be a ROM, a random access memory (RAM), a CD-ROM, a magnetic tape, a floppy disk, an optical data storage device, etc.

[00168] A non-transitory computer readable storage medium is further provided. When the instructions in the non-transitory computer readable storage medium are executed by the

processor of the terminal so that the terminal may execute the method for using the power saving signaling pattern.

[00169] Other embodiments of the disclosure will be apparent to those skilled in the art from consideration of the specification and practice of the disclosure disclosed here. This application is intended to cover any variations, uses, or adaptations of the disclosure following the general principles thereof and including such departures from the present disclosure as come within known or customary practice in the art. It is intended that the specification and examples be considered as exemplary only, with a true scope and spirit of the disclosure being indicated by the following claims.

[00170] It will be appreciated that the disclosure is not limited to the exact construction that has been described above and illustrated in the accompanying drawings, and that various modifications and changes can be made without departing from the scope thereof. It is intended that the scope of the disclosure only be limited by the appended claims.

WHAT IS CLAIMED IS:

1. A method for using a power saving signaling pattern, comprising:

determining, by a terminal, a Discontinuous Reception (DRX) timer in an operating state, the DRX timer being one of: an on duration timer with a long DRX cycle and an on duration timer with a short DRX cycle;

determining, by the terminal, a pattern for monitoring the power saving signaling based on the DRX timer; and

monitoring, by the terminal, the power saving signaling based on the determined pattern for monitoring the power saving signaling.

2. The method as claimed in claim 1, wherein, determining by the terminal the pattern for monitoring the power saving signaling based on the DRX timer, comprises:

determining to use a first pattern for monitoring the power saving signaling in response to the DRX timer being the on duration timer with the long DRX cycle; and

determining to use a second pattern for monitoring the power saving signaling in response to the DRX timer being the on duration timer with the short DRX cycle;

wherein, the first pattern for monitoring the power saving signaling is same with the second pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the second pattern for monitoring the power saving signaling.

3. The method as claimed in claim 1, wherein, determining by the terminal the pattern for monitoring the power saving signaling based on the DRX timer, comprises:

determining not to use the pattern for monitoring the power saving signaling in response to the DRX timer being the on duration timer with the short DRX cycle.

4. The method as claimed in any one of claims 1 to 3, further comprising:

preconfiguring a method for using the pattern for monitoring the power saving signaling;

or

predefining a method for using the pattern for monitoring the power saving signaling.

5. The method as claimed in any one of claims 1 to 3, wherein, the pattern for monitoring the power saving signaling comprises a cycle of the power saving signaling;

the cycle of the power saving signaling is $1/N$ times or N times of a DRX cycle; the DRX cycle is the long DRX cycle or the short DRX cycle;

or,

the cycle of the power saving signaling is $1/N$ times or N times of a DRX on duration timer, wherein, N is an integer greater than or equal to 1.

6. An apparatus for using a power saving signaling pattern, comprising:

a processing module, configured to determine a Discontinuous Reception (DRX) timer in an operating state, the DRX timer being one of: an on duration timer with a long DRX cycle and an on duration timer with a short DRX cycle;

the processing module, configured to determine a pattern for monitoring the power saving signaling based on the DRX timer; and

a receiving module, configured to monitor the power saving signaling based on the determined pattern for monitoring the power saving signaling.

7. The apparatus as claimed in claim 6, wherein,

the processing module is configured to, determine to use a first pattern for monitoring the power saving signaling in response to the DRX timer being the on duration timer with the long DRX cycle; and

the processing module is configured to, determine to use a second pattern for monitoring the power saving signaling in response to the DRX timer being the on duration timer with the short DRX cycle;

wherein, the first pattern for monitoring the power saving signaling is same with the second pattern for monitoring the power saving signaling, or the first pattern for monitoring the power saving signaling is different from the second pattern for monitoring the power saving signaling.

8. The apparatus as claimed in claim 6, wherein,
the processing module is configured to, determine not to use the pattern for monitoring the power saving signaling in response to the DRX timer being the on duration timer with the short DRX cycle.

9. The apparatus as claimed in any one of claims 6 to 8, wherein,
a method for using the pattern for monitoring the power saving signaling is preconfigured;
or
a method for using the pattern for monitoring the power saving signaling is predefined.

10. The apparatus as claimed in any one of claims 6 to 8, wherein, the pattern for monitoring the power saving signaling comprises a cycle of the power saving signaling;
the cycle of the power saving signaling is $1/N$ times or N times of a DRX cycle; the DRX cycle is the long DRX cycle or the short DRX cycle;
or,
the cycle of the power saving signaling is $1/N$ times or N times of a DRX on duration timer, the DRX cycle is the long DRX cycle or the short DRX cycle;
wherein, N is an integer greater than or equal to 1.

11. A terminal, comprising:
a processor;
a transceiver coupled to the processor; and
a memory configured to store instructions executable by the processor;
wherein, the processor is configured to load and execute the executable instructions to implement the method as claimed in any one of claims 1 to 5.

12. A computer readable storage medium, wherein the computer readable storage medium stores at least one instruction, at least one program, a code set, or an instruction set,

and the at least one instruction, the at least one program, the code set, or the instruction set is loaded and executed by a processor to implement the method as claimed in any one of claims 1 to 5.