ABSTRACT

METHOD OF POSITIONING AN I.C. AND IC HANDLER UTILIZING SAID METHOD

An object of the present invention is to provide a method of positioning ICs and an IC handler 50 utilizing said method in which a vertical mechanism 60 is not provided in a positioning mechanism 10. In the IC handler 50, a positioning wall piece enclose a table 68, it has two pairs of inner faces, each pair of which are faced each other, lower portions S of each pair are formed into vertical faces 82 and mutually separated with a distance corresponding to outermost edges of the IC 70, upper portions T of each pair are formed into slope faces 84 whose distance is gradually made longer upward. An elastic member 20 elastically supports the positioning wall piece 72, which is capable of moving vertically. The vertical mechanism 60 is located above the positioning mechanism 10 and pushes the positioning wall piece 72 downward so as to correspond the upper face of the table 68 to the upper portions T when the IC 70 is mounted on the table 68, and allows to move the positioning wall piece 72 upward so as to correct a horizontal deviation of the IC 70 by the slope faces 84 and to position the same by the vertical faces 82 after the IC 70 is mounted on the table 68.