(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau

(43) International Publication Date
1 May 2003 (01.05.2003)

PCT

(10) International Publication Number
WO 03/035229 A2

(51) International Patent Classification[7]: B01D 61/00

(21) International Application Number: PCT/SG02/00251

(22) International Filing Date: 25 October 2002 (25.10.2002)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/335,875 26 October 2001 (26.10.2001) US
10/279,627 24 October 2002 (24.10.2002) US

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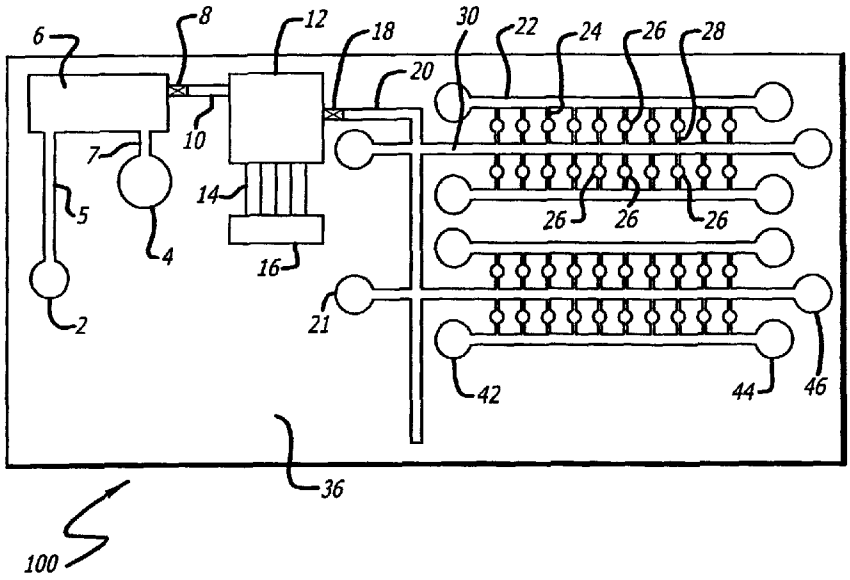
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(81) Designated States *(national)*: AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.

(84) Designated States *(regional)*: ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE,

[Continued on next page]

(54) Title: SAMPLE PREPARATION INTEGRATED CHIP



(57) Abstract: The present invention relates to an apparatus comprising a substrate having at least one assay station. The at least one assay station has at least a first assay station channel and at least a second assay station channel and the first and second assay station channels each separately being in communication with the at least one assay station. The apparatus has an arrangement of at least first and second multipurpose channels in communication with the first and second assay station channels, respectively. The first multipurpose channel and first assay station channel have internal surface characteristics conducive to conduction of a sample solution therethrough. There is at least one sample fluid inlet in communication with the at least first multipurpose channel,. and at least one isolation-medium inlet in communication with the at least first and second multipurpose channels. The at least one second multipurpose channel has an internal surface portion non-conducive to conduction of said sample solution.

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ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— *without international search report and to be republished upon receipt of that report*

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