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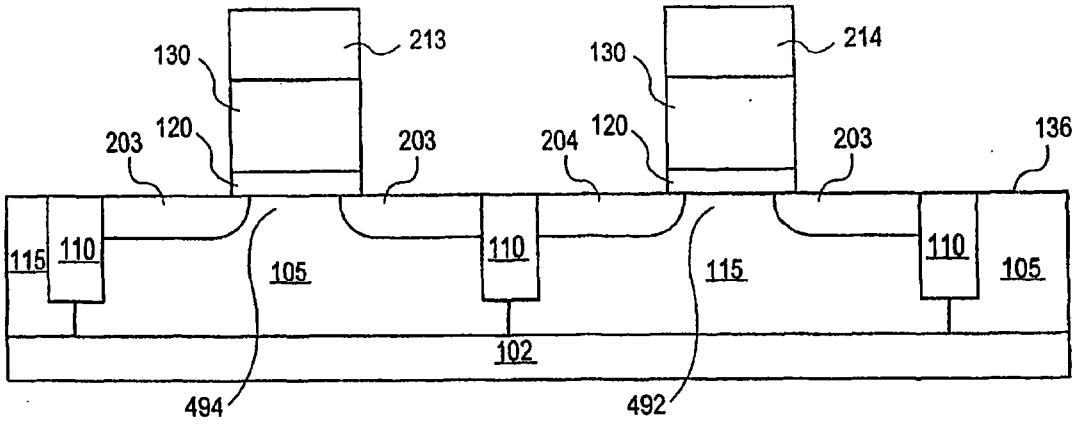
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(54) Title: GATE-INDUCED STRAIN FOR MOS PERFORMANCE IMPROVEMENT




(57) Abstract: There is disclosed an apparatus including a substrate (105, 115) defining an interior of the apparatus, a device exterior to the substrate including a gate electrode (130, 132), and a straining layer (213, 214) exterior to the gate electrode and exterior to the substrate.

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