

ABSTRACT

Conversion of an RF signal amplifier from an unbalanced signal configuration to a balanced signal configuration is provided while including an image trap for both sides of the balanced circuit. The grounded side of the secondary of a double tuned circuit (14,16,18,20,22) is lifted from ground to provide a balanced circuit configuration. A trap filter is provided in the balanced circuit configuration by a pair of small capacitors (CS1,CS2) coupled between the balanced secondary leads (26a,26b) to appropriate signal nodes.

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This Sheet Has Been Replaced

