

ABSTRACT

A horizontal oscillator (102), responsive to an on/off control signal, generates an output signal that is coupled via a driver stage (300) to a control terminal (pin 8 of MC1391) of a horizontal deflection output transistor (Q2, Fig. 1a). During a transition interval (1msec, Fig. 3c) that follows a standby mode of operation, the duty cycle of the oscillator output signal (V_{OSC} , Fig. 1b) is reduced relative to when a run mode of operation occurs to provide soft-start operation.

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FIGURE 1b

This Sheet Has Been Replaced

