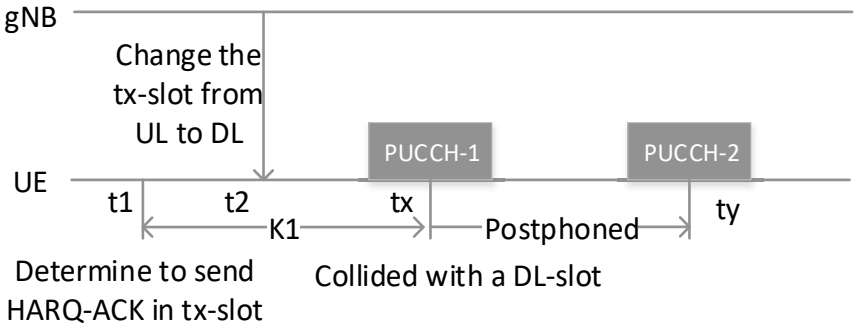

FIG. 1

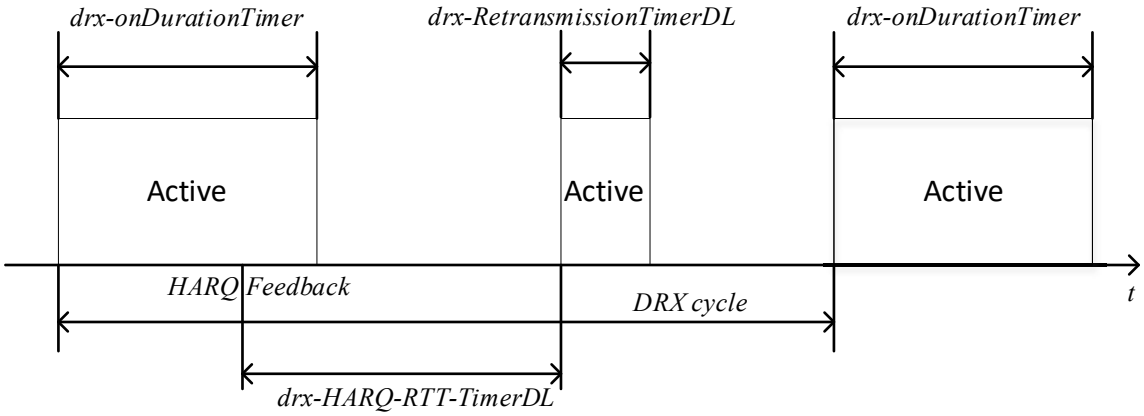

FIG. 2

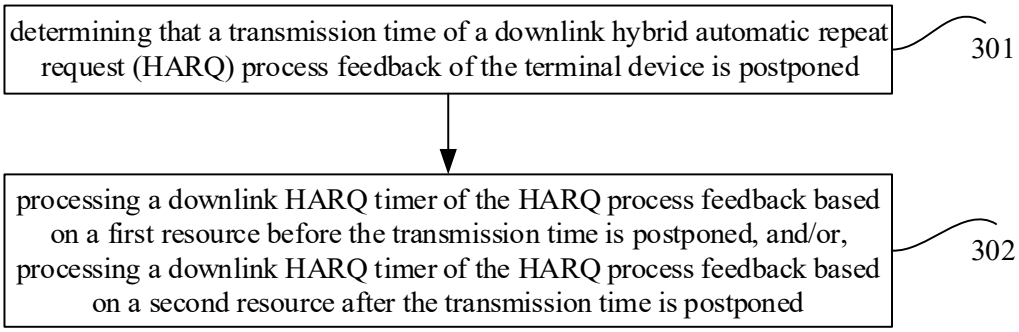

FIG. 3

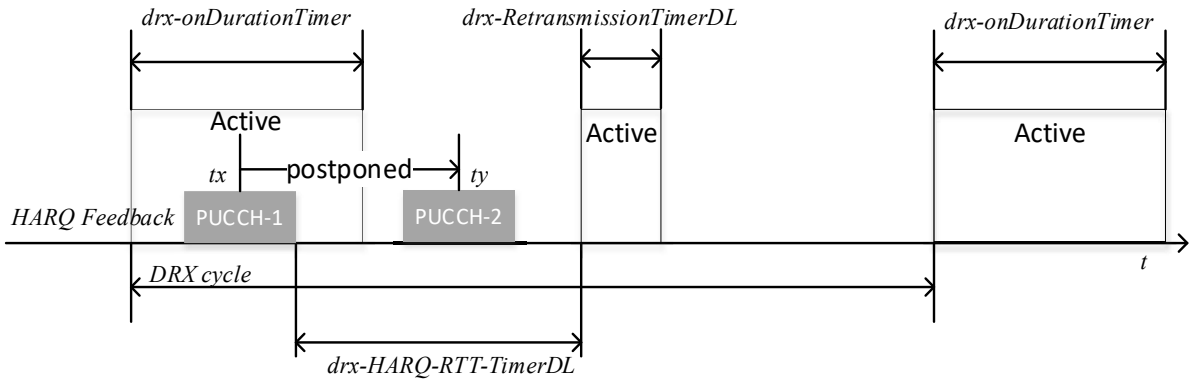

FIG. 4

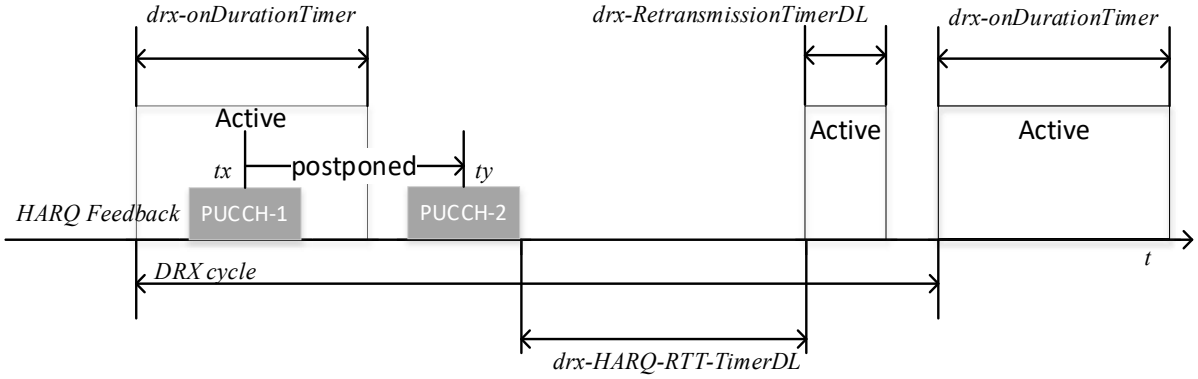

FIG. 5

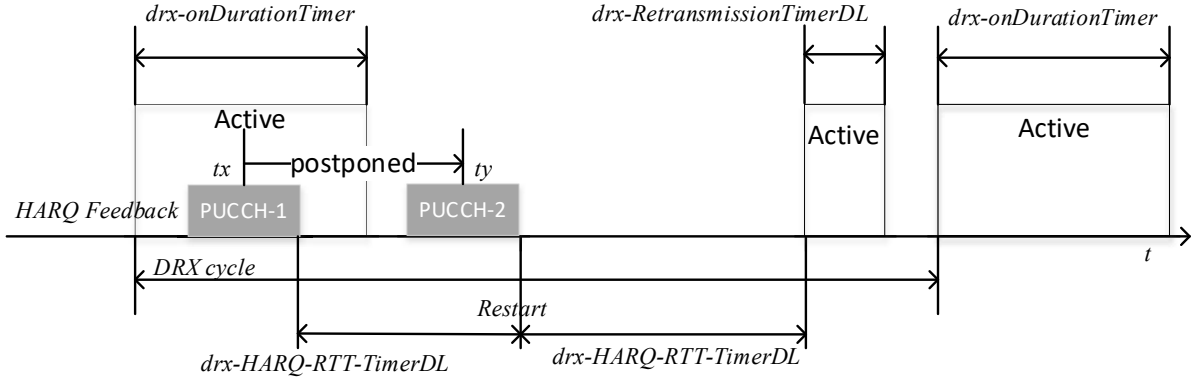

FIG. 6

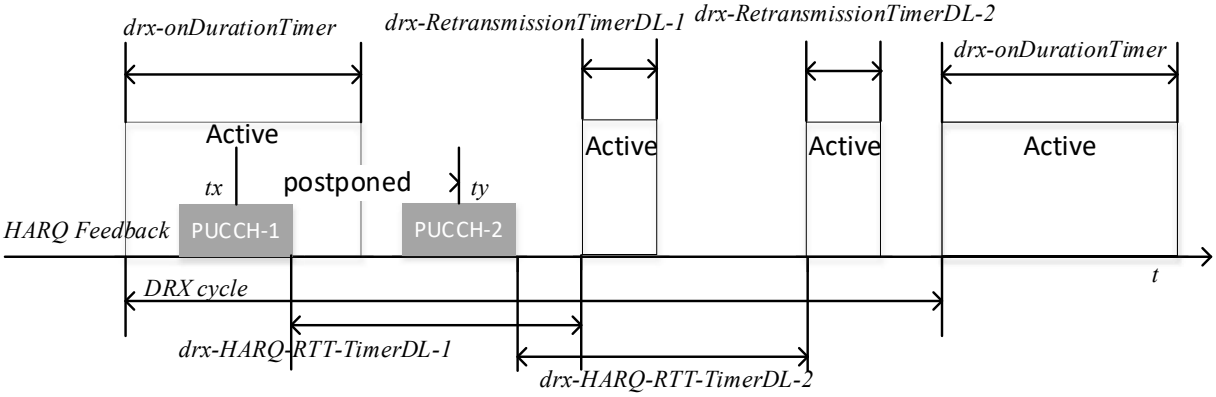

FIG. 7

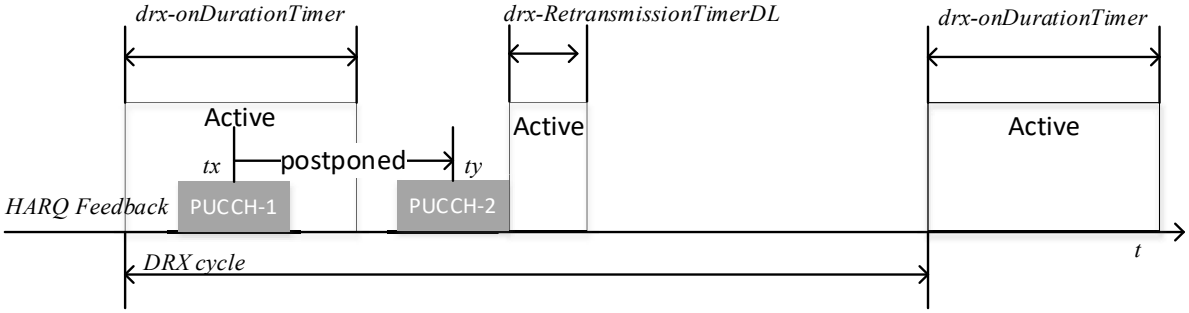

FIG. 8

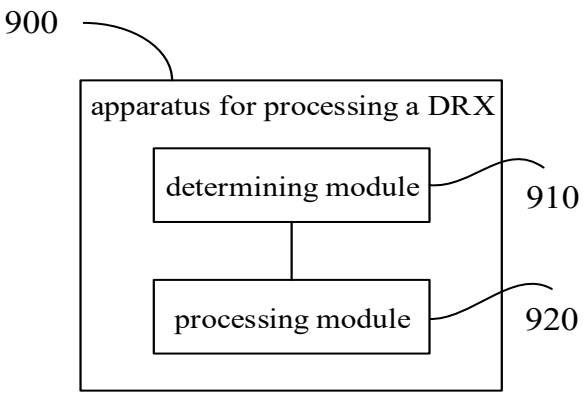

FIG. 9

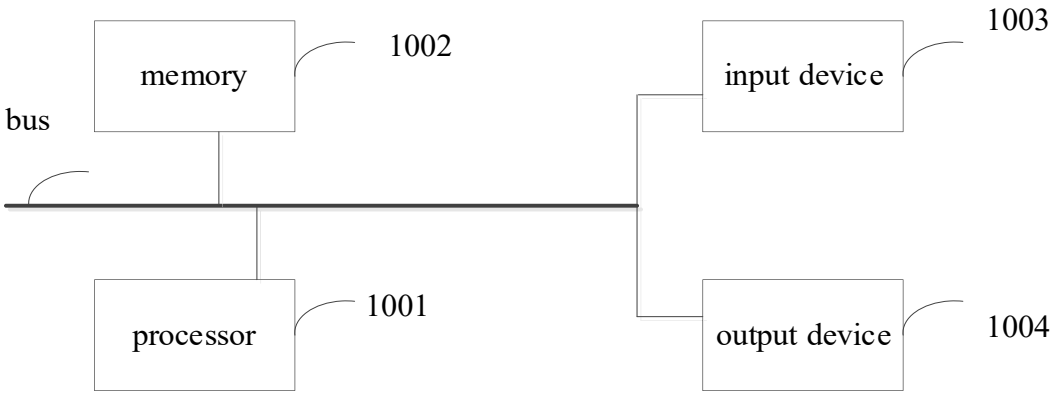
memory
1002
input device
1003
bus
processor
1001
output device
1004

FIG. 10