

ABSTRACT

Picture data read out from a VRAM 18 are sent via line buffers 75a to 75d to a selection synthesis unit 63. The line buffer 75d seizes picture data supplied from outside for sending the seized picture data to the VRAM 18. The VRAM 18 can write the picture data from outside supplied via the line buffer 75d and read out the picture data based on addresses from a controller in the same way as other picture data. On the other hand, cache memories 74a and 74b can read out picture data under control by the controller 71 to display plural pictures in a tiled pattern on a display screen.