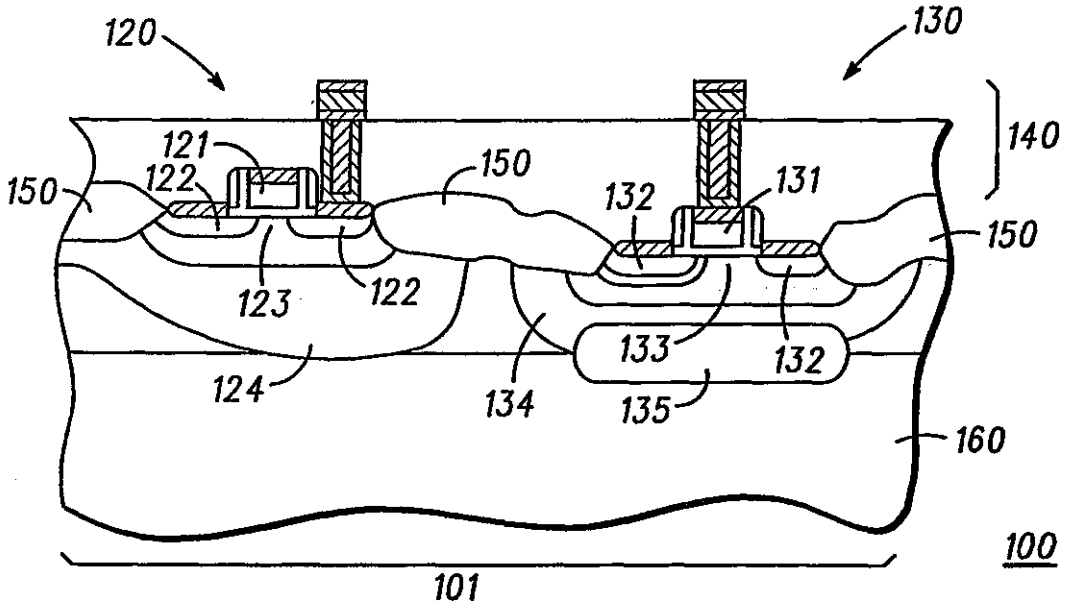

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FIG. 1

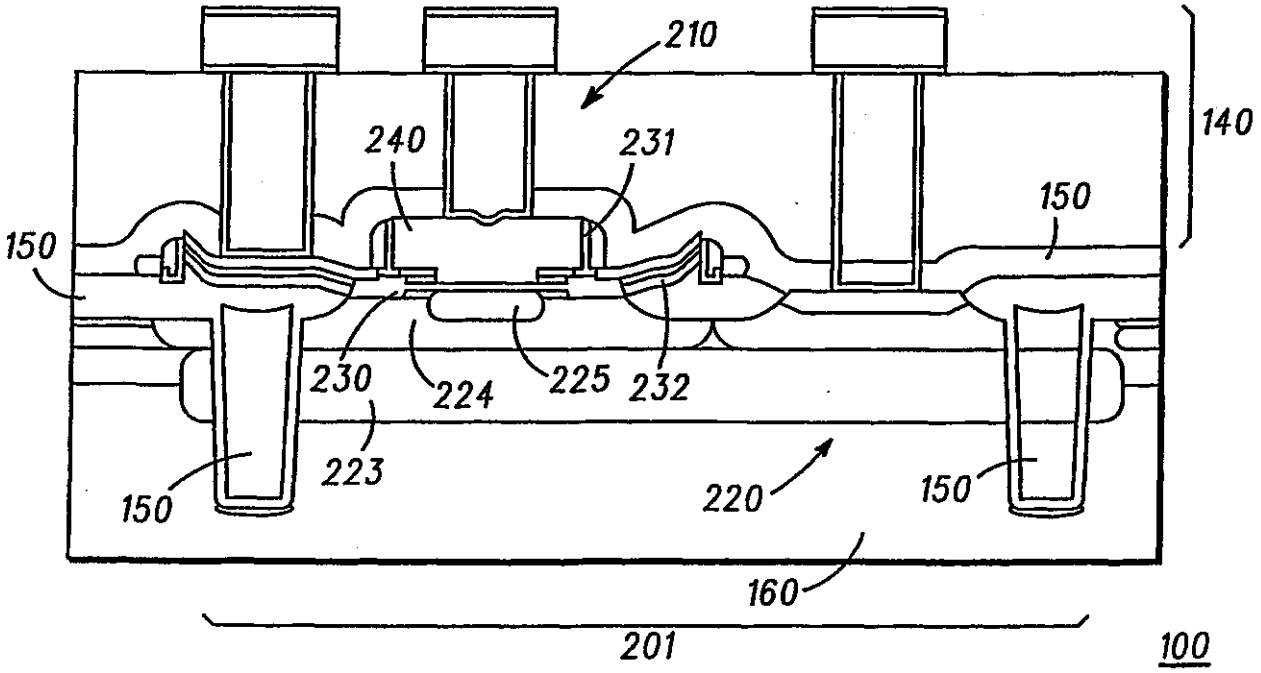

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FIG. 2

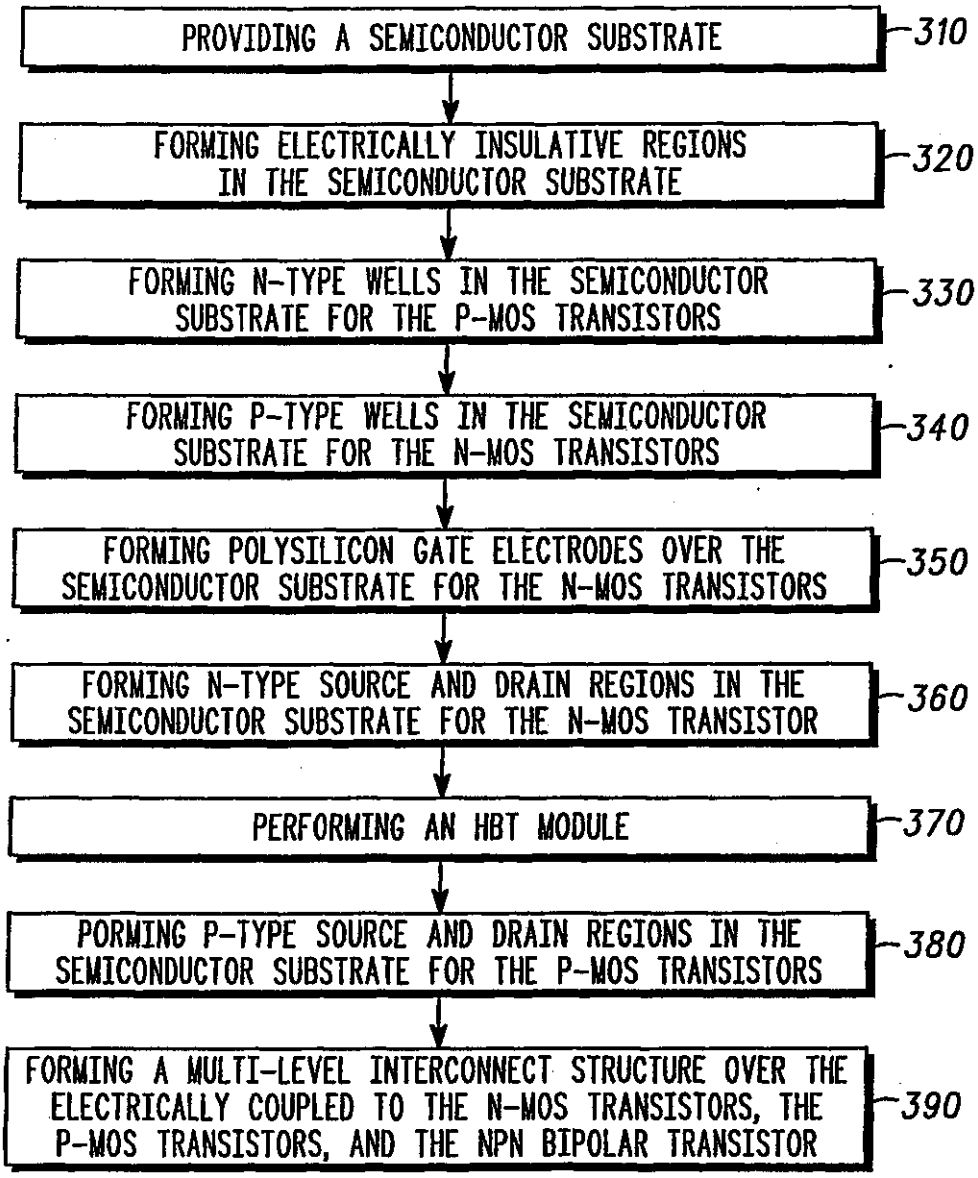

PROVIDING A SEMICONDUCTOR SUBSTRATE
310
FORMING ELECTRICALLY INSULATIVE REGIONS IN THE SEMICONDUCTOR SUBSTRATE
320
FORMING N-TYPE WELLS IN THE SEMICONDUCTOR SUBSTRATE FOR THE P-MOS TRANSISTORS
330
FORMING P-TYPE WELLS IN THE SEMICONDUCTOR SUBSTRATE FOR THE N-MOS TRANSISTORS
340
FORMING POLYSILICON GATE ELECTRODES OVER THE SEMICONDUCTOR SUBSTRATE FOR THE N-MOS TRANSISTORS
350
FORMING N-TYPE SOURCE AND DRAIN REGIONS IN THE SEMICONDUCTOR SUBSTRATE FOR THE N-MOS TRANSISTOR
360
PERFORMING AN HBT MODULE
370
PORMING P-TYPE SOURCE AND DRAIN REGIONS IN THE SEMICONDUCTOR SUBSTRATE FOR THE P-MOS TRANSISTORS
380
FORMING A MULTI-LEVEL INTERCONNECT STRUCTURE OVER THE ELECTRICALLY COUPLED TO THE N-MOS TRANSISTORS, THE P-MOS TRANSISTORS, AND THE NPN BIPOLAR TRANSISTOR
390
300

FIG. 3

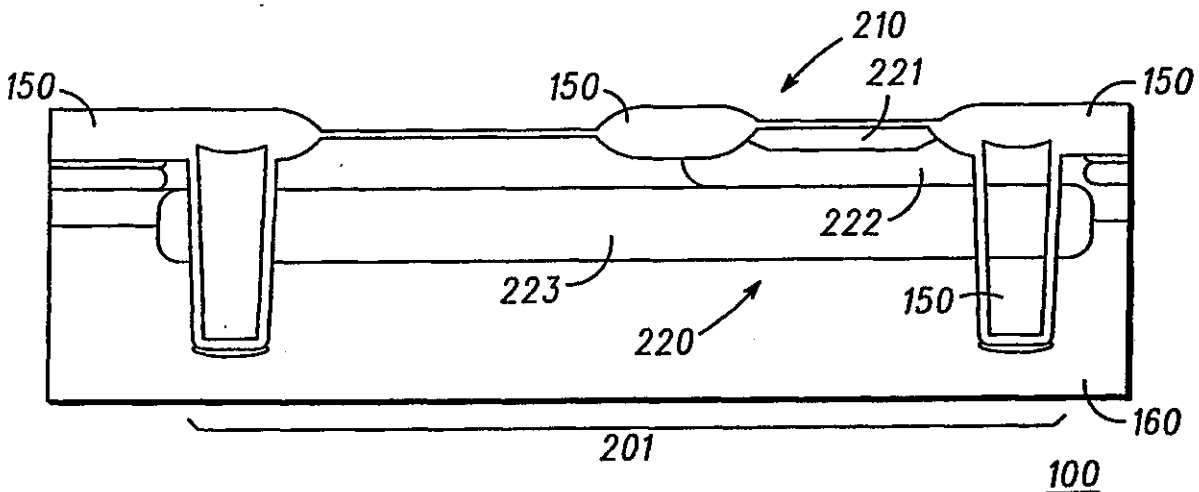


FIG. 5

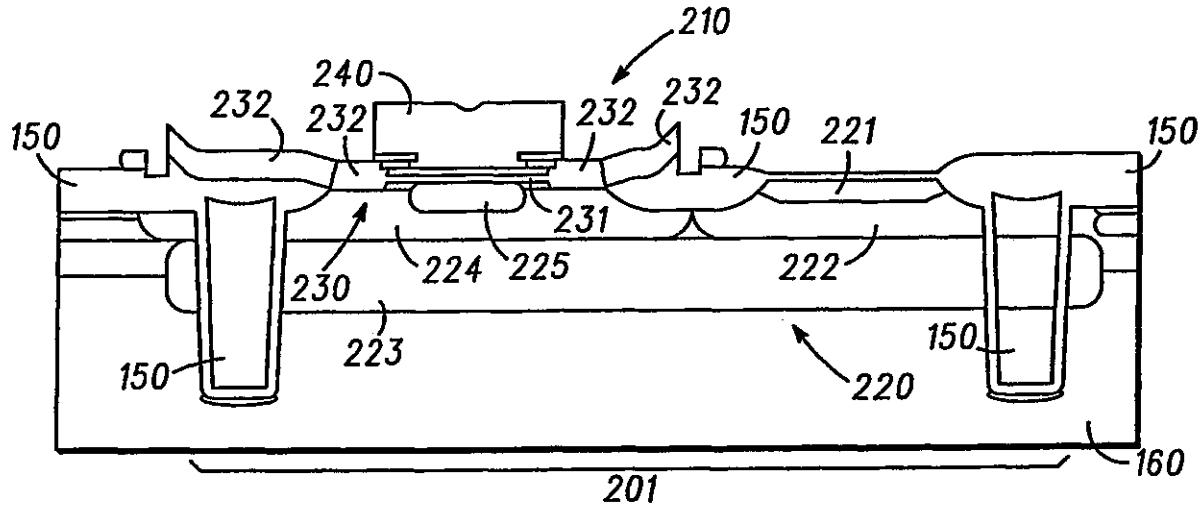


FIG. 6

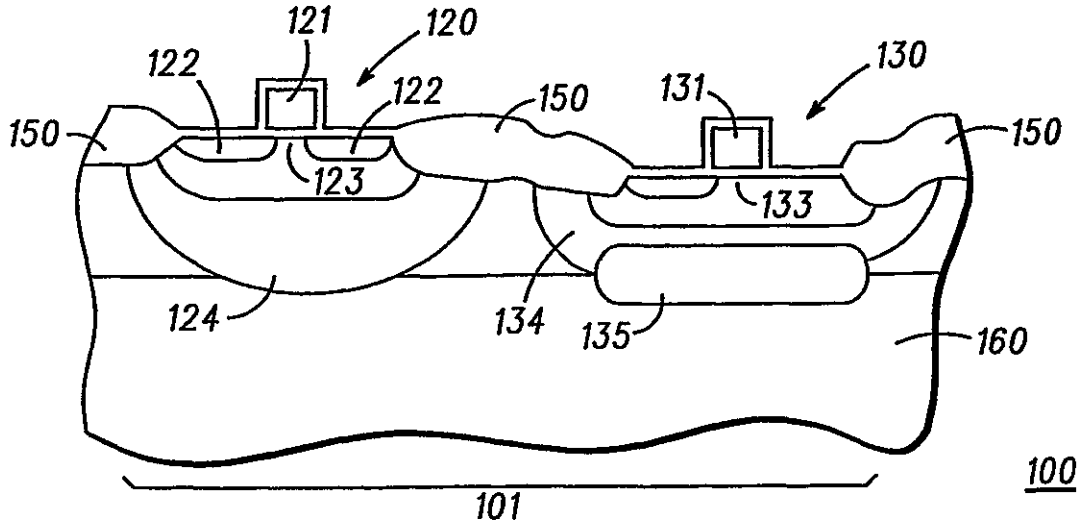


FIG. 7

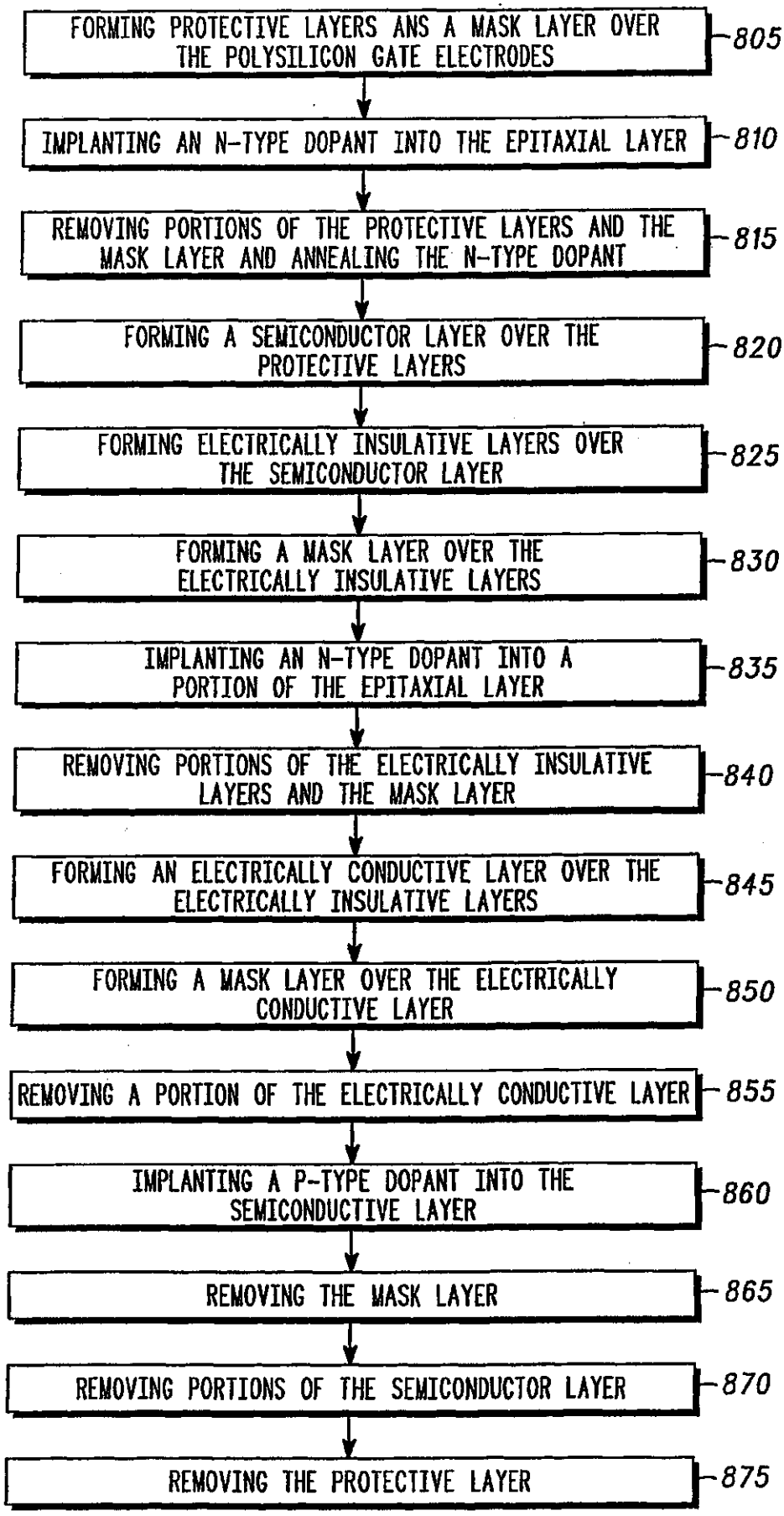

FORMING PROTECTIVE LAYERS ANS A MASK LAYER OVER THE POLYSILICON GATE ELECTRODES
805
IMPLANTING AN N-TYPE DOPANT INTO THE EPITAXIAL LAYER
810
REMOVING PORTIONS OF THE PROTECTIVE LAYERS AND THE MASK LAYER AND ANNEALING THE N-TYPE DOPANT
815
FORMING A SEMICONDUCTOR LAYER OVER THE PROTECTIVE LAYERS
820
FORMING ELECTRICALLY INSULATIVE LAYERS OVER THE SEMICONDUCTOR LAYER
825
FORMING A MASK LAYER OVER THE ELECTRICALLY INSULATIVE LAYERS
830
IMPLANTING AN N-TYPE DOPANT INTO A PORTION OF THE EPITAXIAL LAYER
835
REMOVING PORTIONS OF THE ELECTRICALLY INSULATIVE LAYERS AND THE MASK LAYER
840
FORMING AN ELECTRICALLY CONDUCTIVE LAYER OVER THE ELECTRICALLY INSULATIVE LAYERS
845
FORMING A MASK LAYER OVER THE ELECTRICALLY CONDUCTIVE LAYER
850
REMOVING A PORTION OF THE ELECTRICALLY CONDUCTIVE LAYER
855
IMPLANTING A P-TYPE DOPANT INTO THE SEMICONDUCTIVE LAYER
860
REMOVING THE MASK LAYER
865
REMOVING PORTIONS OF THE SEMICONDUCTOR LAYER
870
370
REMOVING THE PROTECTIVE LAYER
875

FIG. 8

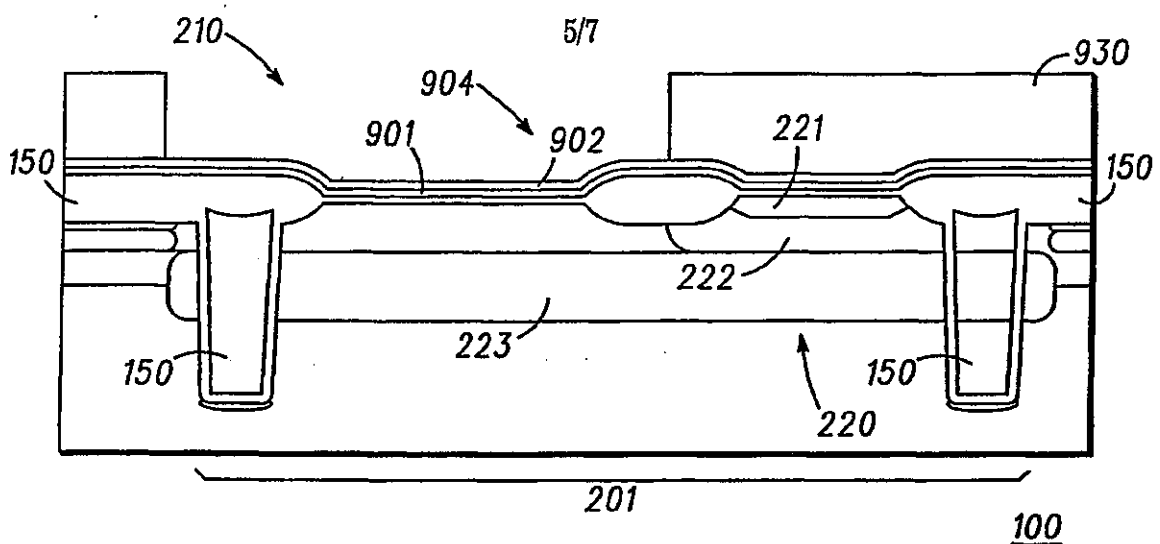


FIG. 9

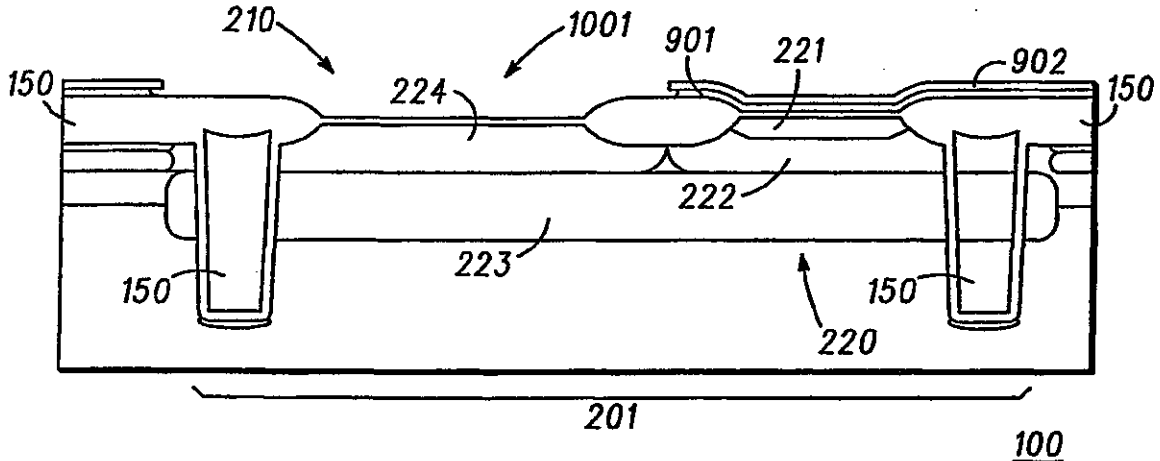


FIG. 10

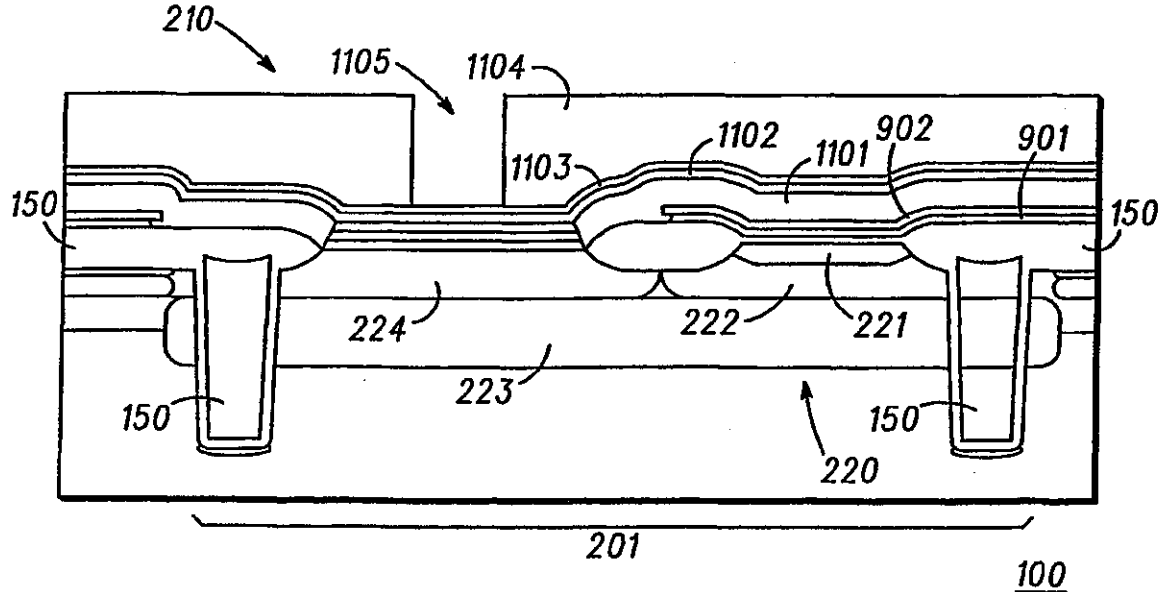


FIG. 11

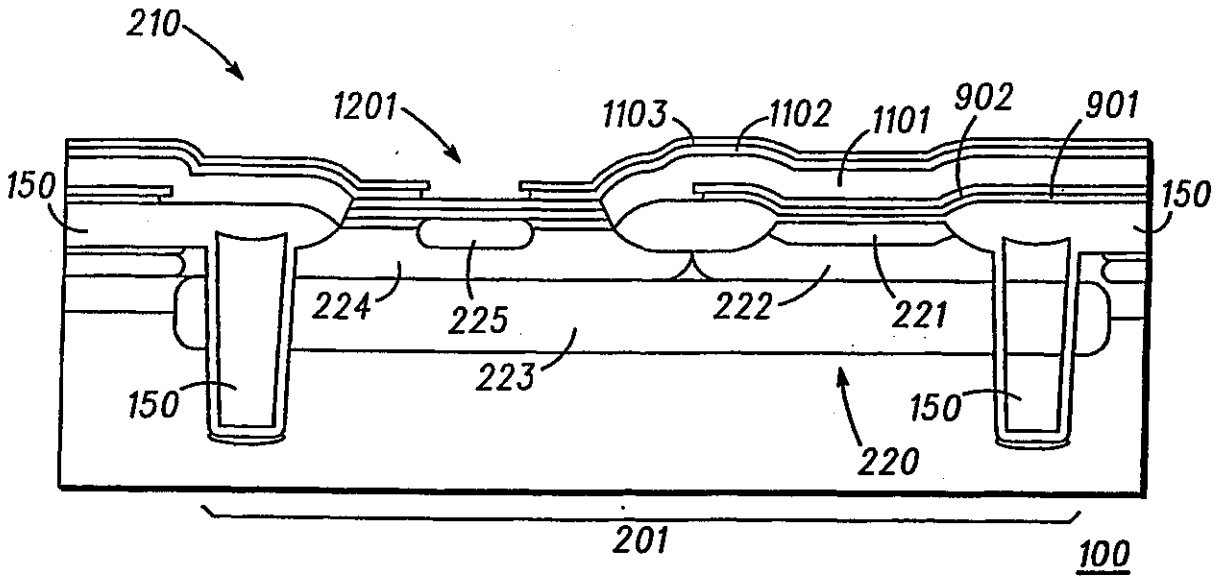

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FIG. 12

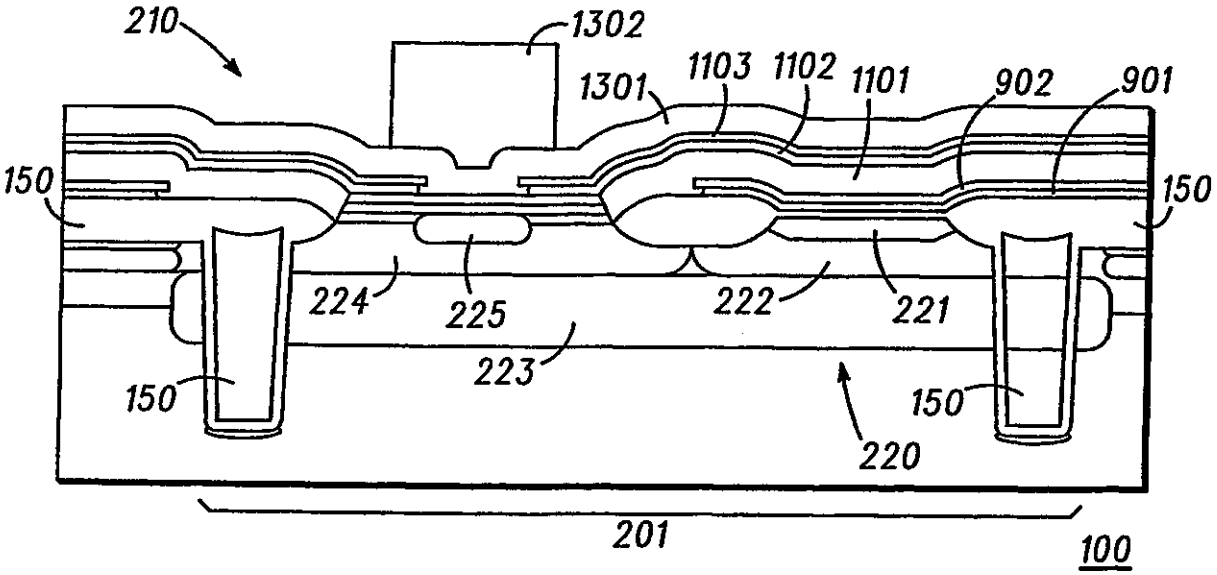

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FIG. 13

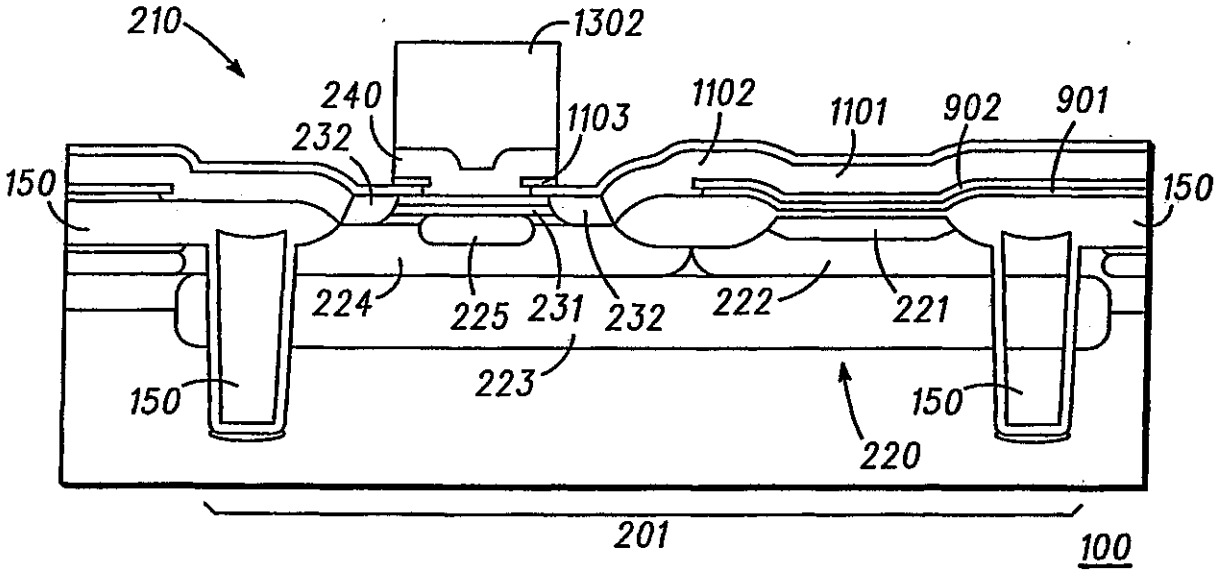

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FIG. 14

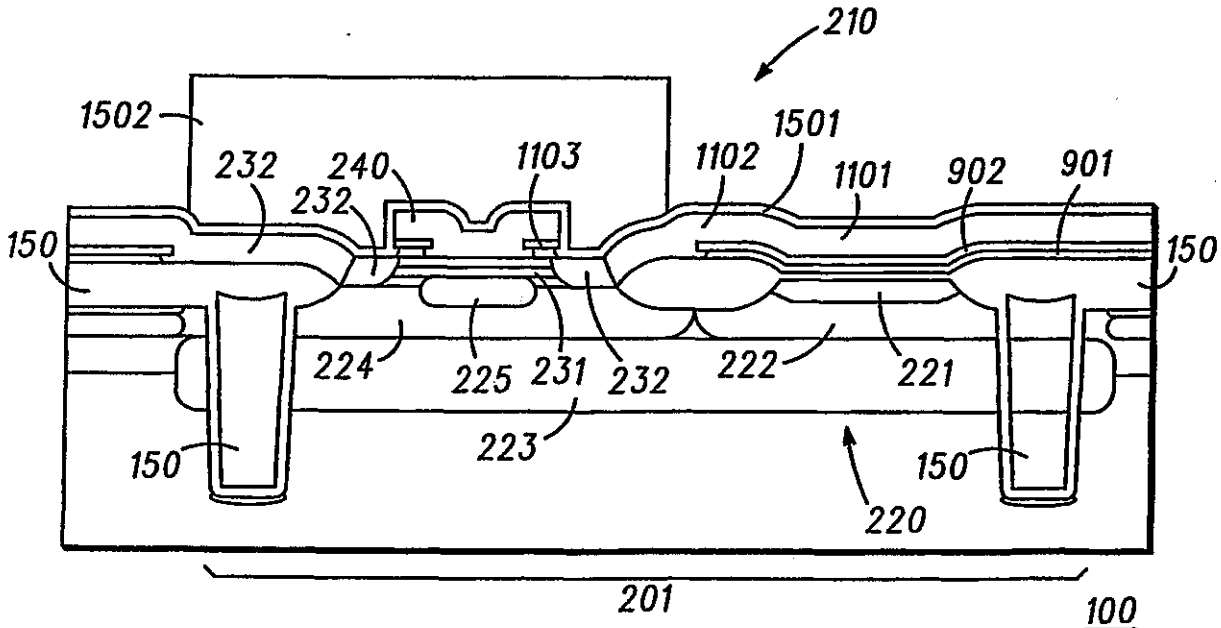

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FIG. 15