

**TRANSPARENT PCI TO PCI BRIDGE WITH DYNAMIC MEMORY AND I/O
MAP PROGRAMMING**

ABSTRACT OF THE DISCLOSURE

A PCI repeater coupled between a primary bus and a secondary bus includes logic to allow downstream and upstream bursting across the repeater. The PCI repeater is operable to echoe transactions in either an upstream or downstream direction. During configuration, the PCI repeater snoops configuration cycles on the primary bus to build
5 an address map of devices on the primary bus. The PCI repeater then uses the address map as a lookup table as a positive determination of whether to forward a transaction upstream.

(Fig. 1)