

ABSTRACT

REDUCED SWITCHING CURRENT BUFFER AMPLIFIER

Buffer amplifiers are arranged in a branching tree-like structure to distribute a clock signal to many loads in an integrated circuit. In order to reduce switching current and EMI, the buffer amplifiers which are the last buffer amplifiers in the branching structure are comprised of a series coupled first inverting amplifier (301, 305) and a second inverting amplifier (303, 307). These inverting amplifiers are coupled to an input port and an output port of the buffer amplifiers. A non-inverting amplifier (309, 311) of the buffer amplifiers is parallel coupled with the series coupled inverting amplifiers between the input port and the output port.

(Please note that Figure 3 should accompany the abstract when published)