FIG.1A

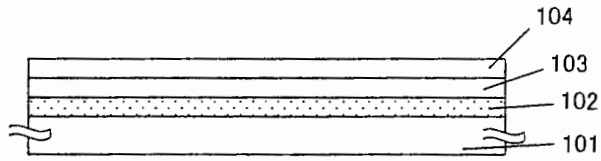




FIG.1B

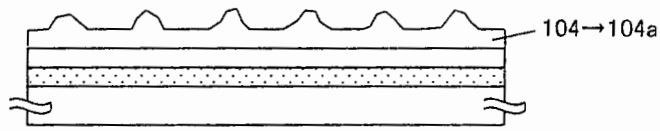


FIG.1C

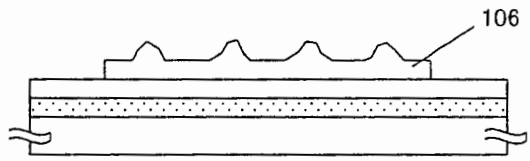
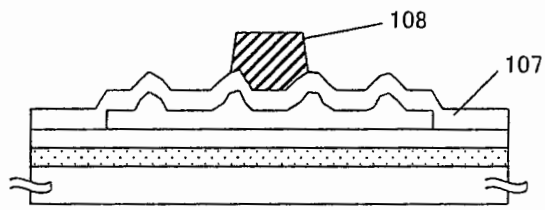


FIG.1D



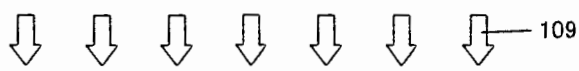
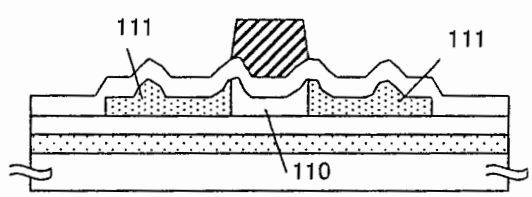


FIG.1E



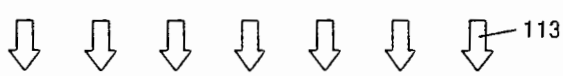


FIG.1F

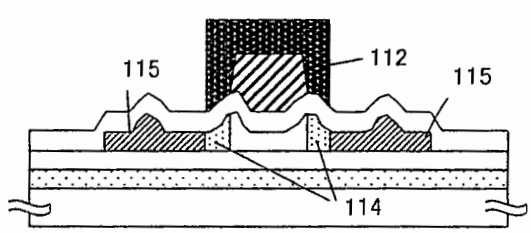


FIG.1G

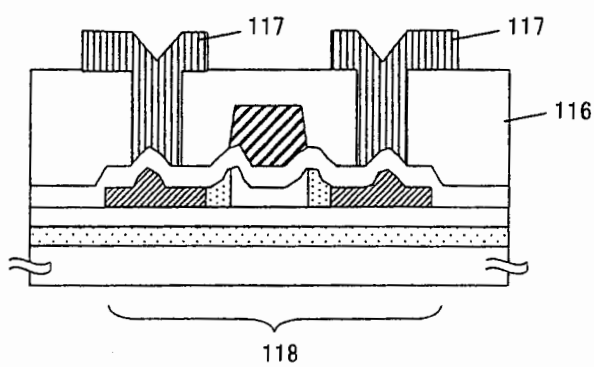


FIG.2A

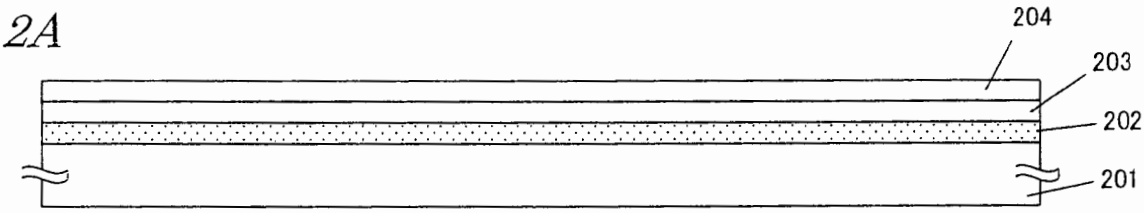


FIG.2B

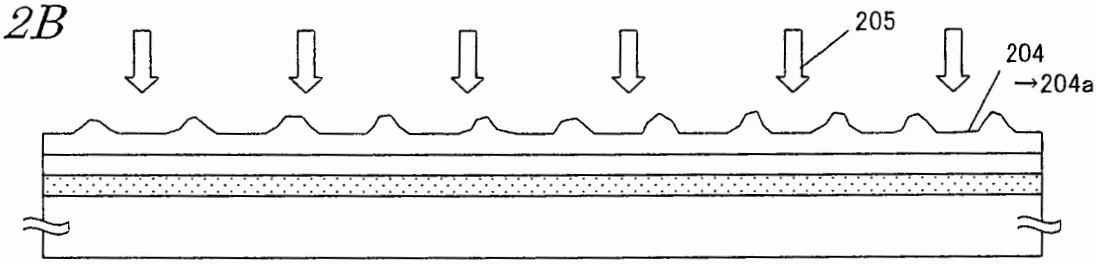


FIG.2C

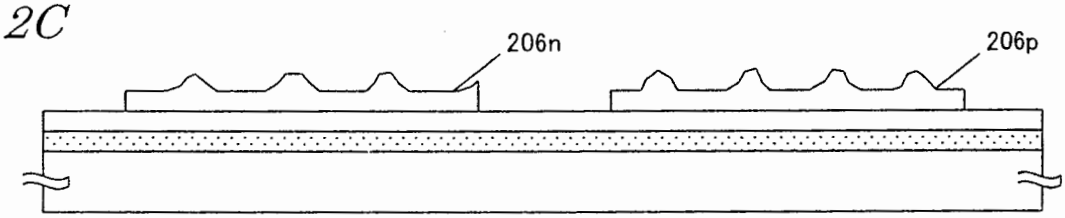


FIG.2D

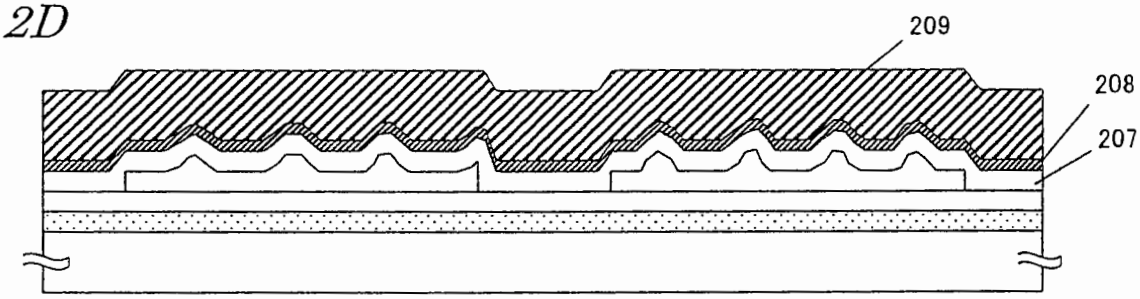


FIG.2E

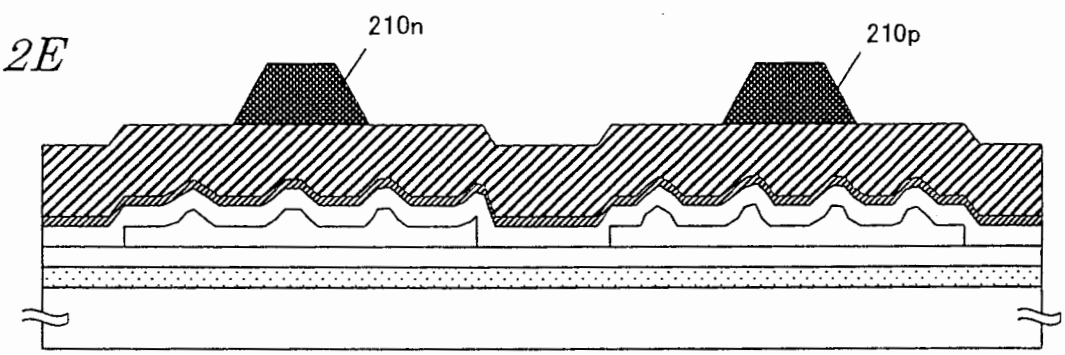


FIG.2F

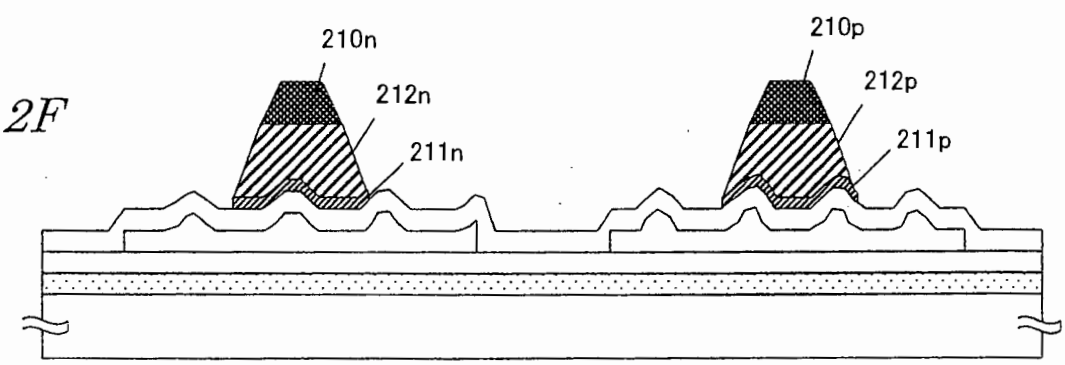


FIG.3A

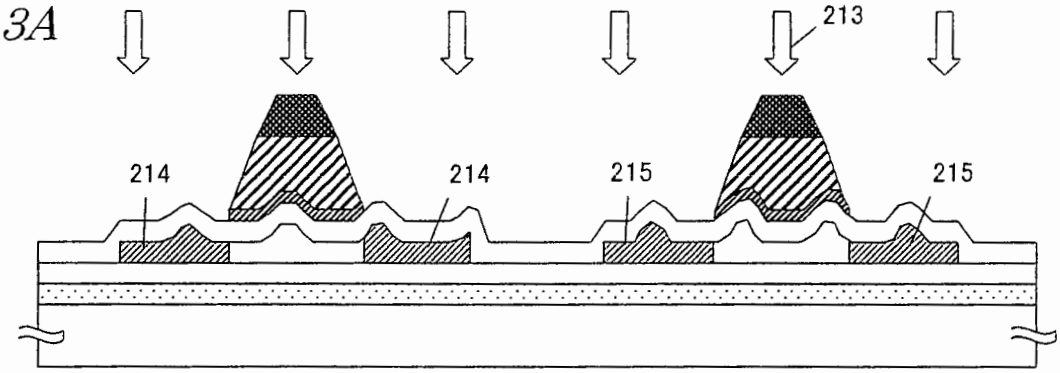


FIG.3B

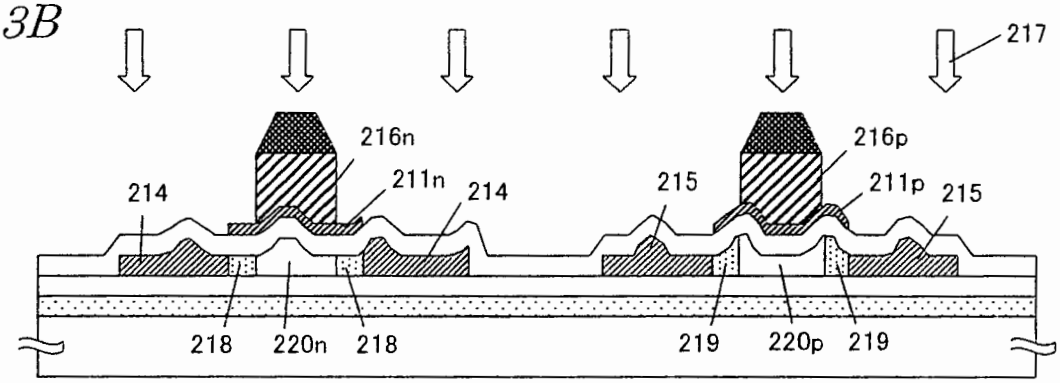


FIG.3C

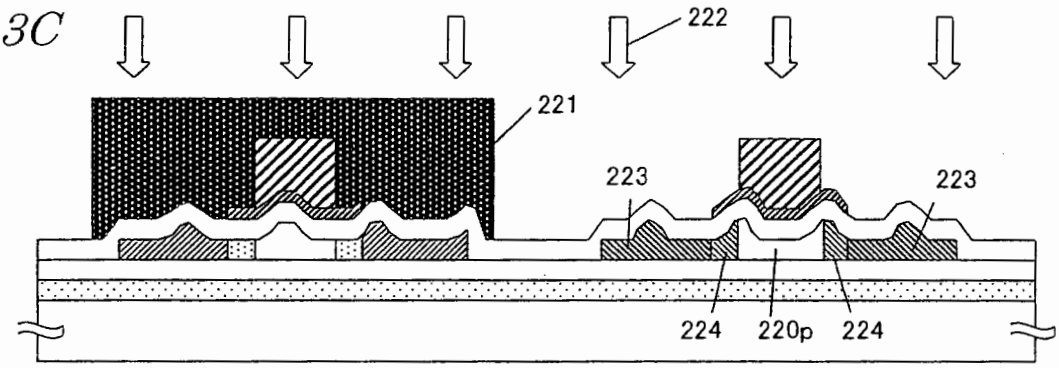


FIG.3D

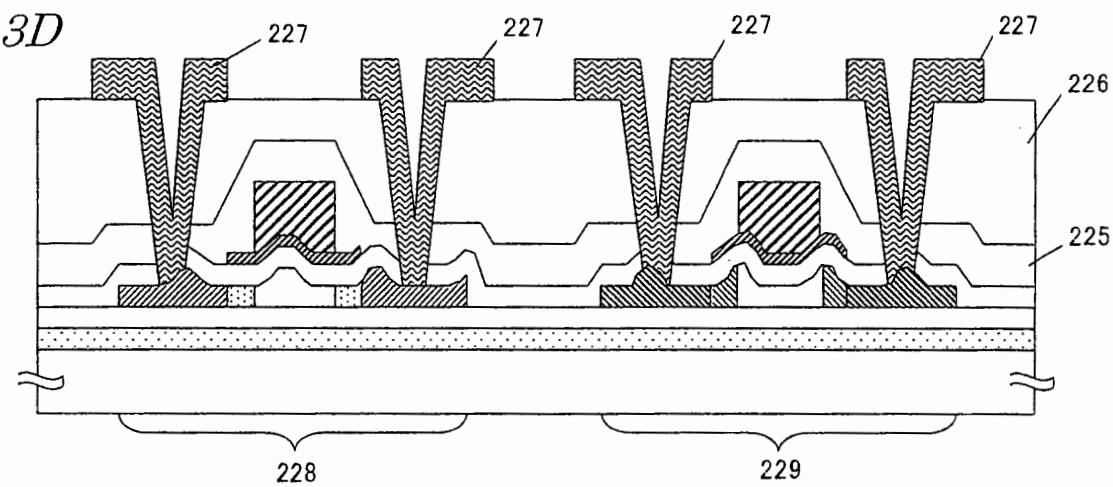


FIG.4A

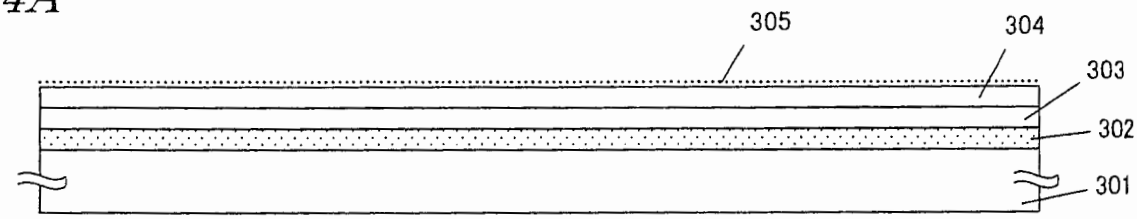


FIG.4B

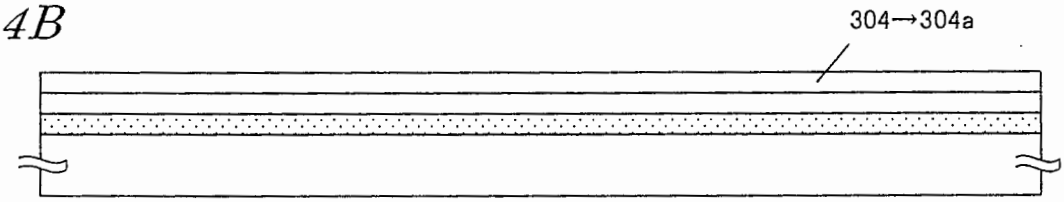


FIG.4C

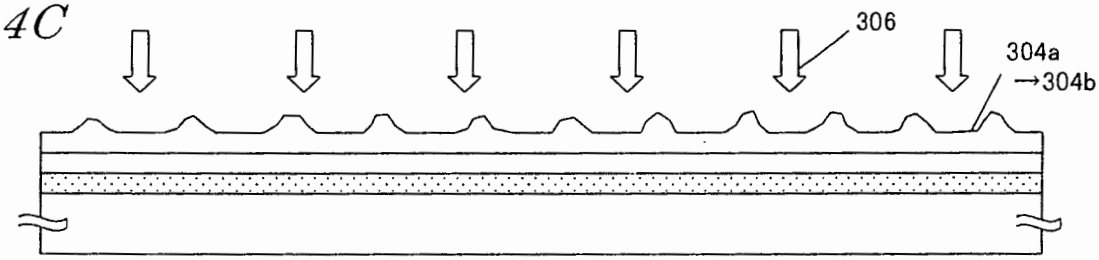


FIG.4D

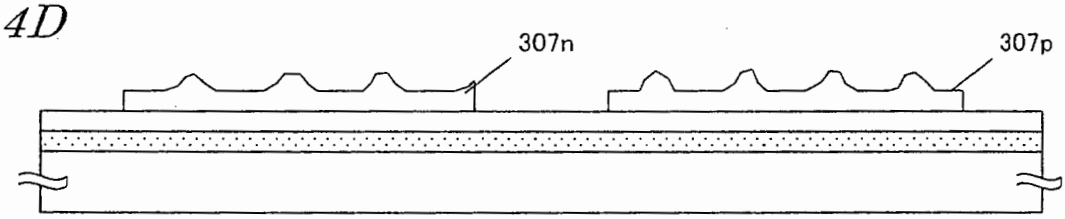


FIG.4E

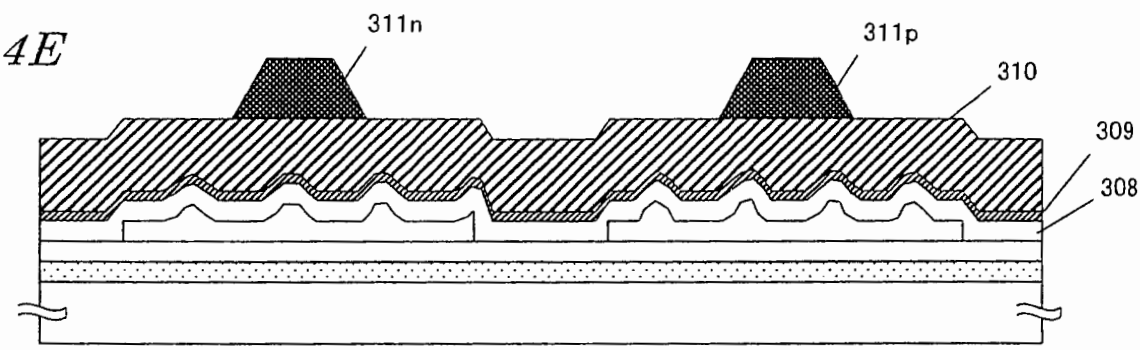


FIG.4F

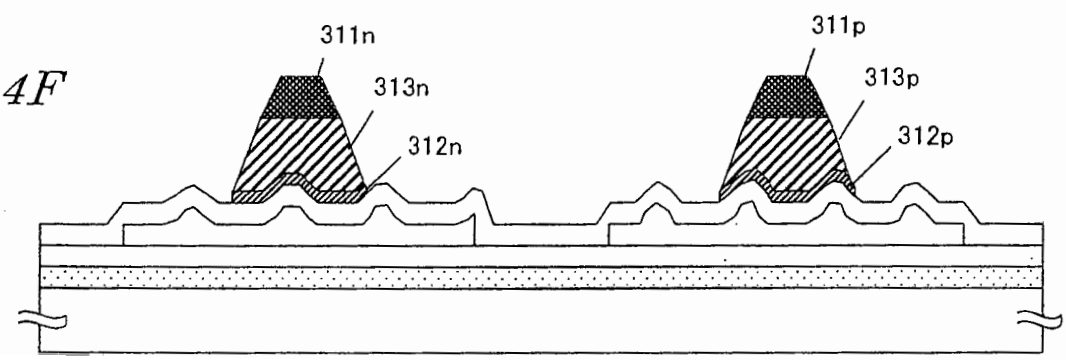


FIG.5A

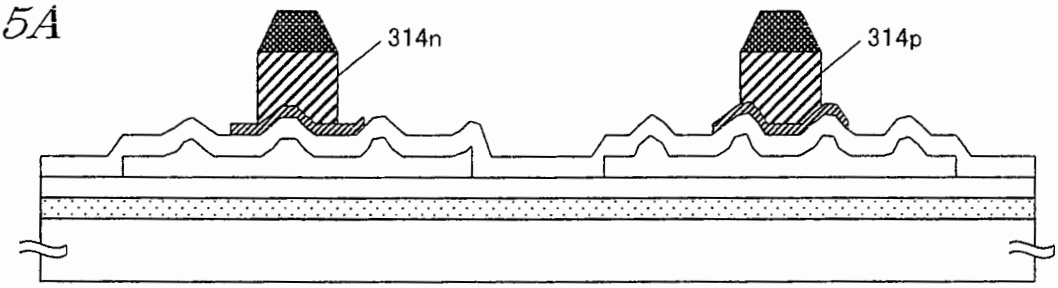


FIG.5B

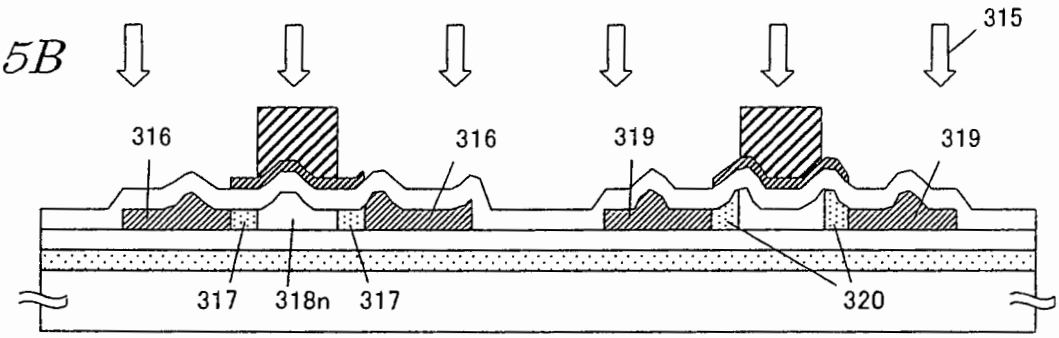


FIG.5C

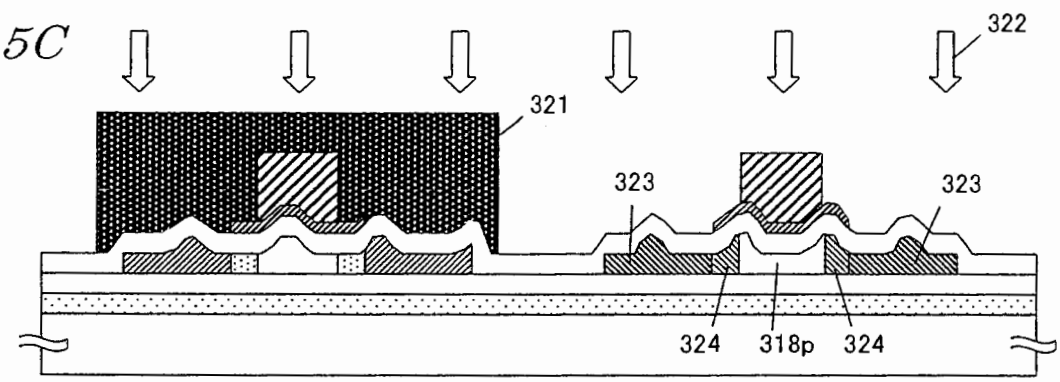


FIG.5D

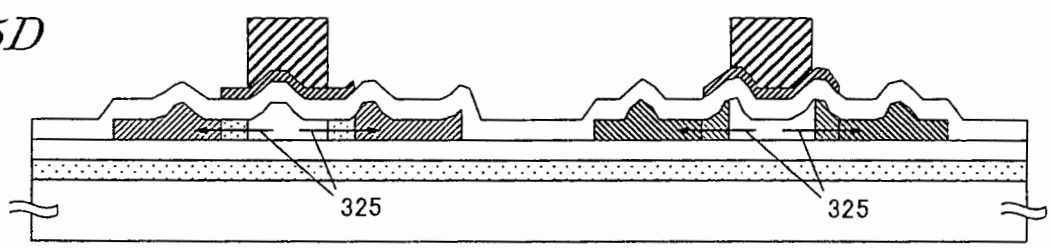


FIG.5E

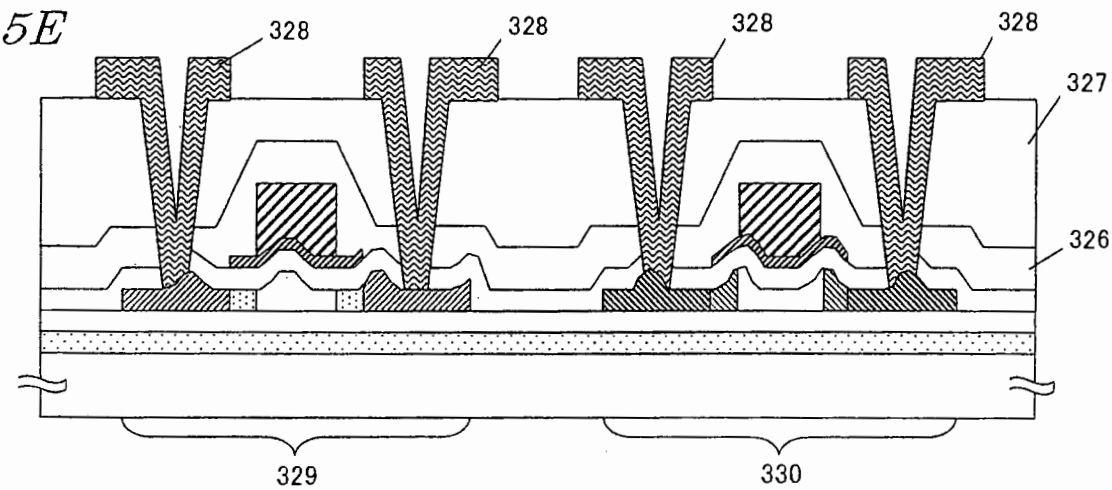


FIG.6A

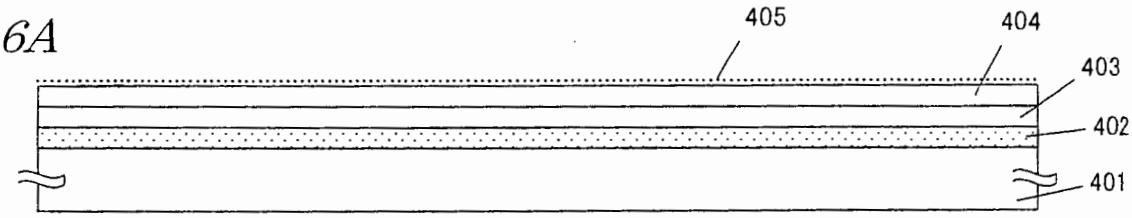


FIG.6B

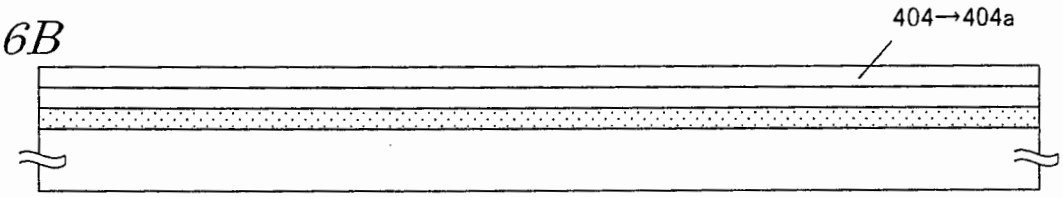


FIG.6C

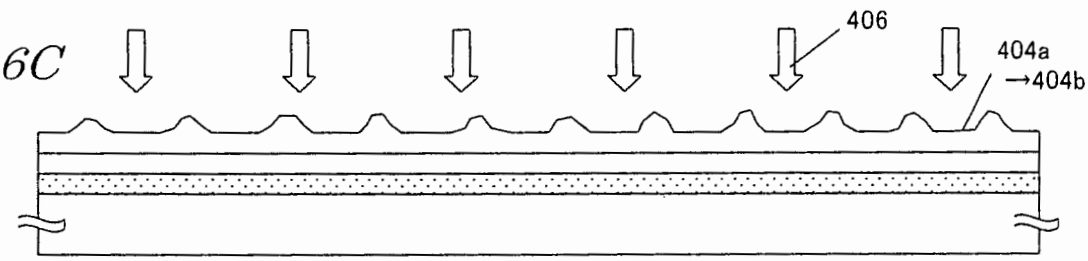


FIG.6D

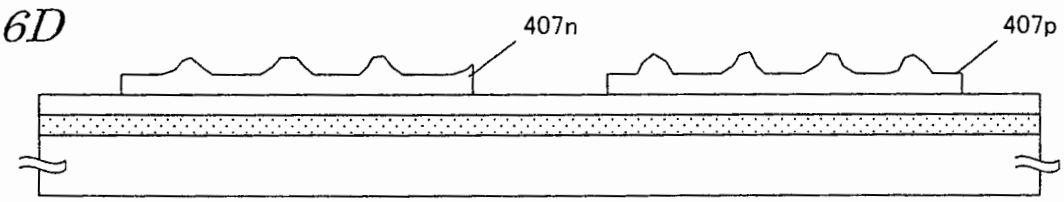


FIG.6E

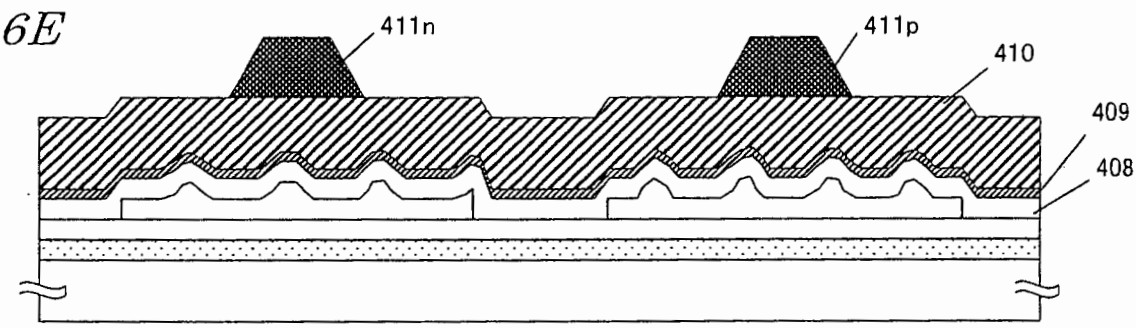


FIG.6F

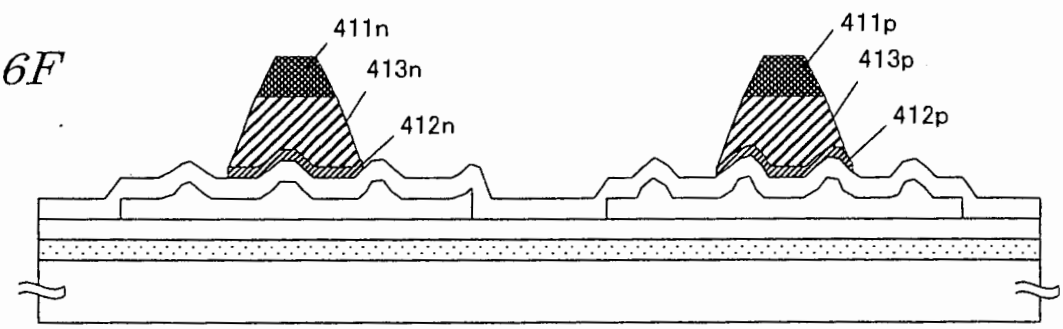


FIG.6G

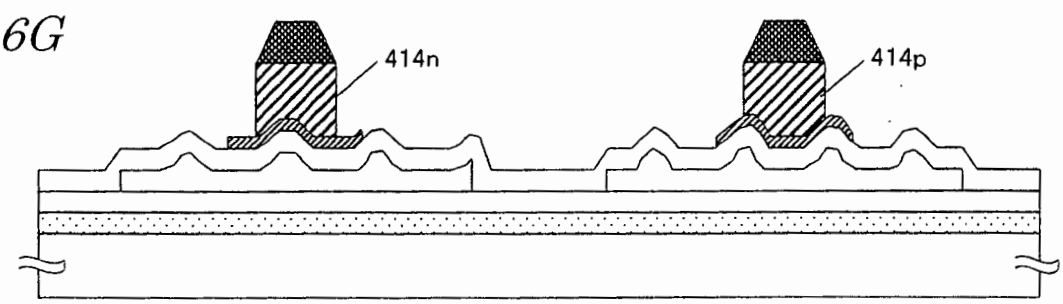


FIG. 7A

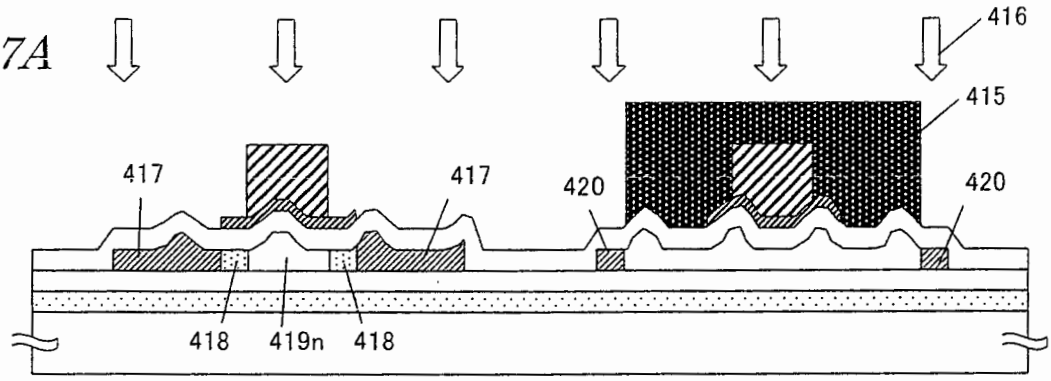


FIG. 7B

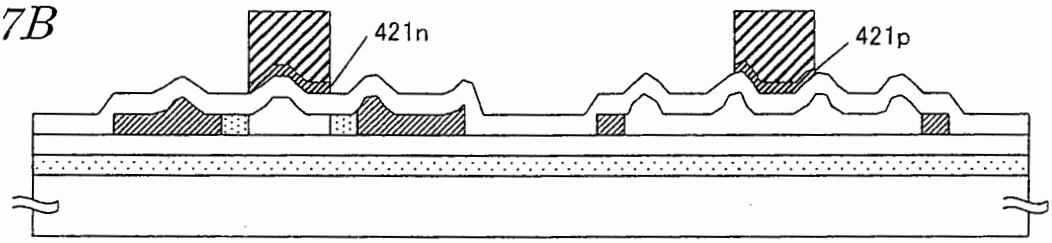


FIG. 7C

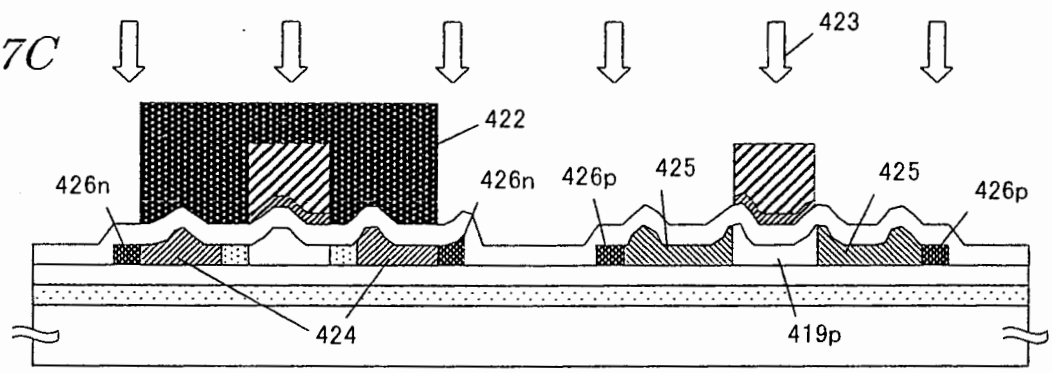


FIG. 7D

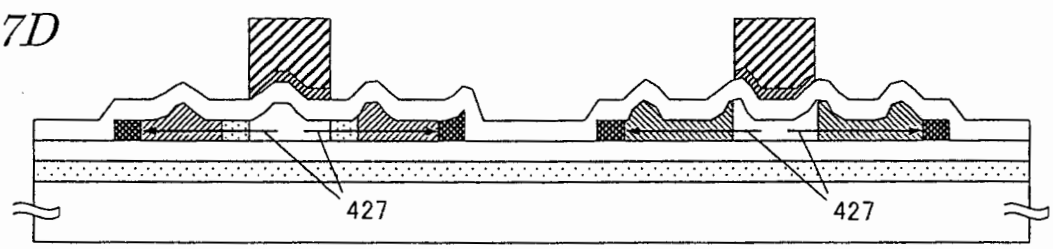


FIG. 7E

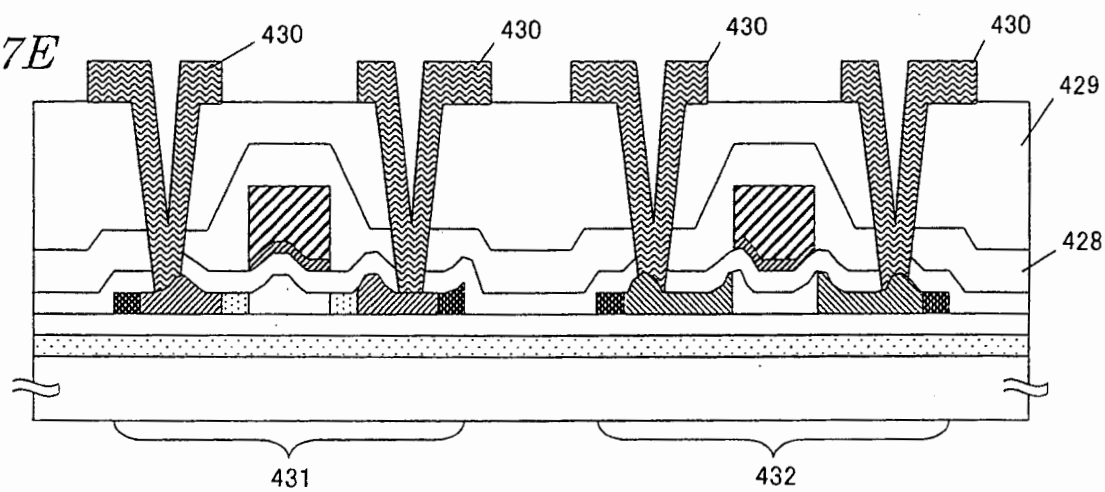


FIG. 8A

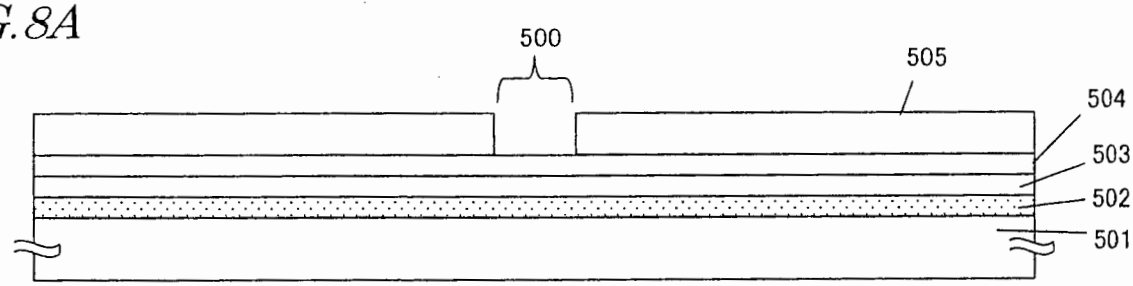


FIG. 8B

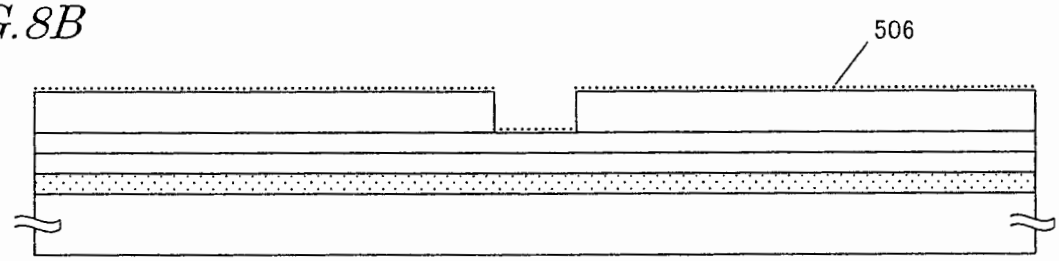


FIG. 8C

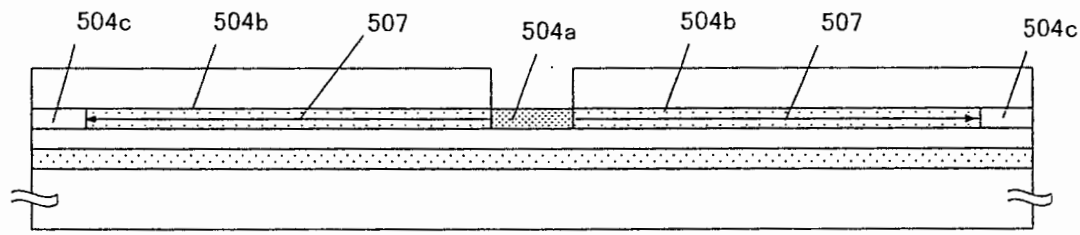


FIG. 8D

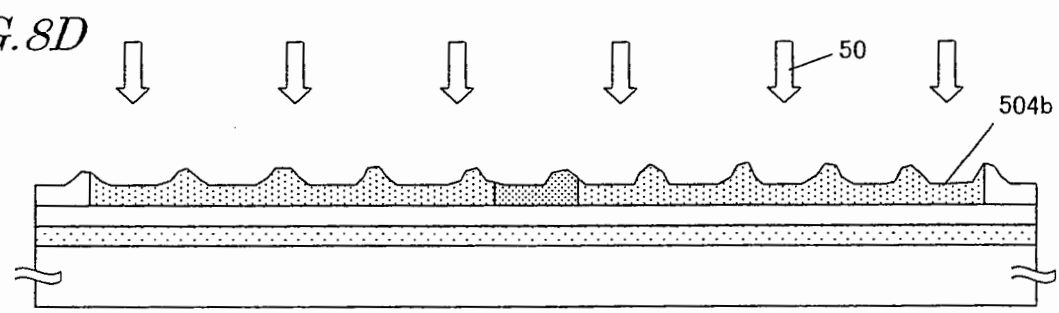


FIG. 8E

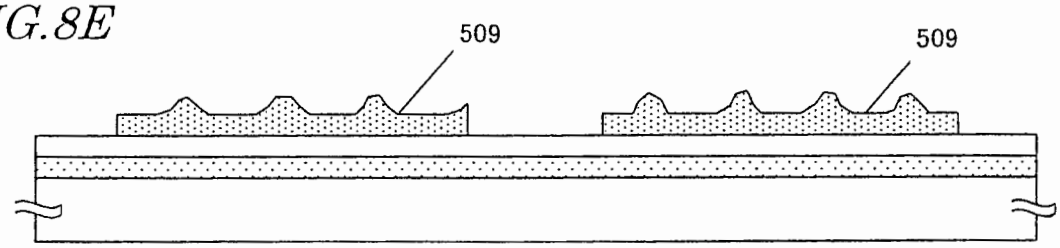


FIG.9A

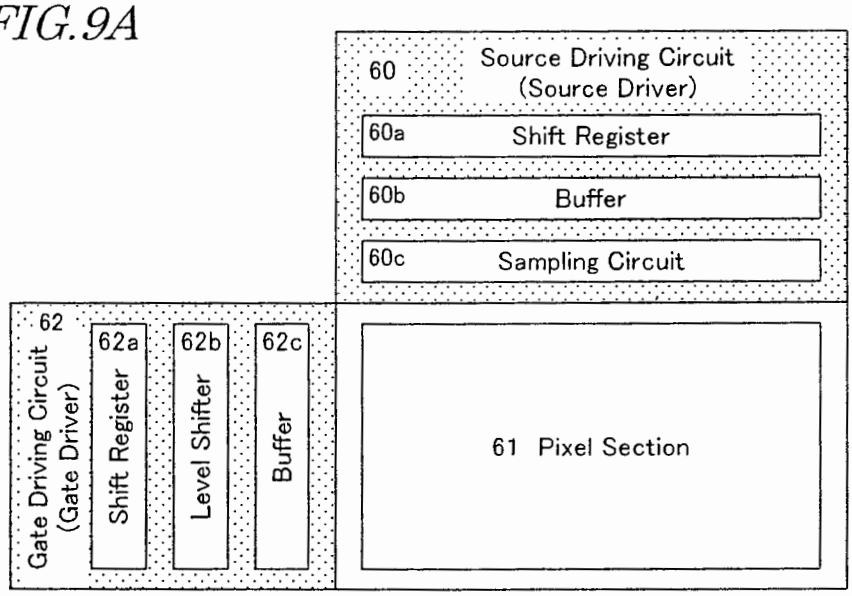

60
Source Driving Circuit
(Source Driver)
60a
Shift Register
60b
Buffer
60c
Sampling Circuit
62
Gate Driving Circuit
(Gate Driver)
62a
Shift Register
62b
Level Shifter
62c
Buffer
61 Pixel Section

FIG.9B

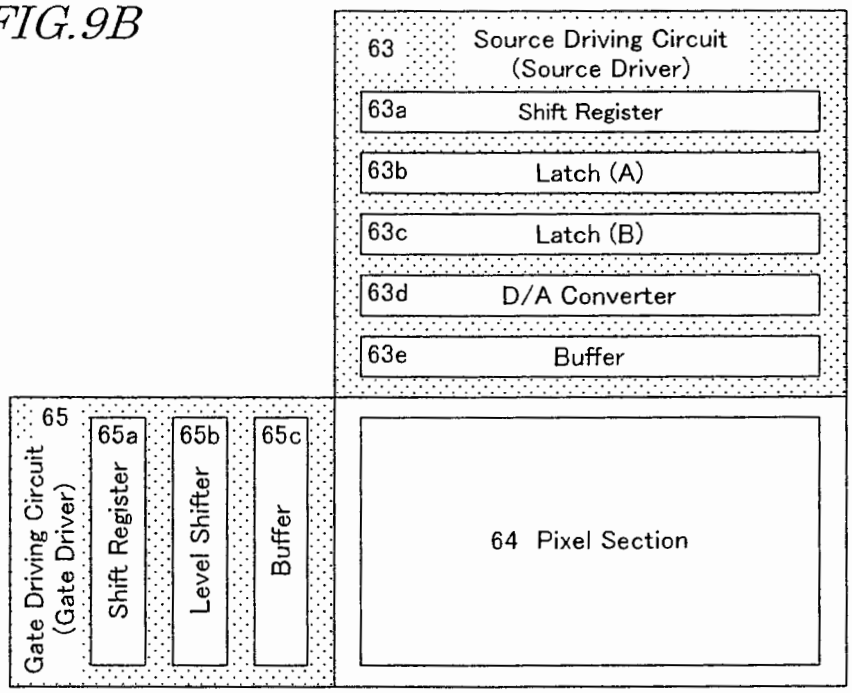

63
Source Driving Circuit
(Source Driver)
63a
Shift Register
63b
Latch (A)
63c
Latch (B)
63d
D/A Converter
63e
Buffer
65
Gate Driving Circuit
(Gate Driver)
65a
Shift Register
65b
Level Shifter
65c
Buffer
64 Pixel Section

FIG.10A

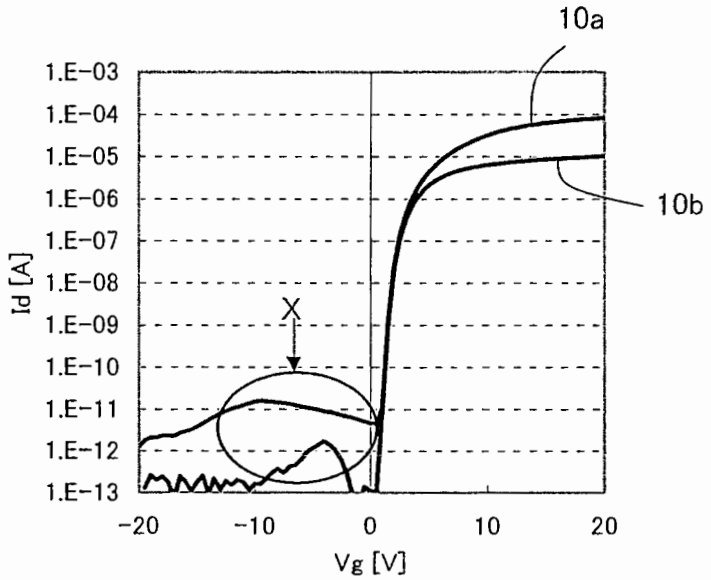


FIG.10B

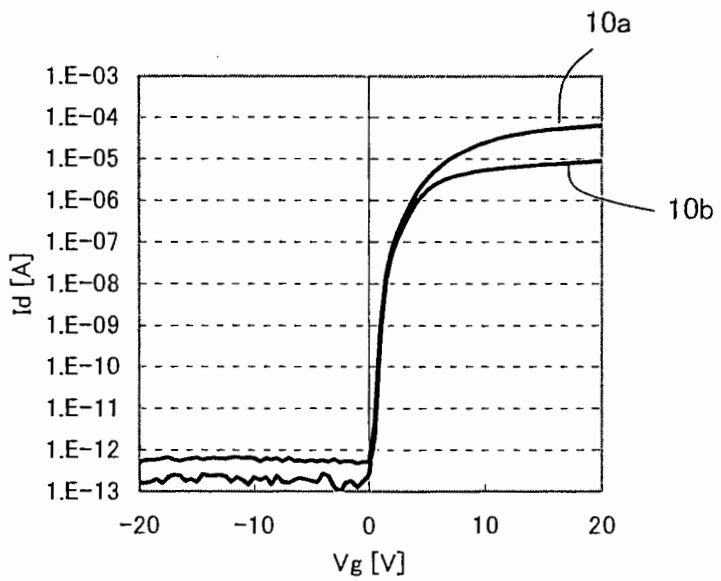


FIG. 11A

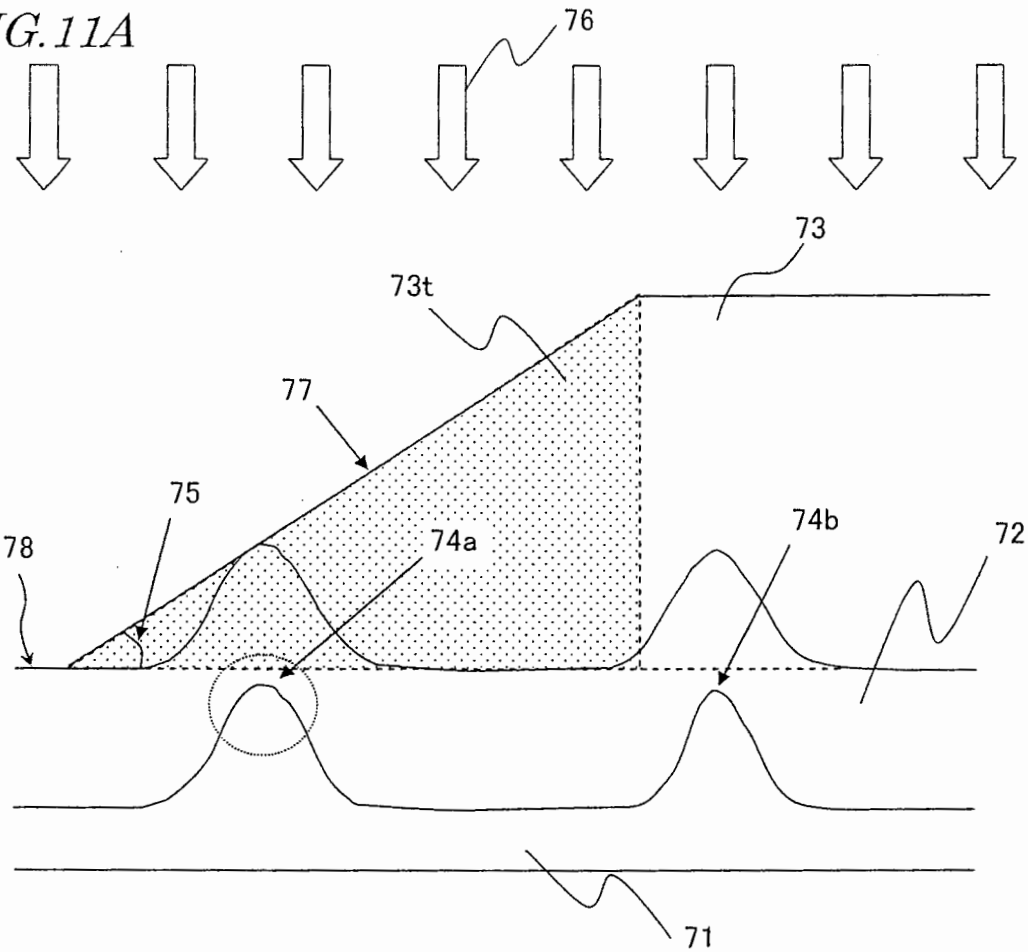


FIG. 11B

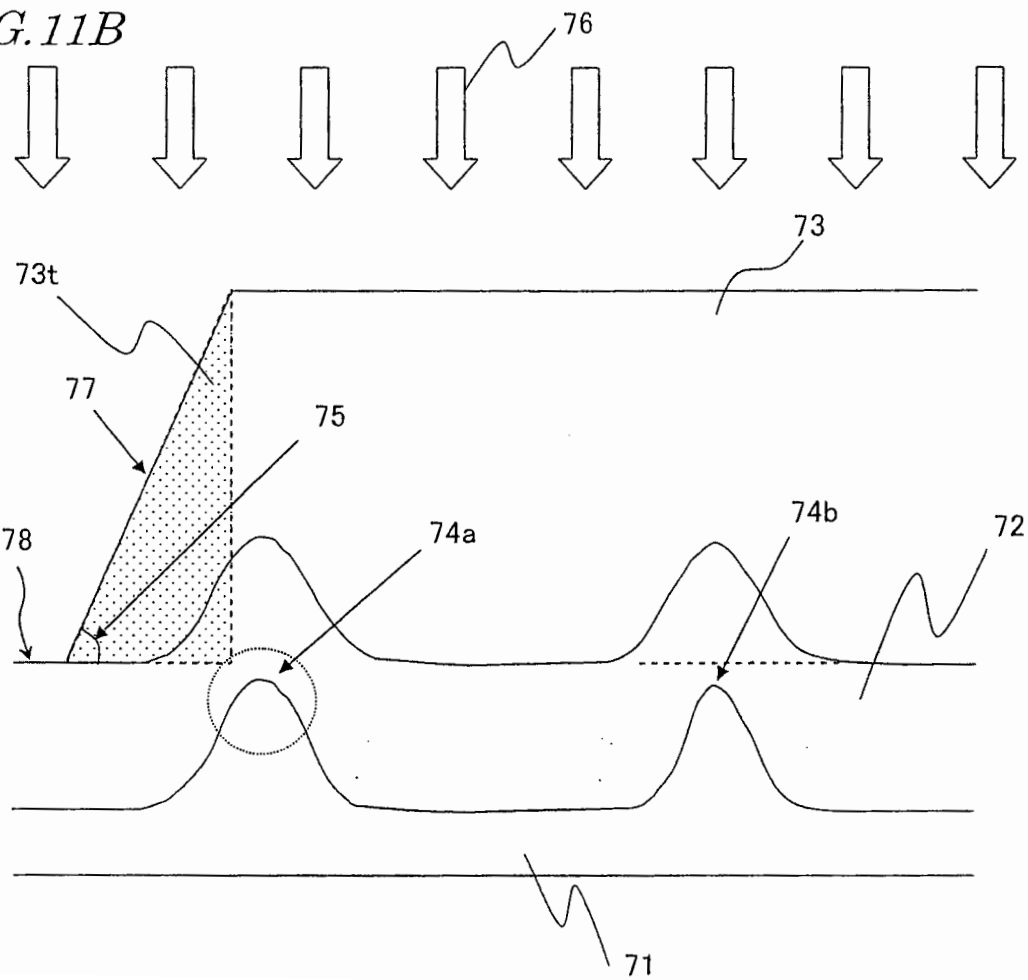


FIG. 12A

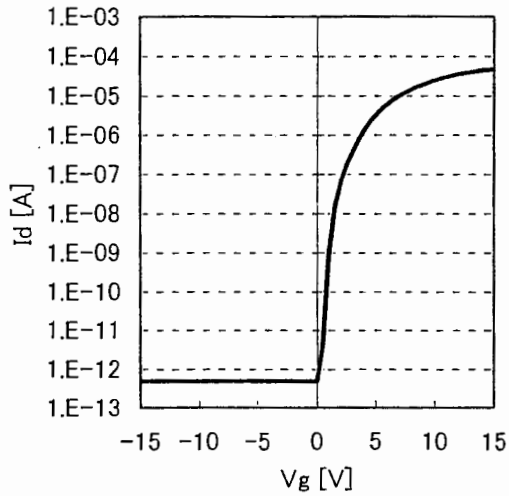


FIG. 12B

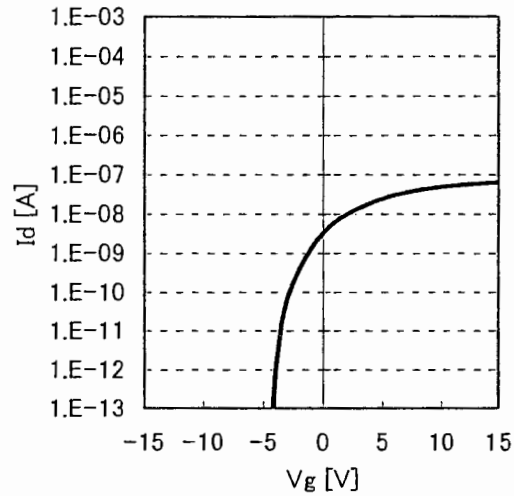


FIG. 12C

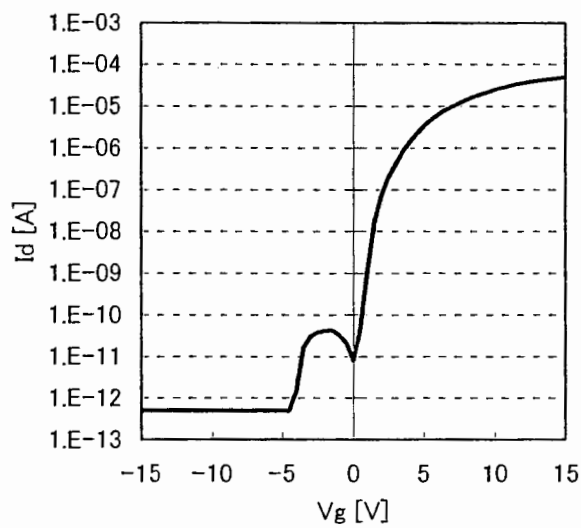


FIG. 13A

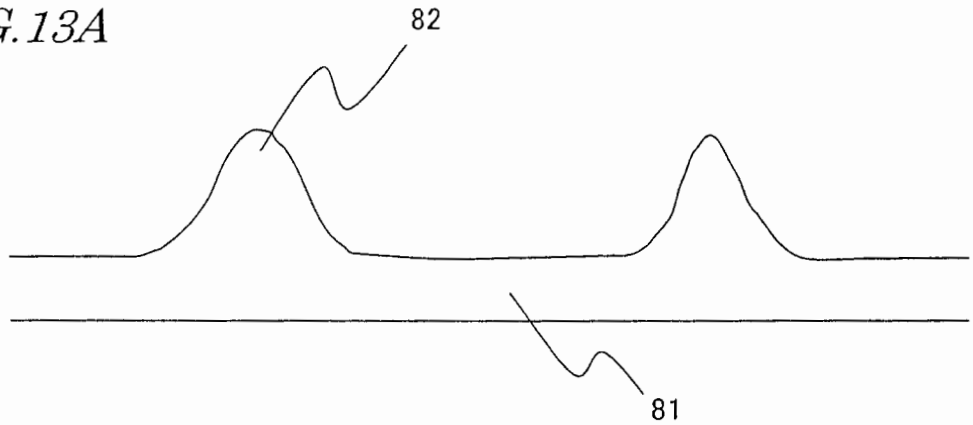


FIG. 13B

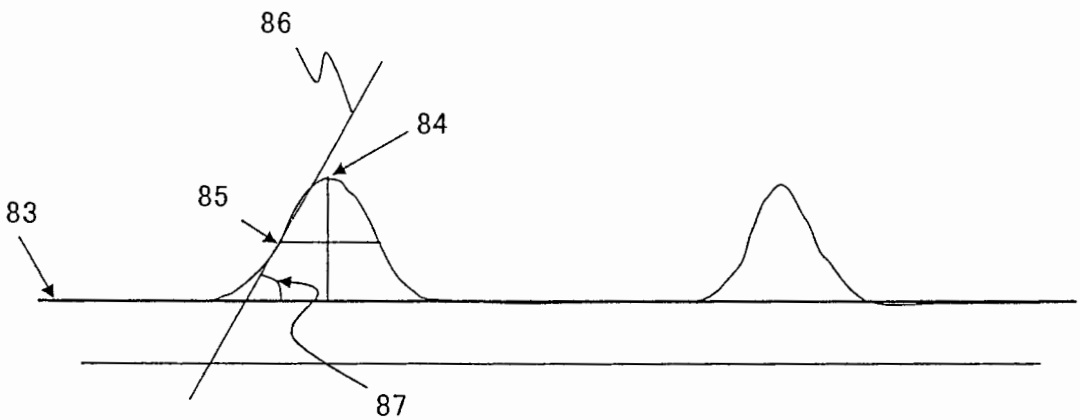


FIG. 14A

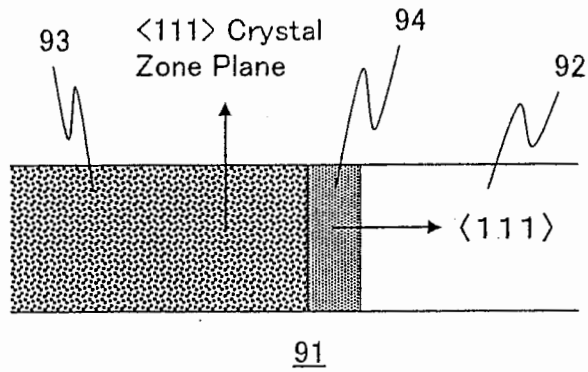


FIG. 14B

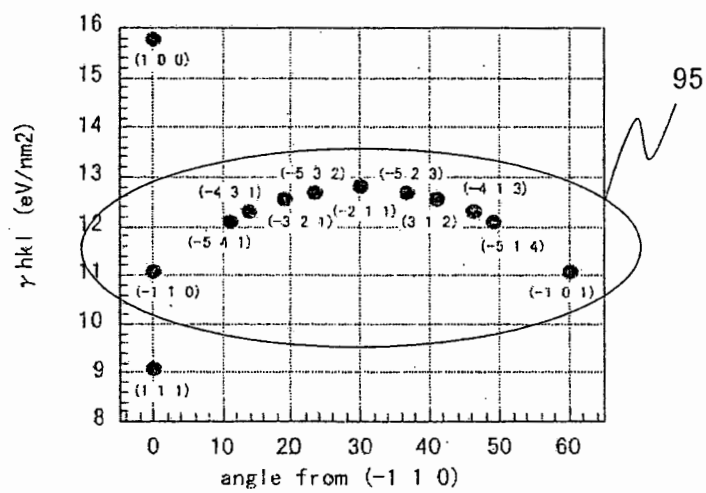
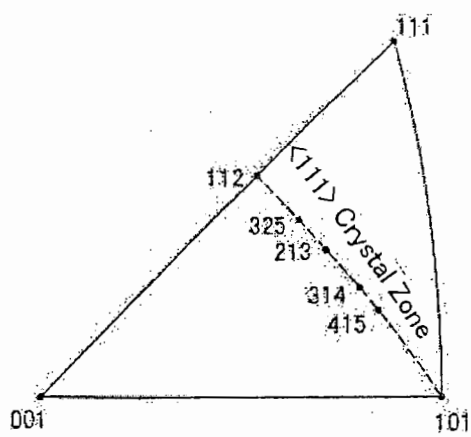


FIG. 14C



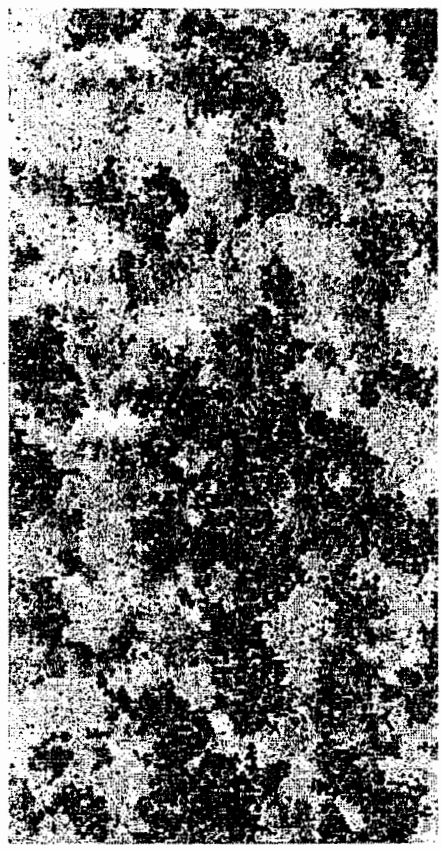


FIG.15A

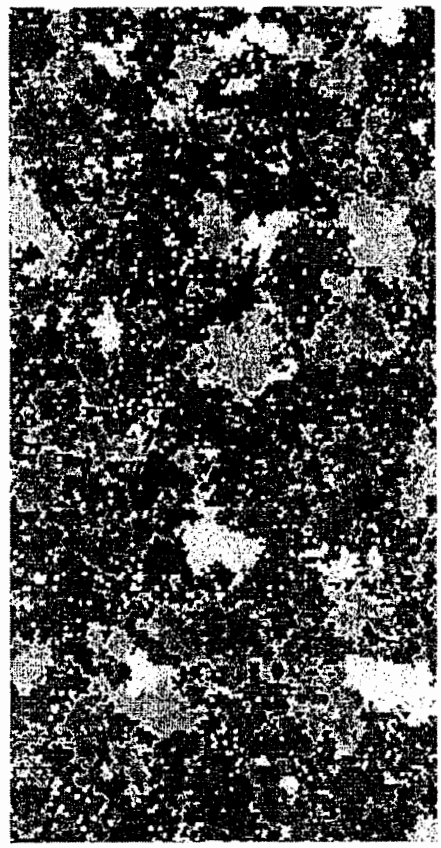


FIG.15B

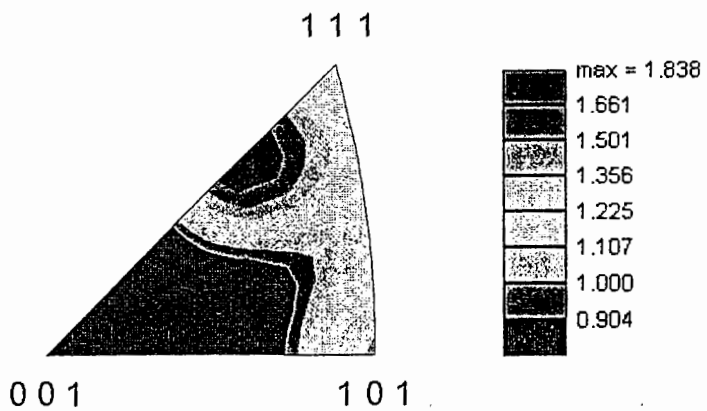


FIG.15C