THIS SHEET HAS BEEN REPLACED

This Sheet Has Been Replaced



ABSTRACT OF THE DISCLOSURE

ACTIVE MATRIX DISPLAY DEVICE

The active matrix display device has a plurality of gate lines Y provided on horizontal lines, a plurality of data lines X provided on vertical lines, and a plurality of picture elements provided at each intersection of both lines. The picture elements PXL provided horizontally constitute the display region 1. The vertical scanning circuit 2 scans vertically each gate line X sequentially, and selects picture elements on one horizontal line every one horizontal period. The horizontal scanning circuit 3 scans each data line Y sequentially in one horizontal period, samples image signal Vsig, and writes by dot sequential scanning the image signal Vsig on picture elements PXL on selected one horizontal line. The data lines are defined to two sections of real data lines Y1, Y2, ...,YL provided in the display region 1 and dummy data lines YD1, YD2, YD3, and YD4 provided outside the display region which dummy data lines intersect with the end section of the data lines. The horizontal scanning circuit 3 scans horizontally on the real data lines Y1, Y2,...,YL with a sampling timing overlapping a plurality of the real data lines, subsequently continue to scan on the dummy data

THIS SHEET HAS BEEN REPLACED

lines YD1, YD2, YD3 and YD4 with the overlapped sampling timing.

Thereby band defect on the side end of the screen when an active matrix display device is driven dot sequentially is eliminated.

This Sheet Has Been Replaced