Abstract

Improved Net Block Assembly

The invention provides a system for positioning integrated circuits singulated from a substrate, the system including a net block having a surface for receiving the integrated circuits, said surface at least partially covered by a two dimensional array of alternating first and second elements arranged in a chequer pattern. Each first element is adapted to receive an integrated circuit and each second element projecting from the surface. Further included is a transport means for transporting the integrated circuits to the net block and positioning a first quantity of the singulated integrated circuits in register with respective first elements; and urging means for urging the portion of integrated circuits into the respective first elements.

[Fig. 10(d)]