ABSTRACT

The present disclosure provides a memory, which may include at least: memory arrays and word line drivers, where the word line drivers are configured to drive word lines in the memory arrays, the word lines include first-type word lines and second-type word lines, the word line drivers include first drivers and second drivers, the first drivers are configured to drive the first-type word lines, and the second drivers are configured to drive the second-type word lines; and connection bridges, including first connection bridges and second connection bridges, where the first-type word lines are connected to the first drivers via the first connection bridges, and the second-type word lines are connected to the second drivers via the second connection bridges; and an extension direction of each of the connection bridges is the same as a first direction, a direction of each of the first connection bridges toward the second connection bridges is perpendicular to the first direction, and the first direction is an extension direction of each of the word lines.

[FIG. 5]