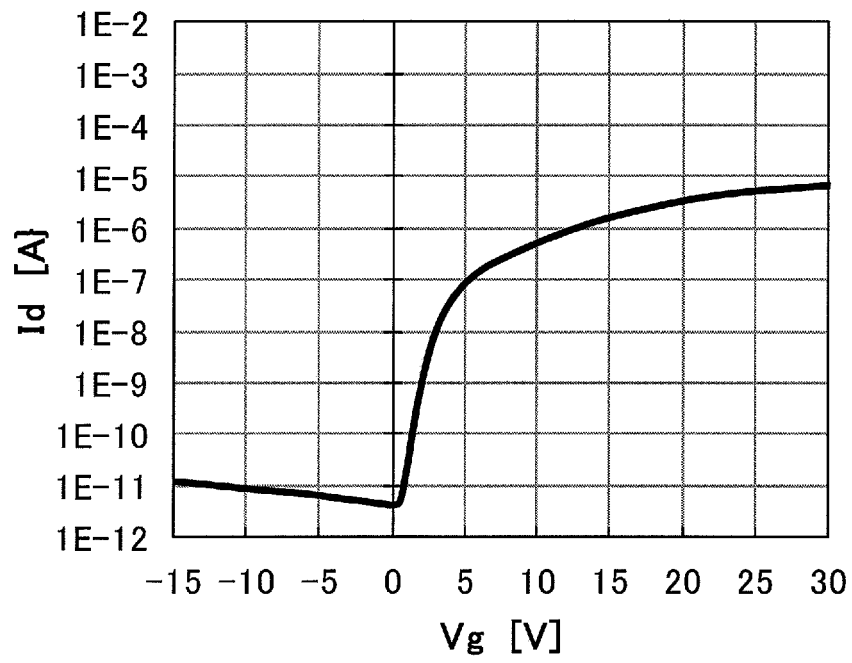


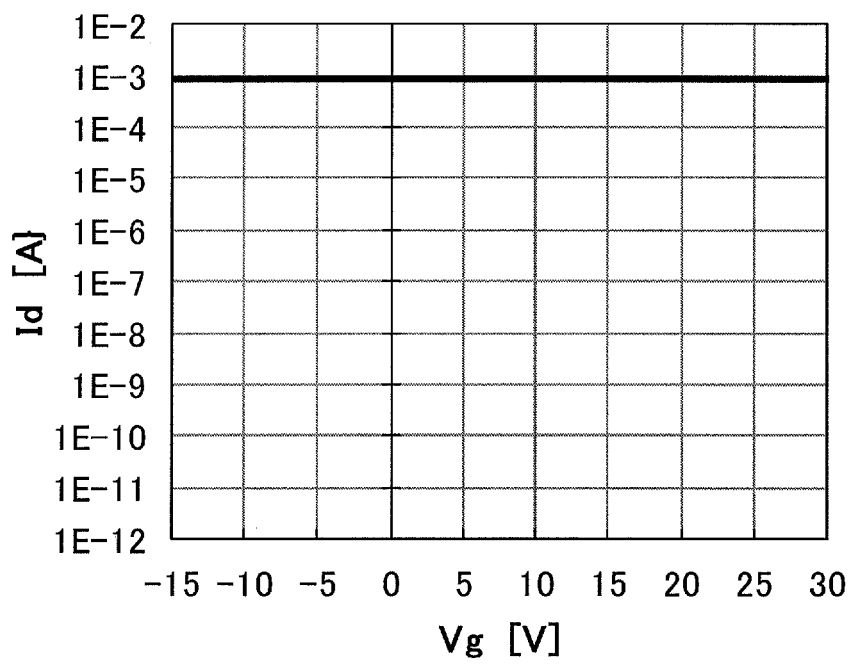
Figure 1 consists of three cross-sectional views of a display device. (a) is a top view showing a substrate 100A with a display area 3' and a peripheral area 3. A vertical line 5 is shown. (b) is a cross-sectional view along line A1-A1' showing layers 2, 3, 4a, 4b, 7, 8a, 8b, and 9. (c) is a cross-sectional view along line A1-A1' showing a pixel structure 500 with a gate line 200 and a data line 100A.

[図2]

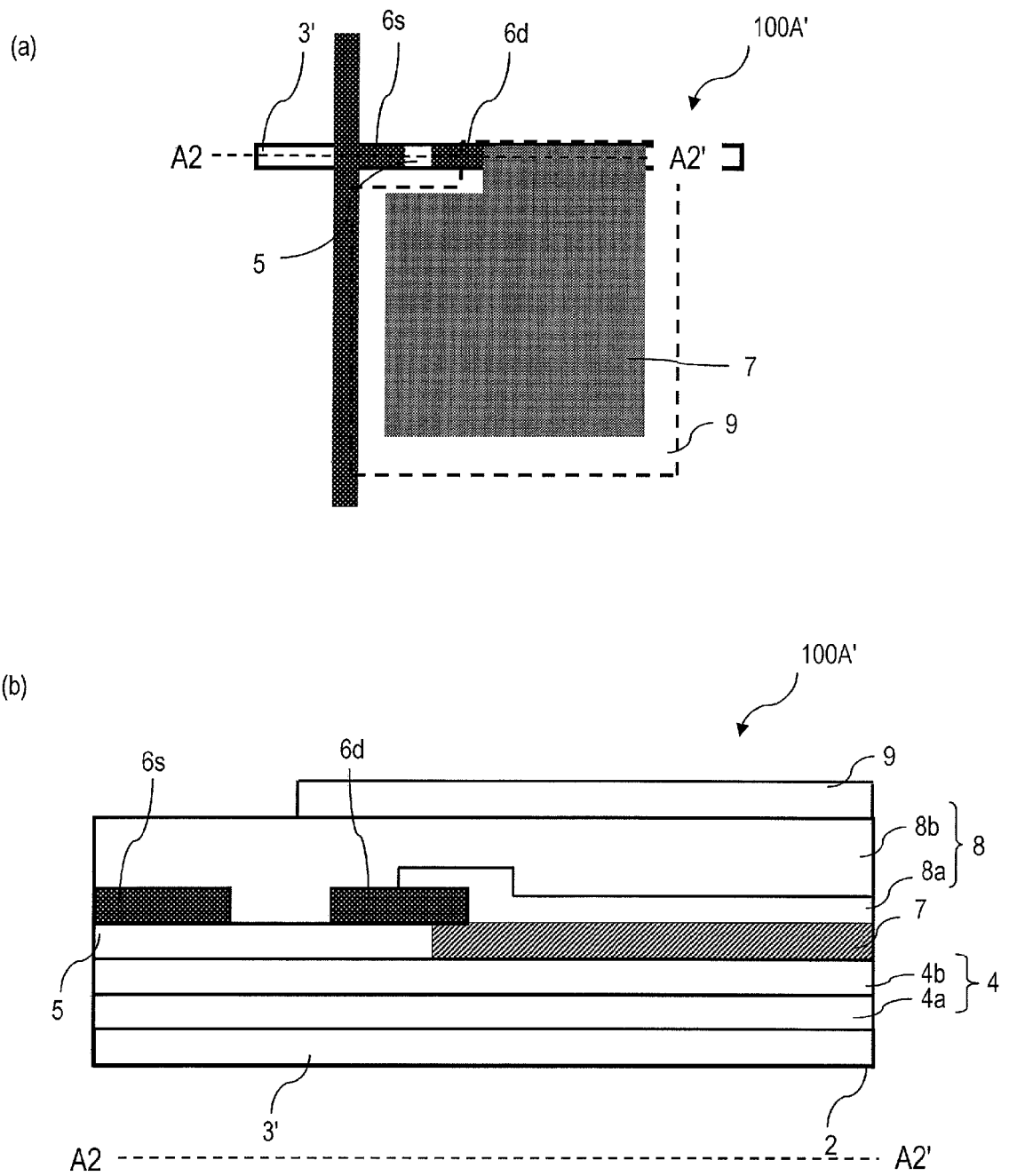
(a)



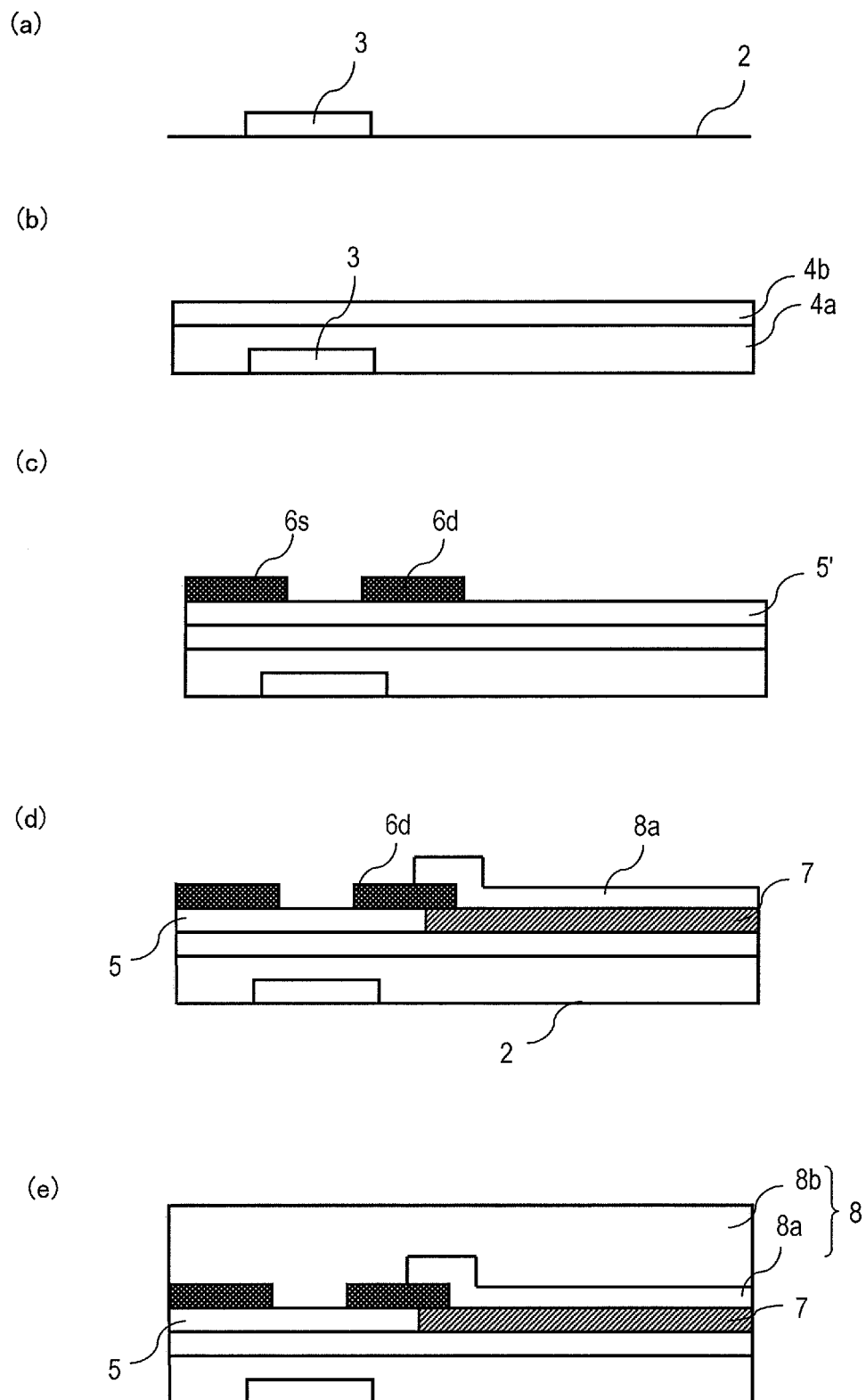
(b)



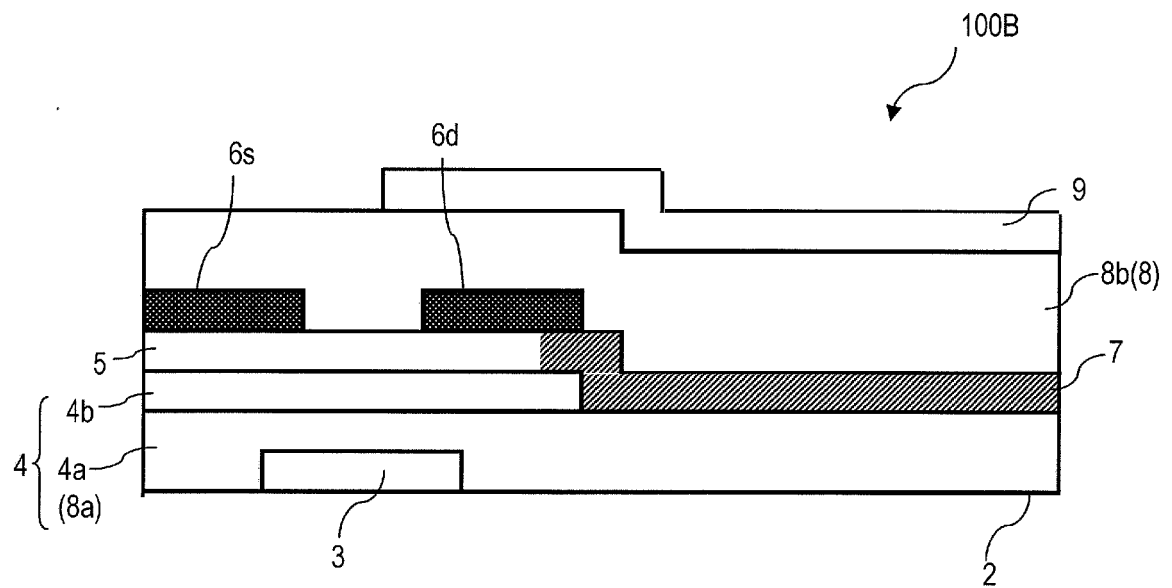
[図3]



[図4]

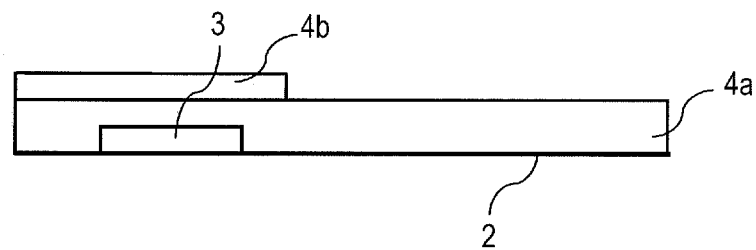


[図5]

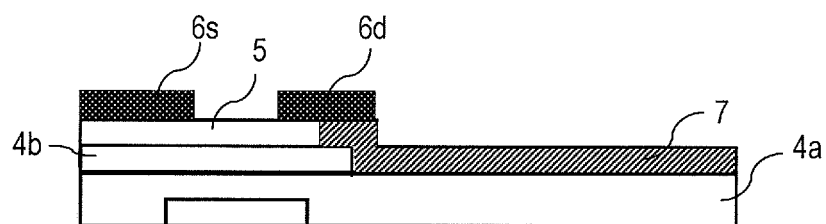


[図6]

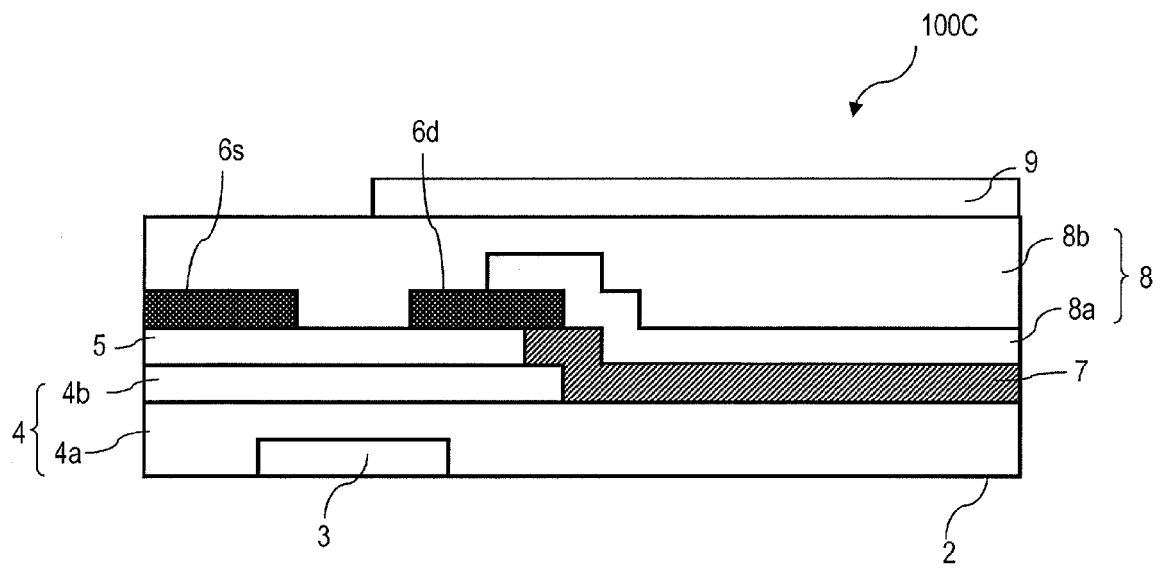
(a)



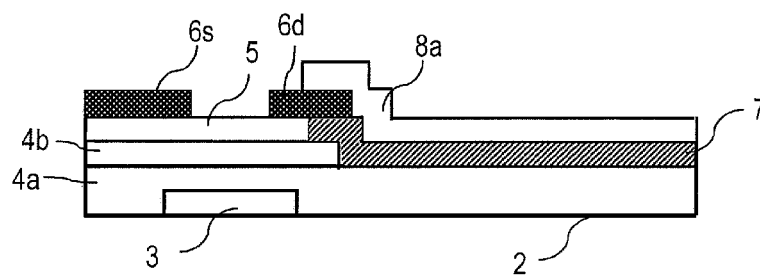
(b)



[図7]



[図8]



[図9]

