

# **CONTROL APPARATUS, MEMORY, SIGNAL PROCESSING METHOD, AND ELECTRONIC DEVICE**

## **ABSTRACT**

The embodiments of the present disclosure provide a control apparatus, a memory, a signal processing method, and an electronic apparatus. The control apparatus includes: a receiving module, configured to receive a read clock signal from the memory, and output the read clock signal; a clock module, configured to generate a first internal clock signal; a selection module, configured to receive the read clock signal and the first internal clock signal, and output one of the read clock signal and the first internal clock signal as a target read clock signal; and a latch module, configured to receive the target read clock signal and a read data signal sent by the memory, and perform latch processing on the read data signal by using the target read clock signal.

[Fig. 4]