

FIG. 1

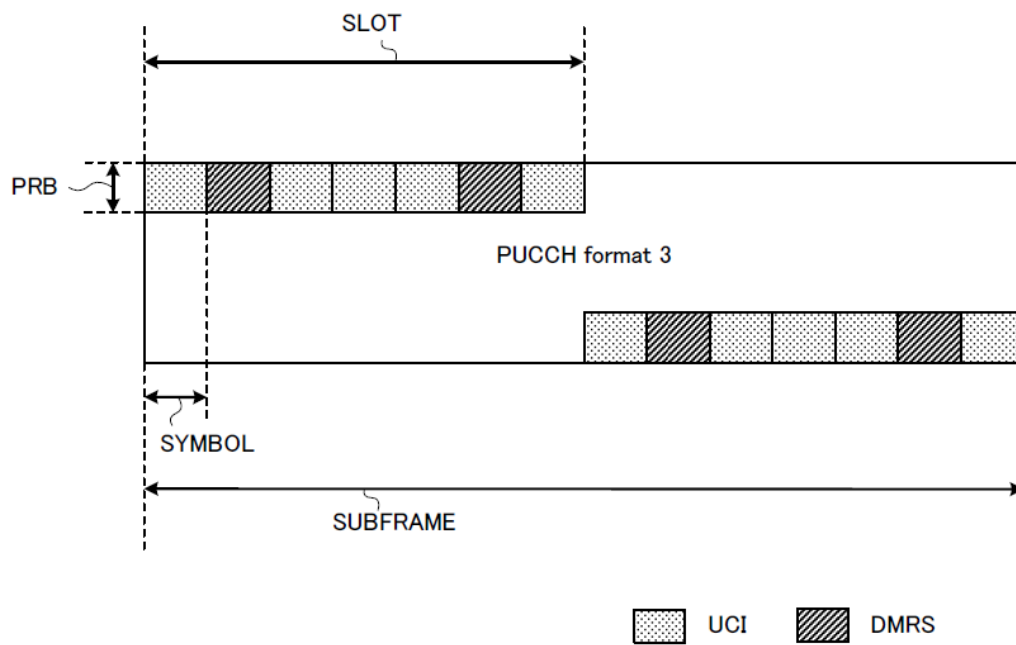


FIG. 2

FIG. 3A

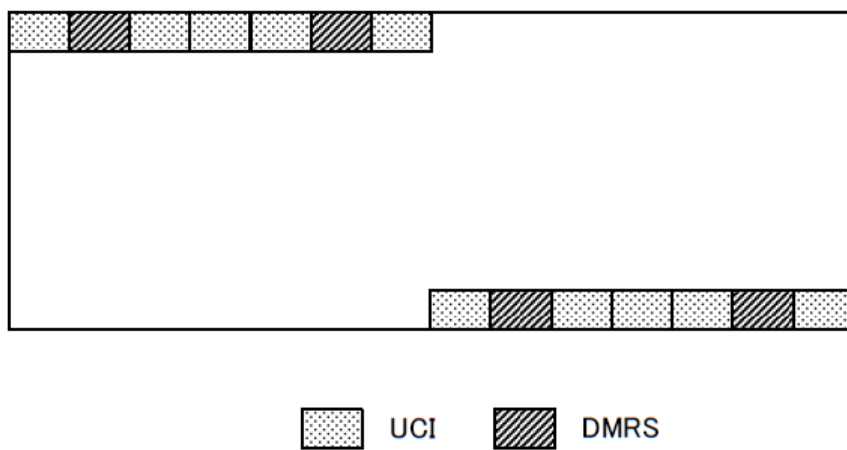


FIG. 3B

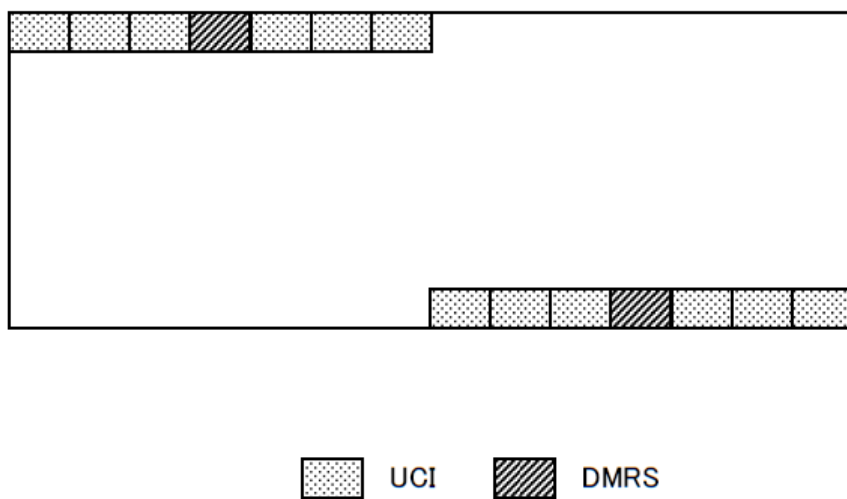


FIG. 4A

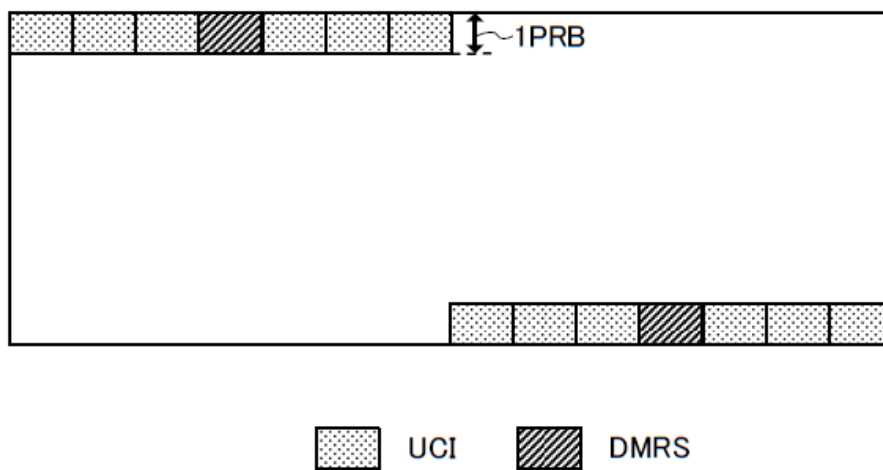


FIG. 4B

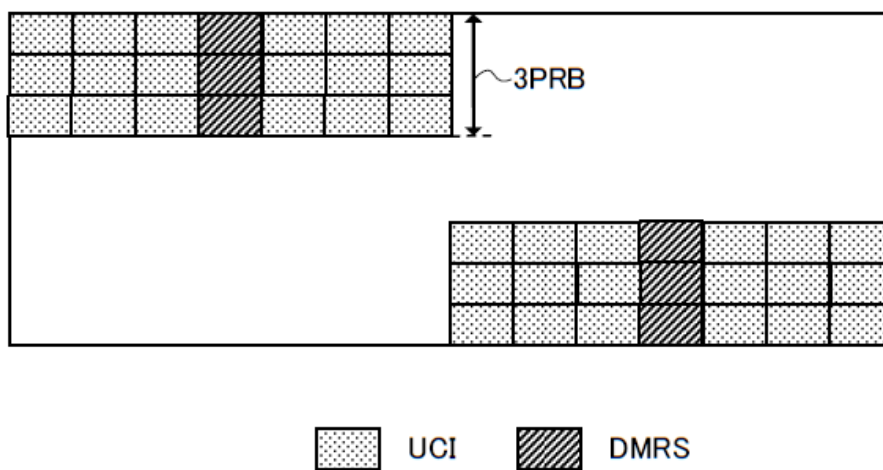


FIG. 5A

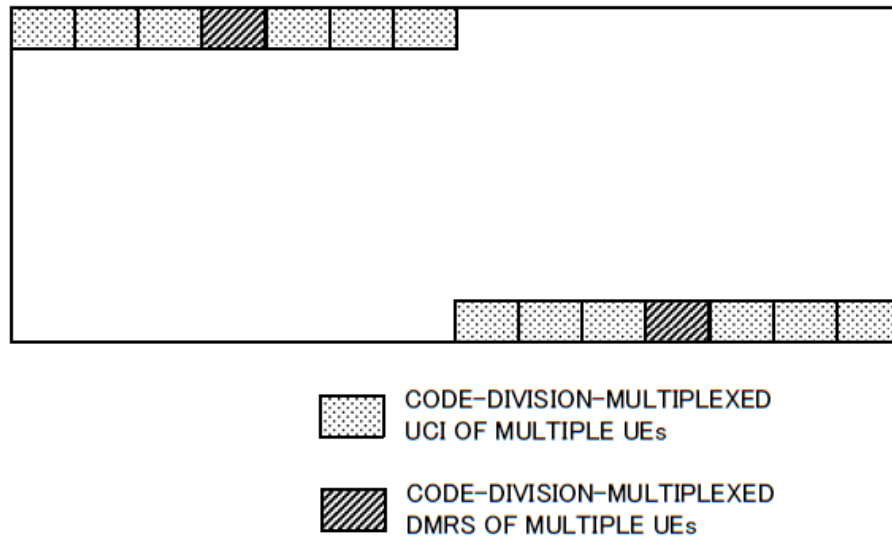


FIG. 5B

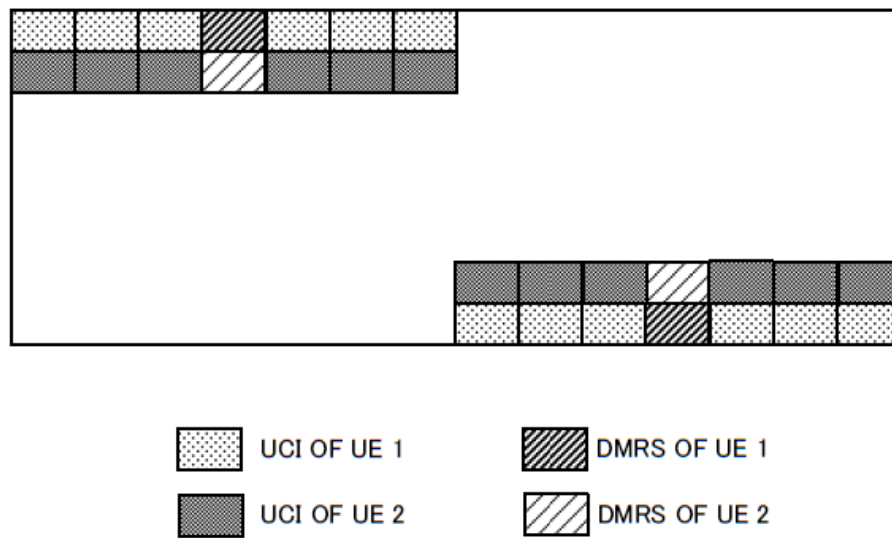


FIG. 6A

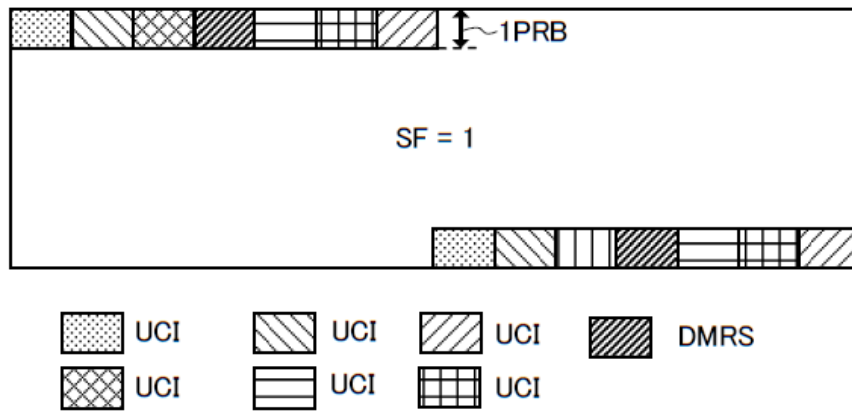


FIG. 6B

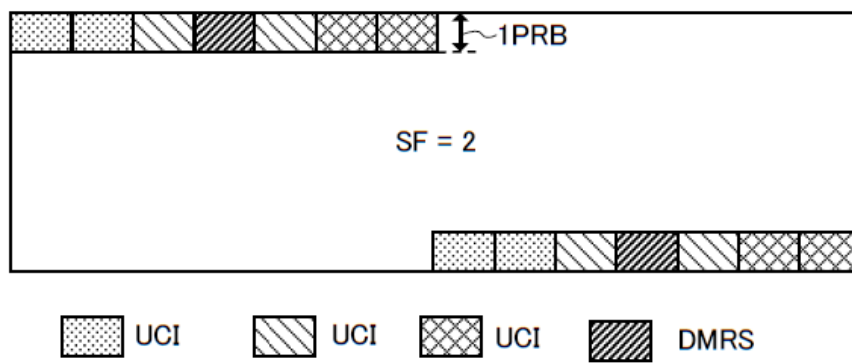
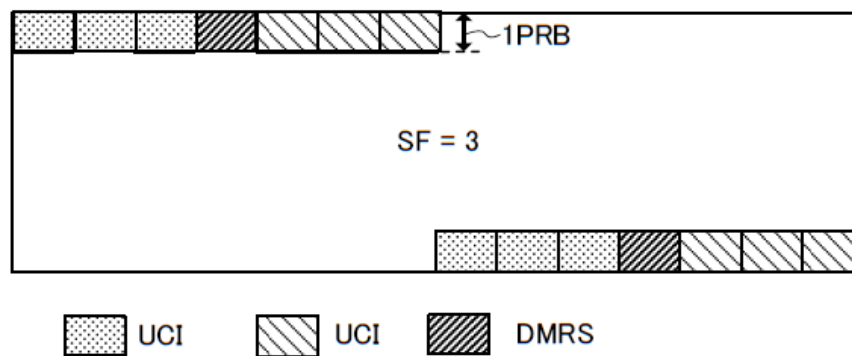


FIG. 6C



SF	PAYLOAD	MAXIMUM NUMBER OF UCI BITs
1	288	64
2	144	28
3	96	16

FIG. 7

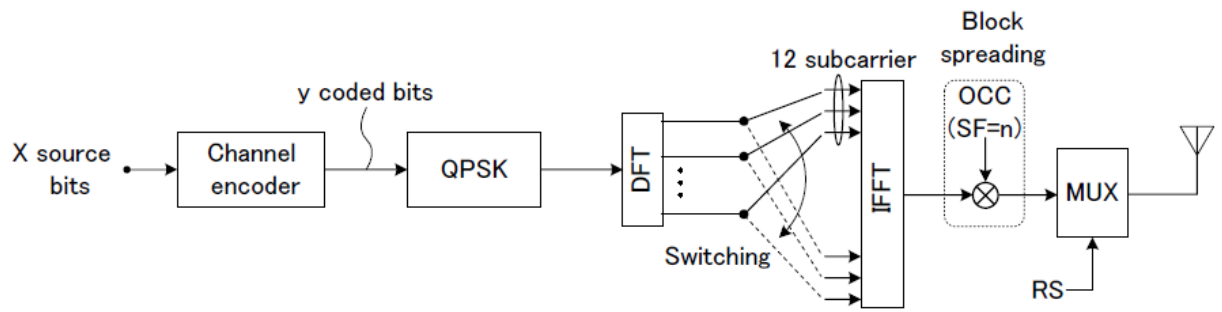


FIG. 8

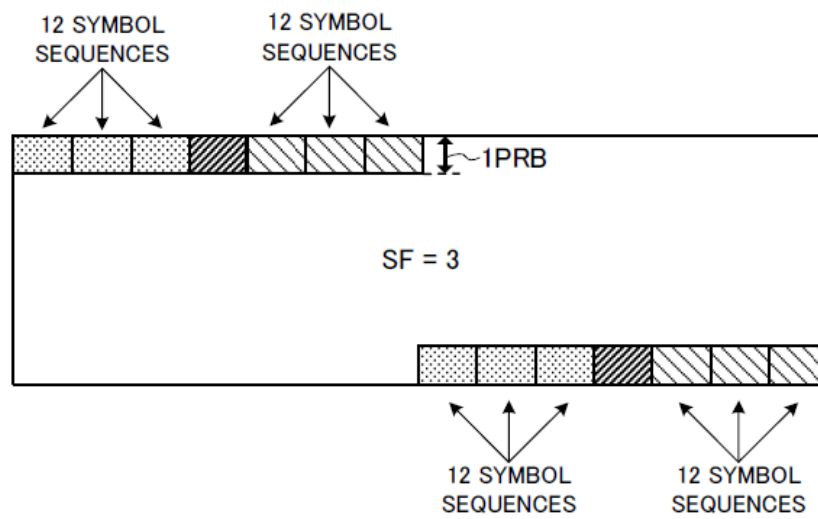


FIG. 9

FIG. 10A

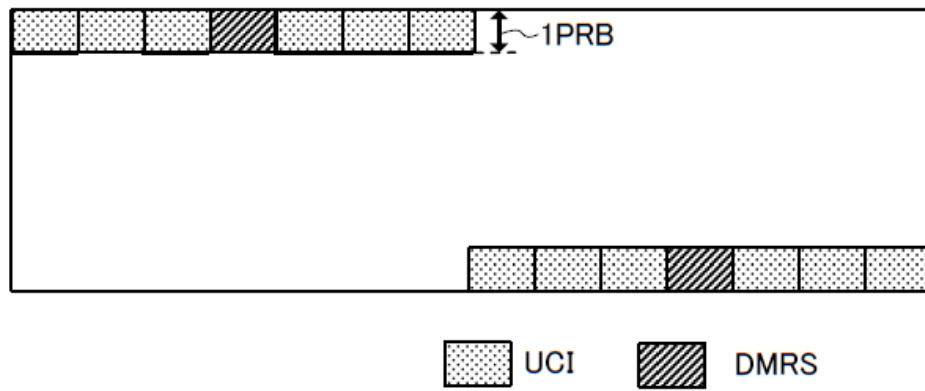


FIG. 10B

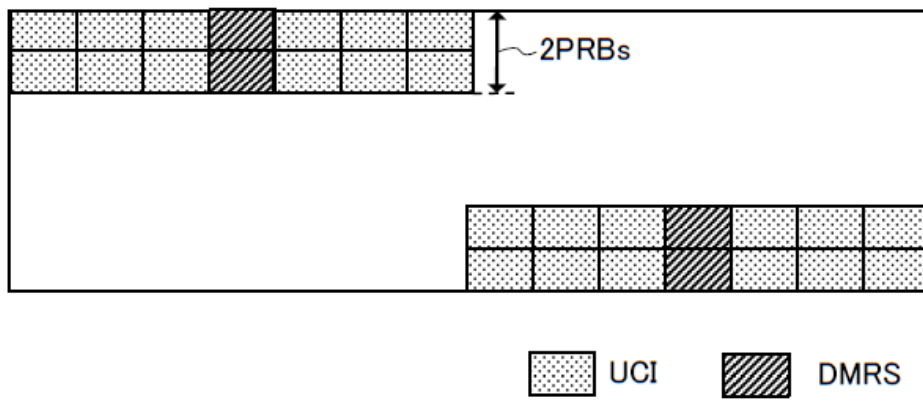
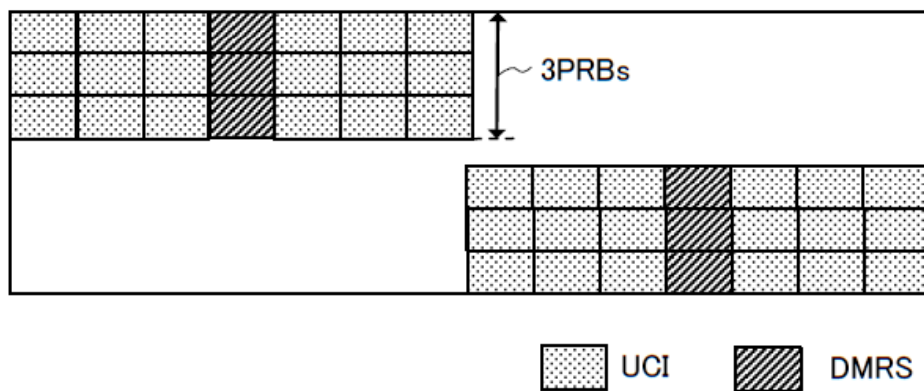


FIG. 10C



NUMBER OF PRBs	SF	PAYLOAD	MAXIMUM NUMBER OF UCI BITS
1	1	288	64
2	1	576	112
3	1	864	172

FIG. 11

FIG. 12A

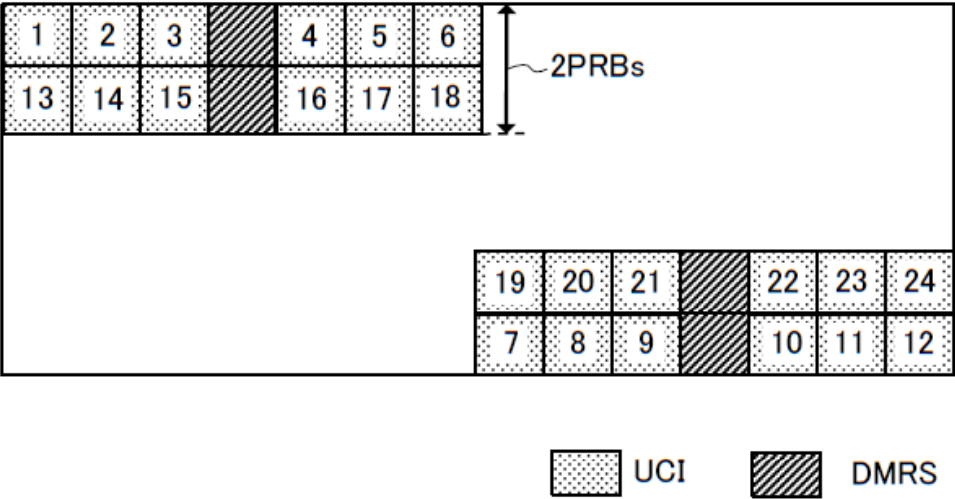
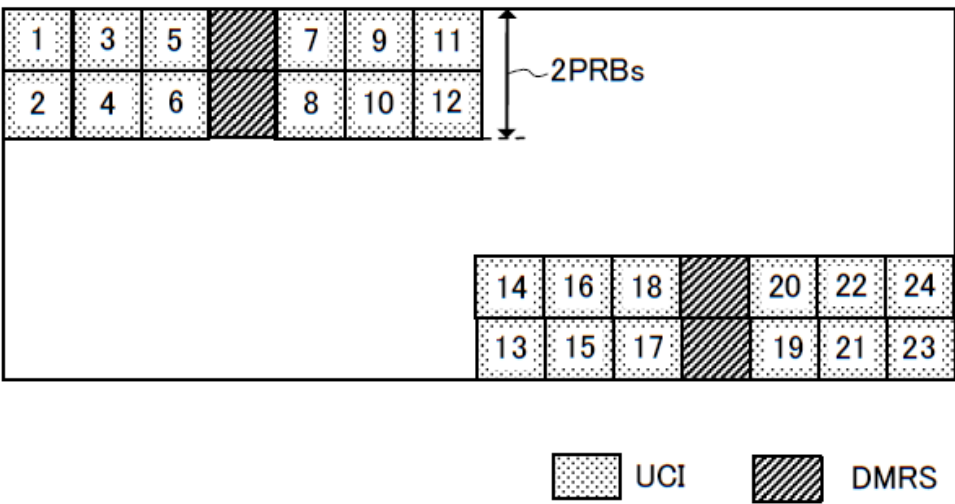


FIG. 12B



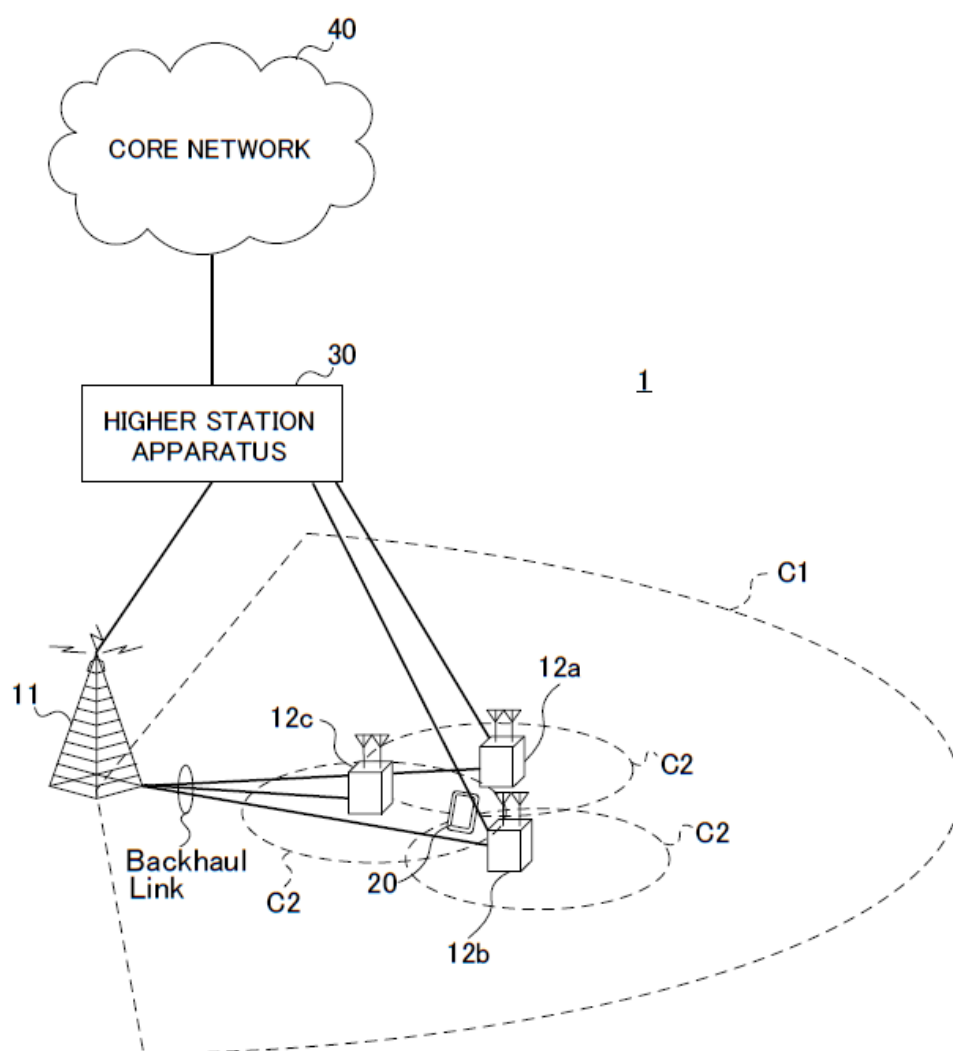


FIG. 13

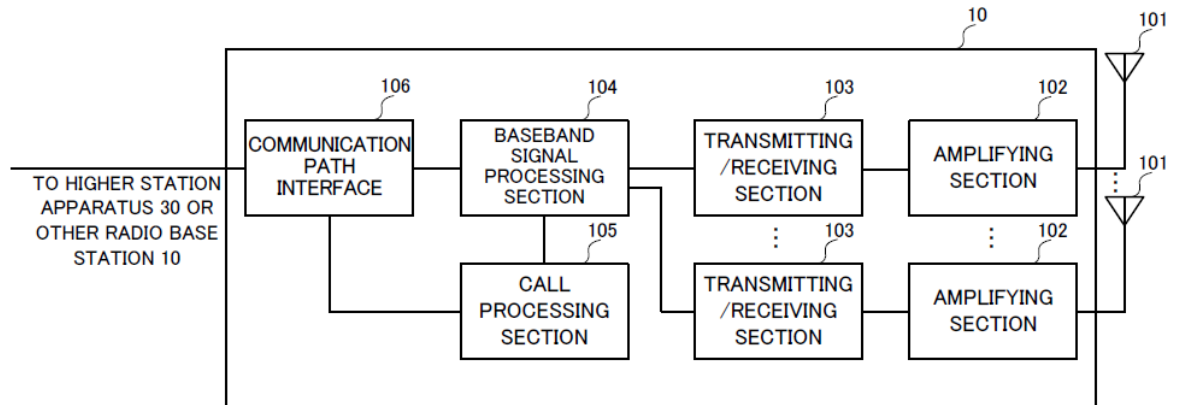


FIG. 14

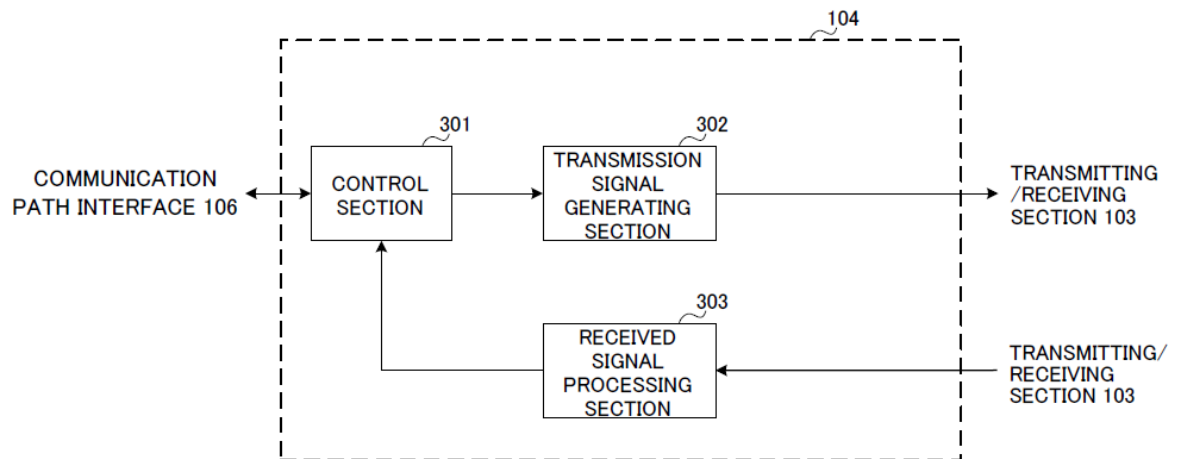


FIG. 15

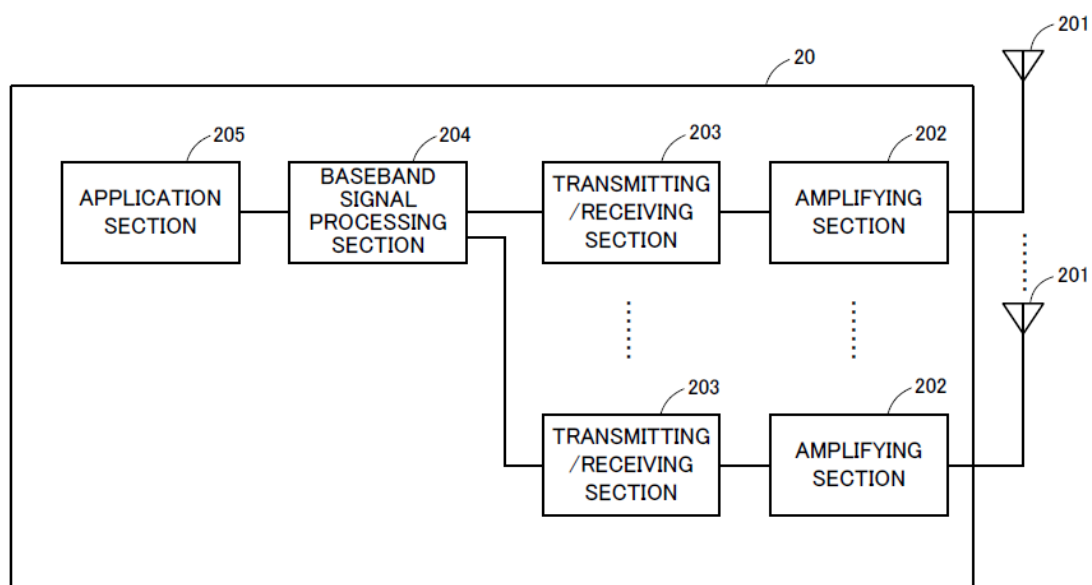


FIG. 16

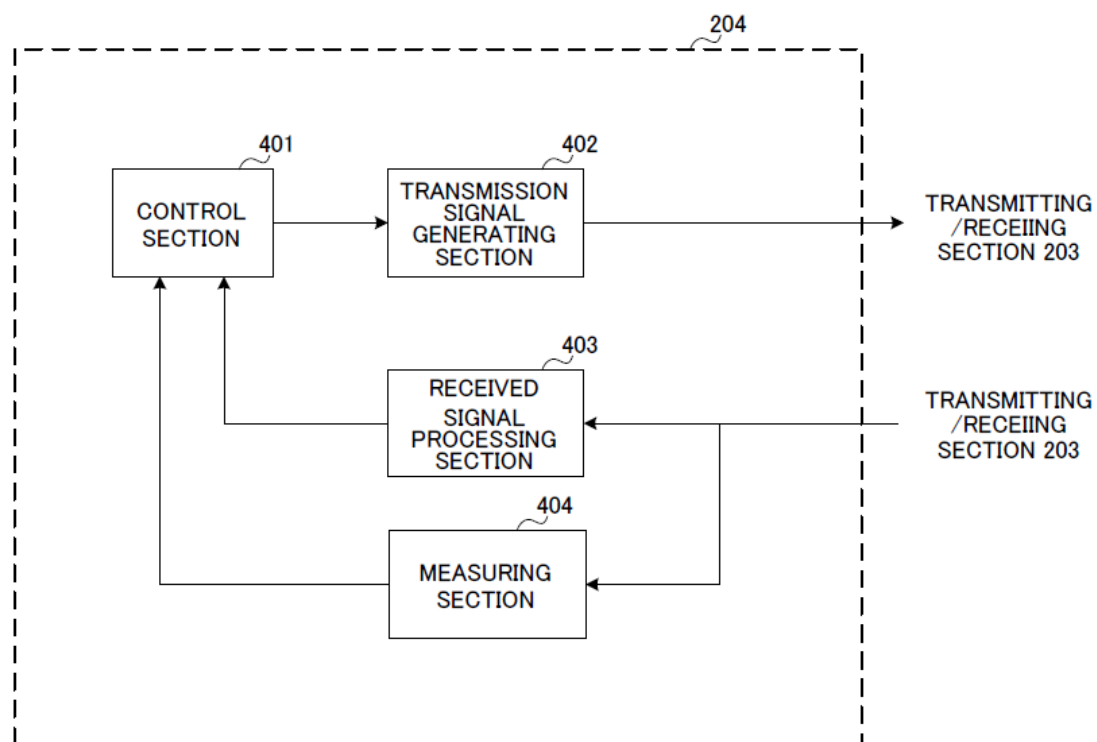


FIG. 17