

LEVEL CONVERSION CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE EMPLOYING THE LEVEL CONVERSION CIRCUIT

ABSTRACT OF THE DISCLOSURE

In a level conversion circuit mounted in an integrated circuit device using a plurality of high- and low-voltage power supplies, the input to the differential inputs are provided. In a level-down circuit, MOS transistors that are not supplied with 3.3 V between the gate and drain and between the gate and source use a thin oxide layer. In a level-up circuit, a logic operation function is provided.

Figures 3a



\*162162\*



\*G00002\*