

ABSTRACT

A storage device includes a nonvolatile memory device including an input/output register, the nonvolatile memory device sensing data from a memory region corresponding to an address and transmitting the data that is sensed from the memory region, and a storage controller that transmits a command and the address to the nonvolatile memory device via a first bus and that transmits and receives data to and from the nonvolatile memory device via a second bus. The nonvolatile memory device, in response to a first command that is received from the storage controller, outputs a sensing status of first data and stores the first data in the input/output register.

FIG. 1