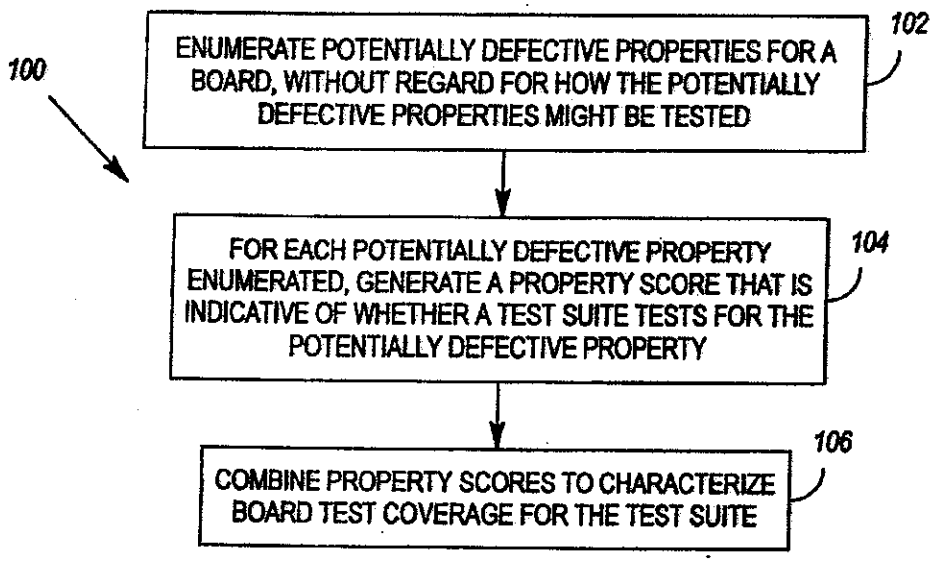

100
ENUMERATE POTENTIALLY DEFECTIVE PROPERTIES FOR A BOARD, WITHOUT REGARD FOR HOW THE POTENTIALLY DEFECTIVE PROPERTIES MIGHT BE TESTED
102
FOR EACH POTENTIALLY DEFECTIVE PROPERTY ENUMERATED, GENERATE A PROPERTY SCORE THAT IS INDICATIVE OF WHETHER A TEST SUITE TESTS FOR THE POTENTIALLY DEFECTIVE PROPERTY
104
COMBINE PROPERTY SCORES TO CHARACTERIZE BOARD TEST COVERAGE FOR THE TEST SUITE
106

FIG. 1

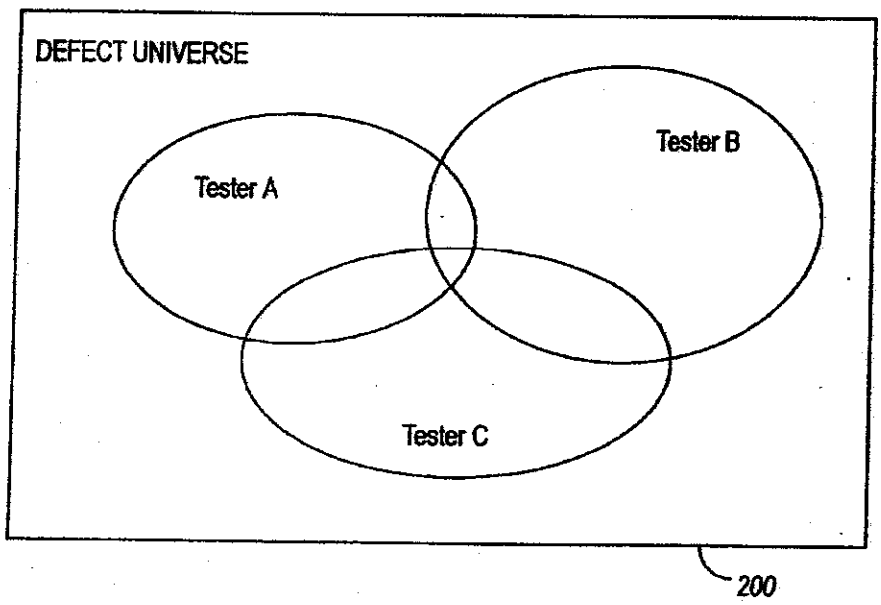

DEFECT UNIVERSE
Tester A
Tester B
Tester C
200

FIG. 2

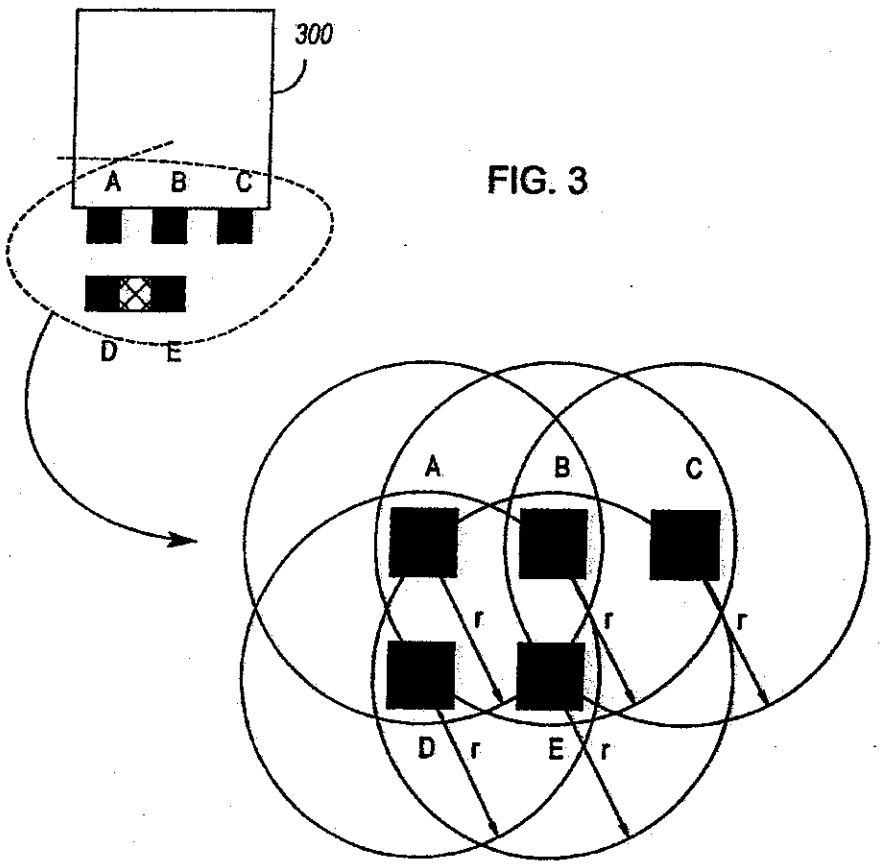

300
A
B
C
D
E
A
B
C
r
r
r
D
r
E
r

FIG. 3

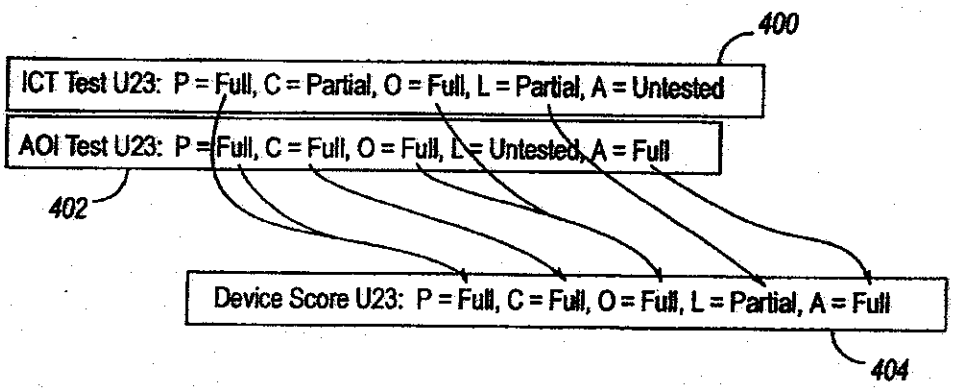
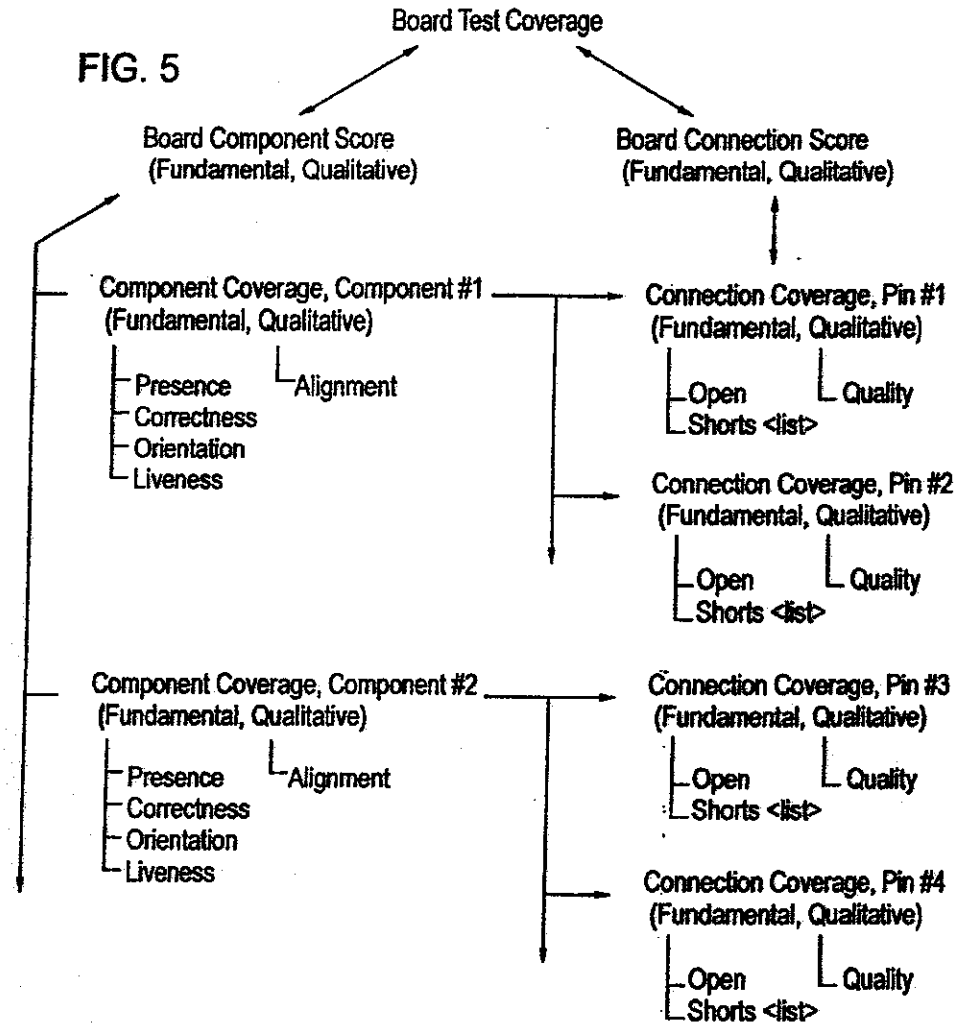

400
ICT Test U23: P = Full, C = Partial, O = Full, L = Partial, A = Untested
AOI Test U23: P = Full, C = Full, O = Full, L = Untested, A = Full
402
Device Score U23: P = Full, C = Full, O = Full, L = Partial, A = Full
404

FIG. 4

FIG. 5


Board Test Coverage
Board Component Score
(Fundamental, Qualitative)
Board Connection Score
(Fundamental, Qualitative)
Component Coverage, Component #1
(Fundamental, Qualitative)
Presence
Alignment
Correctness
Orientation
Liveness
Connection Coverage, Pin #1
(Fundamental, Qualitative)
Open
Quality
Shorts <list>
Connection Coverage, Pin #2
(Fundamental, Qualitative)
Open
Quality
Shorts <list>
Component Coverage, Component #2
(Fundamental, Qualitative)
Presence
Alignment
Correctness
Orientation
Liveness
Connection Coverage, Pin #3
(Fundamental, Qualitative)
Open
Quality
Shorts <list>
Connection Coverage, Pin #4
(Fundamental, Qualitative)
Open
Quality
Shorts <list>

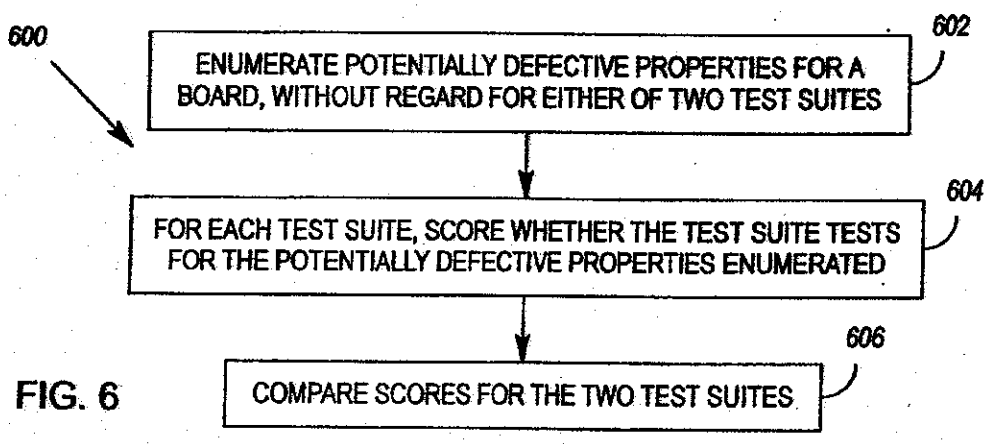

600
ENUMERATE POTENTIALLY DEFECTIVE PROPERTIES FOR A BOARD, WITHOUT REGARD FOR EITHER OF TWO TEST SUITES
602
FOR EACH TEST SUITE, SCORE WHETHER THE TEST SUITE TESTS FOR THE POTENTIALLY DEFECTIVE PROPERTIES ENUMERATED
604
COMPARE SCORES FOR THE TWO TEST SUITES
606

FIG. 6

MAXIMUM THEORETICAL COMPONENT PCOLA SCORES
VERSUS TEST TECHNOLOGY FOR A RESISTOR

Test Type	P	C	O	L	A
In-Circuit	Full	Partial	(NA)	Full	Untested
AOI	Full	Full	(NA)	Untested	Full
AXI	Full	Untested	(NA)	Untested	Full

FIG. 7

MAXIMUM THEORETICAL COMPONENT PCOLA SCORES
VERSUS TEST TECHNOLOGY FOR A DIGITAL DEVICE

Test Type	P	C	O	L	A
In-Circuit	Full	Partial	Full	Full	Untested
AOI	Full	Full	Full	Untested	Full
AXI	Full	Untested	Full	Untested	Full

FIG. 8

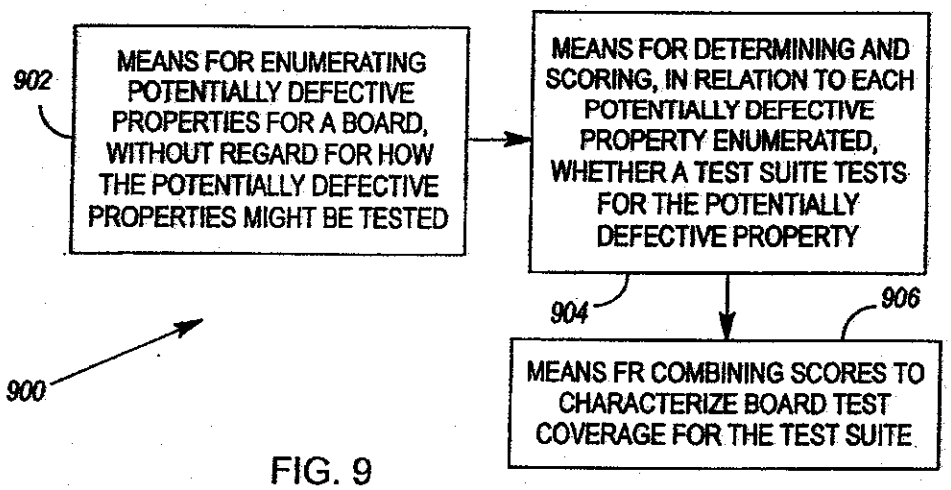


FIG. 9

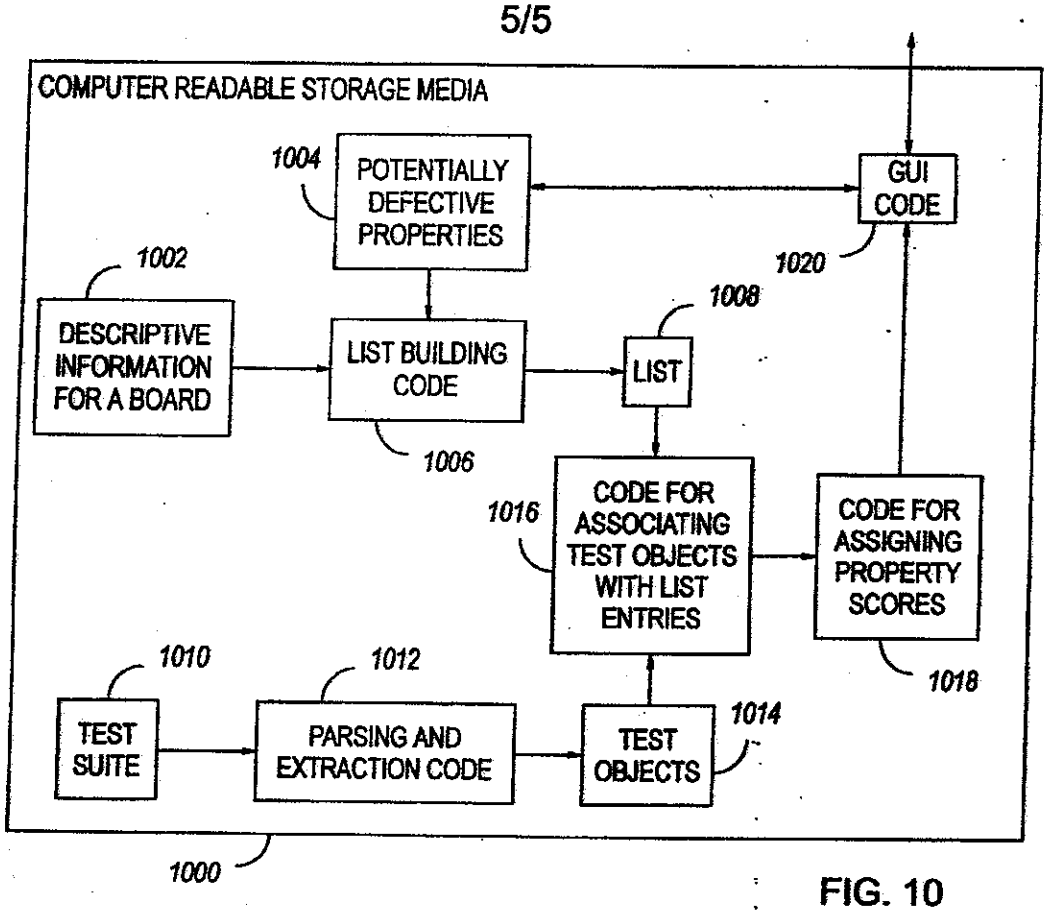

COMPUTER READABLE STORAGE MEDIA
1004
POTENTIALLY DEFECTIVE PROPERTIES
GUI CODE
1020
1002
DESCRIPTIVE INFORMATION FOR A BOARD
LIST BUILDING CODE
1008
LIST
1006
1016
CODE FOR ASSOCIATING TEST OBJECTS WITH LIST ENTRIES
CODE FOR ASSIGNING PROPERTY SCORES
1018
1010
1012
TEST SUITE
PARSING AND EXTRACTION CODE
TEST OBJECTS
1014
1000

FIG. 10

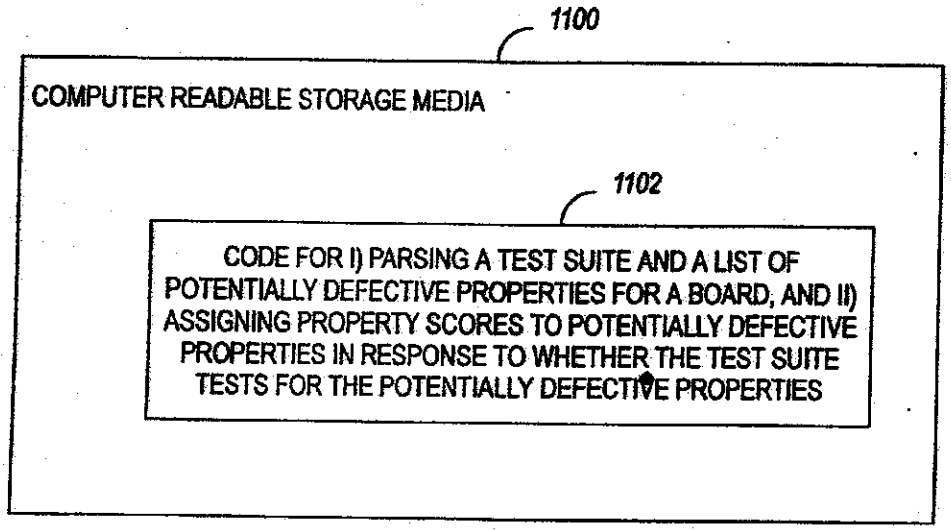

1100
COMPUTER READABLE STORAGE MEDIA
1102
CODE FOR I) PARSING A TEST SUITE AND A LIST OF POTENTIALLY DEFECTIVE PROPERTIES FOR A BOARD, AND II) ASSIGNING PROPERTY SCORES TO POTENTIALLY DEFECTIVE PROPERTIES IN RESPONSE TO WHETHER THE TEST SUITE TESTS FOR THE POTENTIALLY DEFECTIVE PROPERTIES

FIG. 11