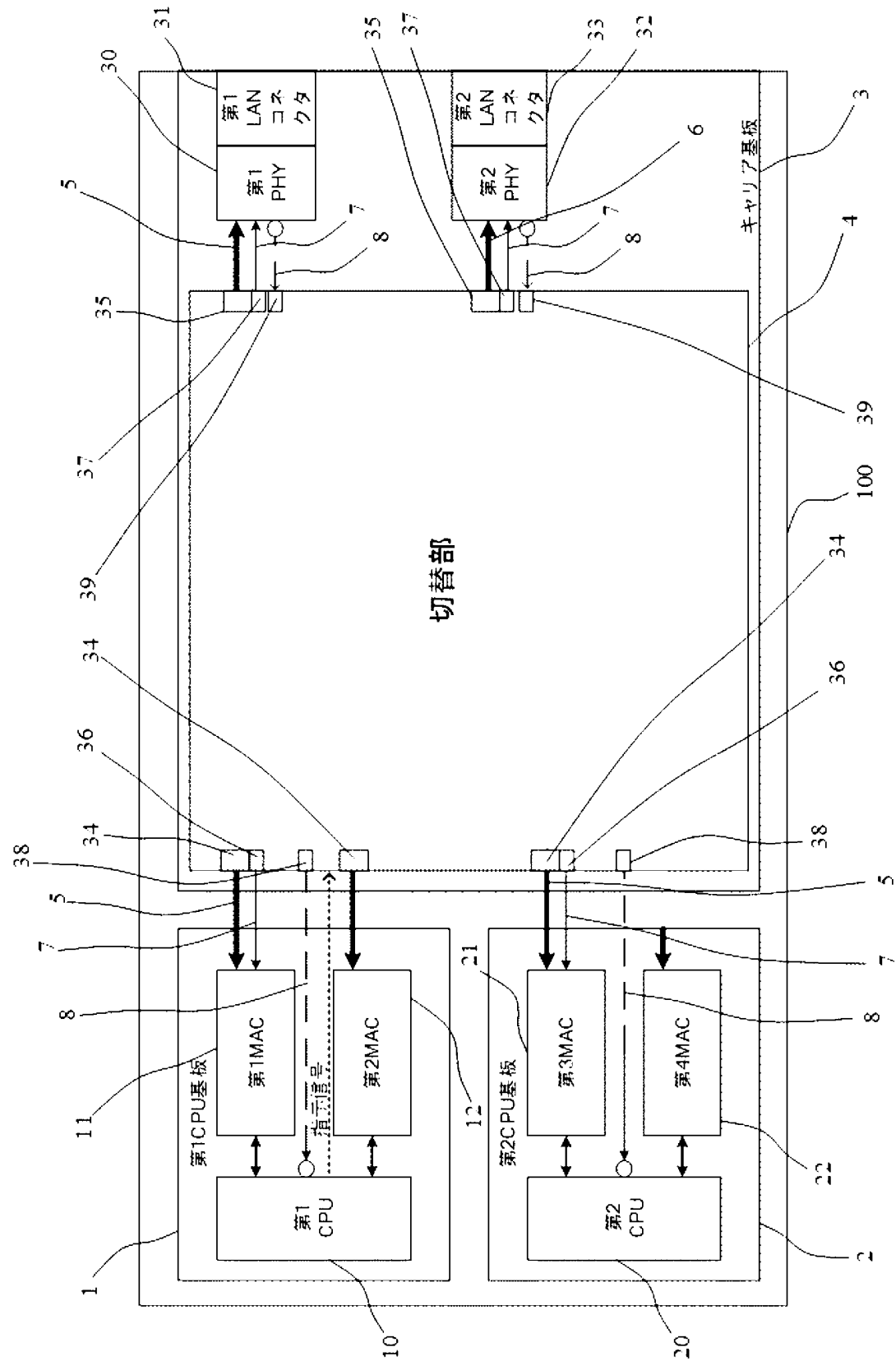
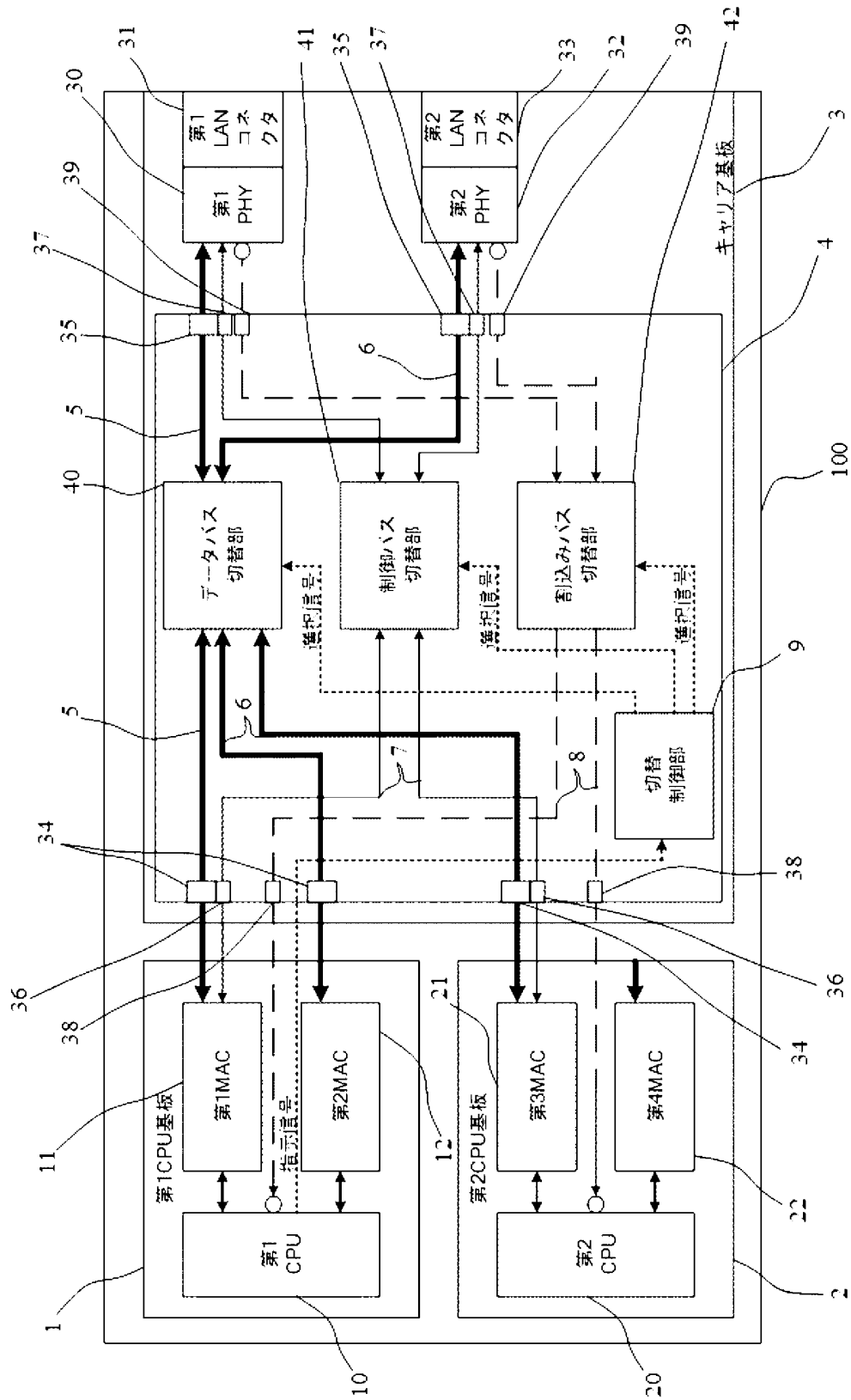


[図1]



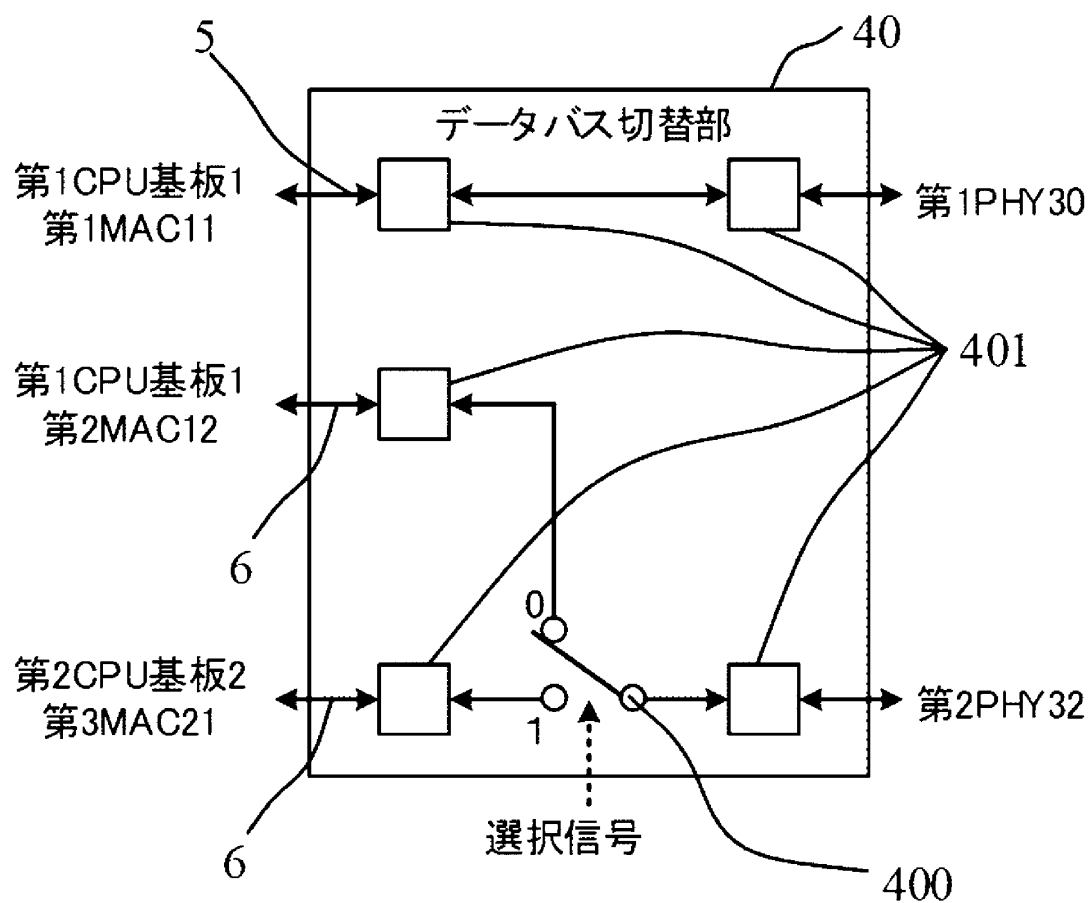
[図2]



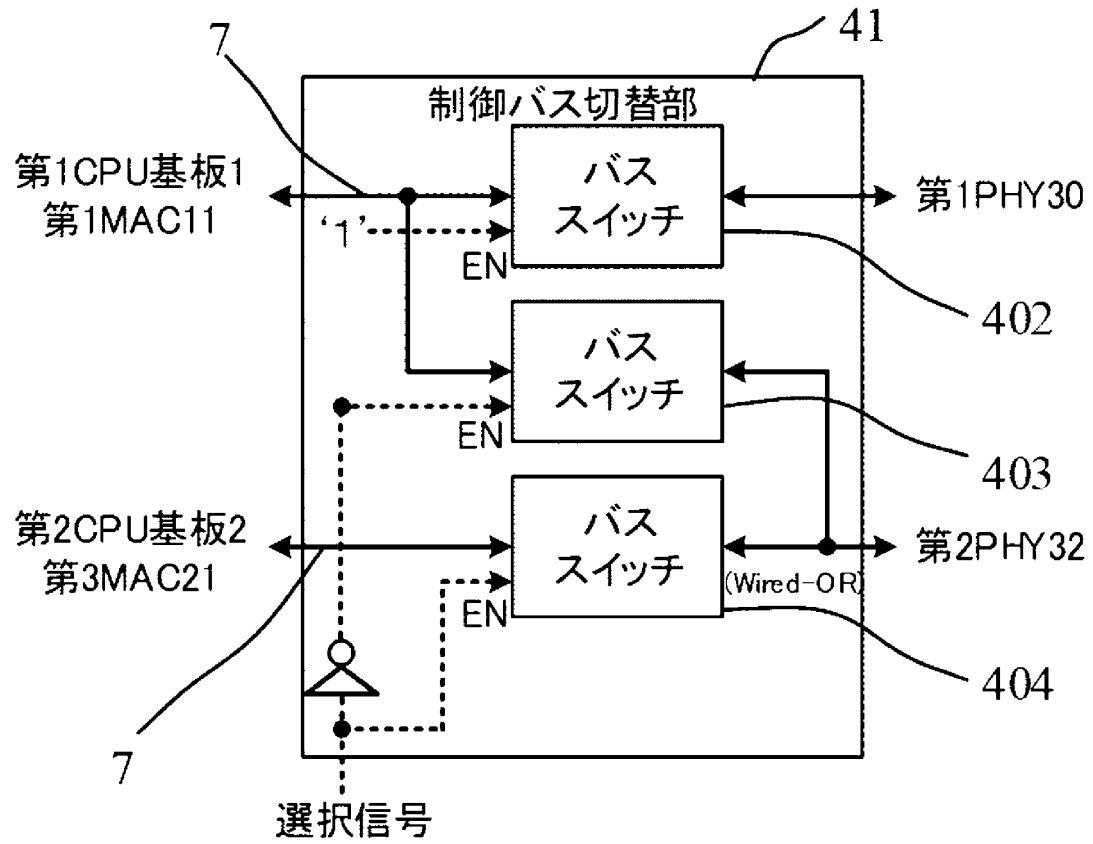
[図3]

選択信号	MAC-PHYの接続			
	第1CPU基板1		第2CPU基板2	
	第1MAC11	第2MAC12	第3MAC21	第4MAC22
0	第1PHY30	第2PHY32	未接続	未接続
1	第1PHY30	未接続	第2PHY32	未接続

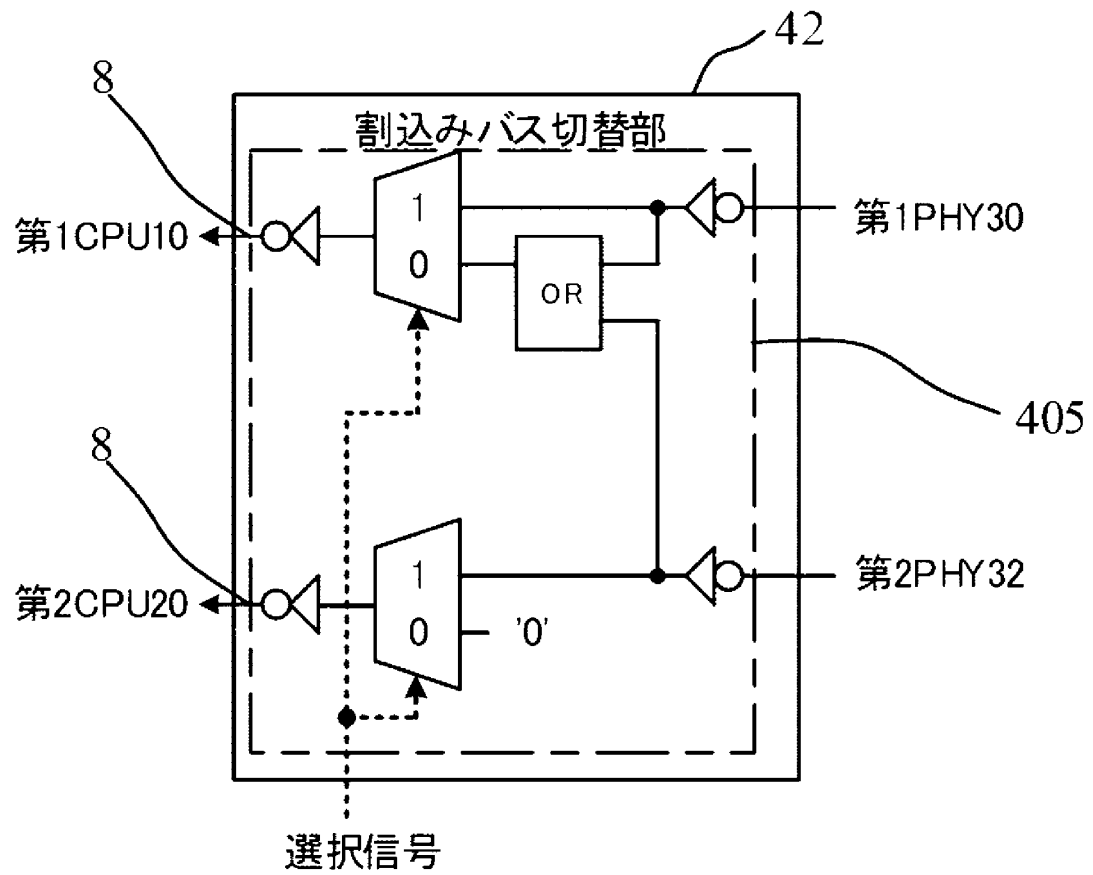
[図4]



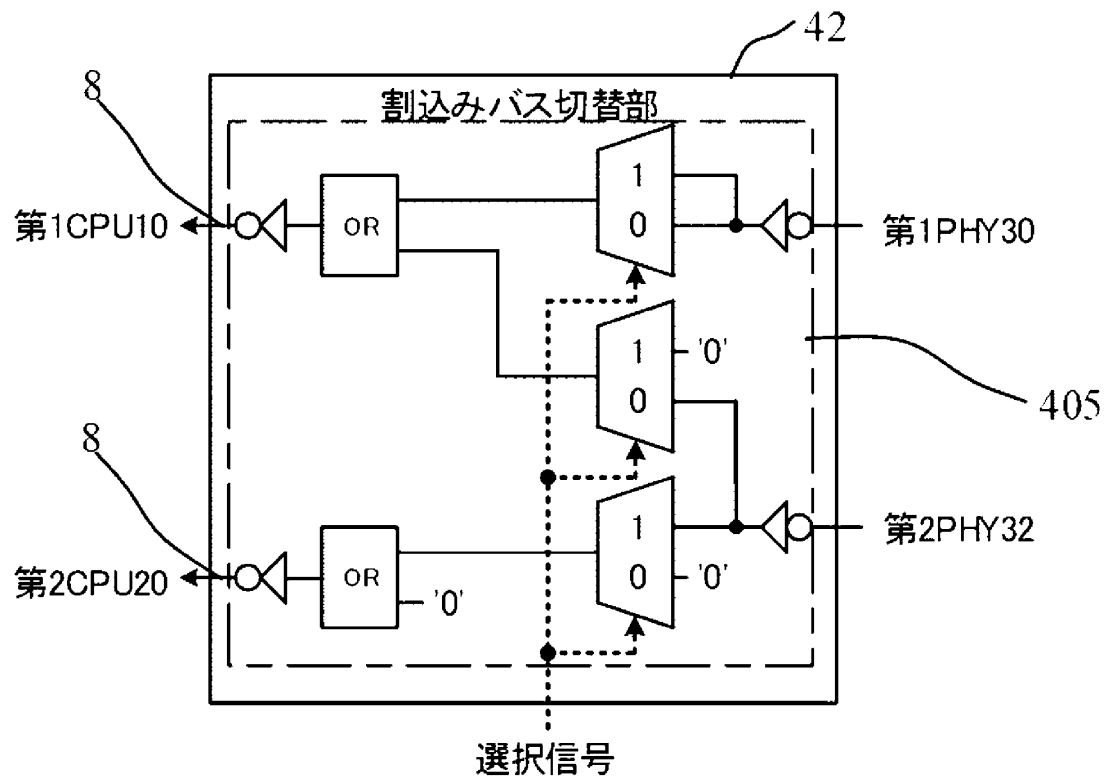
[図5]



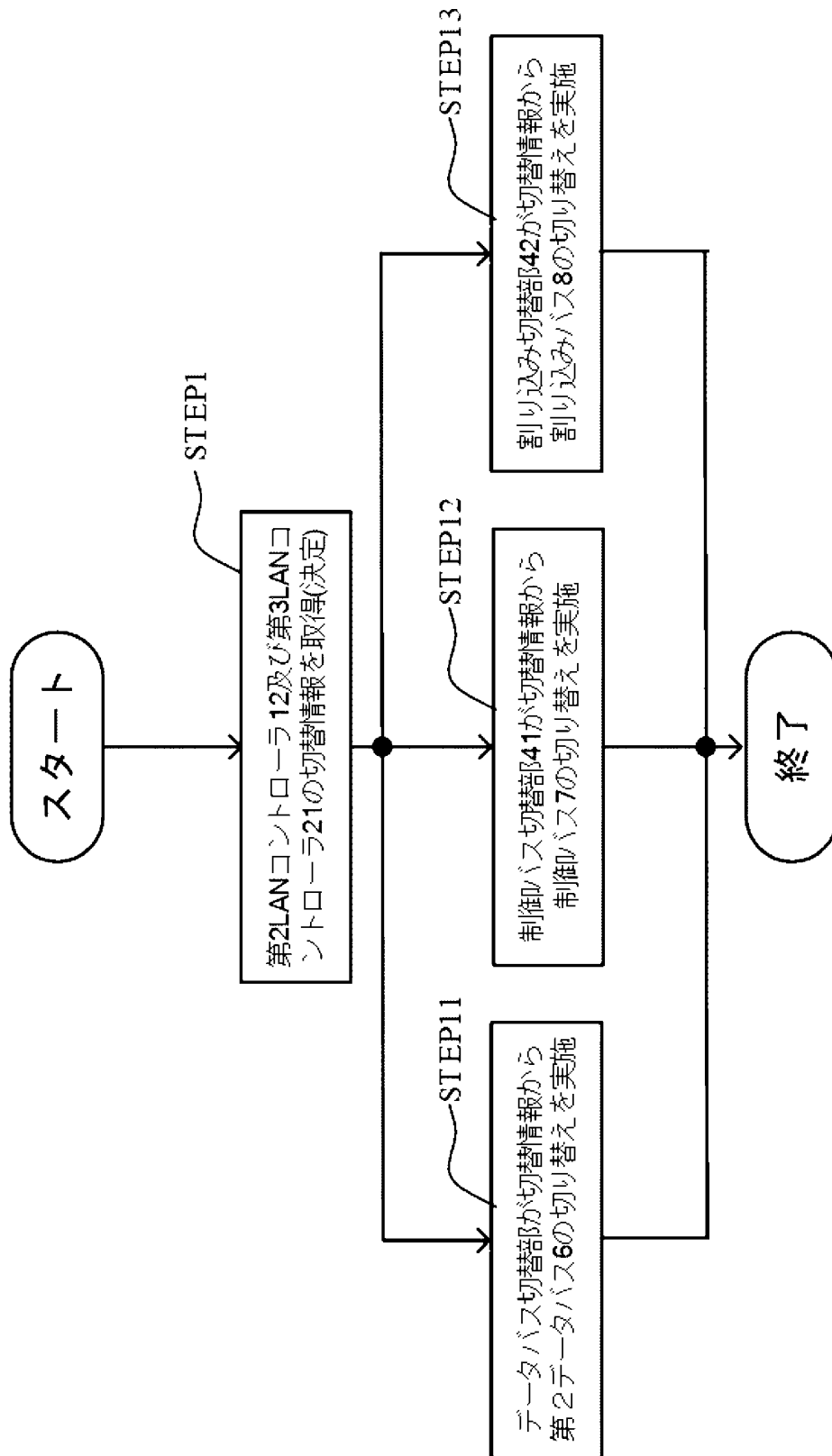
[図6]



[図7]



[図8]



[図9]

