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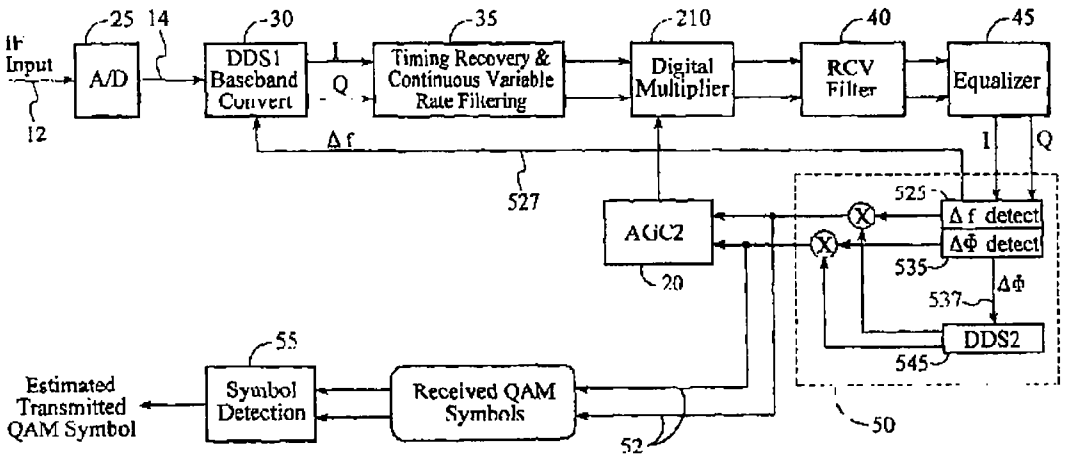
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(54) Title: DIRECT DIGITAL SYNTHESIS IN A QAM DEMODULATOR



(57) Abstract: A quadrature amplitude modulation (QAM) type demodulator having a pair of direct digital synthesizer (DDS) circuits. The first DDS circuit (30) is located in a baseband conversion circuit before a receive filter (40) and digitally tunes the signal within the receive filter bandwidth. The second DDS circuit (545) is within a carrier recovery circuit (50) located after the receive filter (40) and serves to fine tune the signal phase.

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