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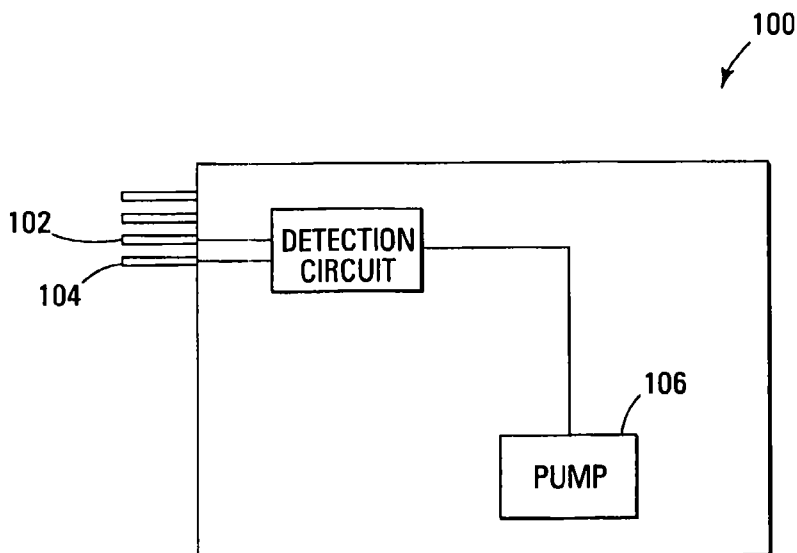
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(54) Title: POWER EFFICIENT MEMORY AND CARDS



(57) Abstract: A memory with an internal detection mechanism to detect the presence of either an external component of an external voltage on some no connect pins, allowing a change in the configuration of the internal voltage pumps based on those detections, or which can be used as a standard device as well. The embodiments allow the system to lower its card power consumption depending upon availability of other voltage sources in the system or available components such as inductors to provide internal voltages more efficiently.

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ACTION

— *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*

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