

CELL VOLTAGE DETECTION CIRCUIT, AND METHOD OF DETECTING CELL VOLTAGE**ABSTRACT OF THE DISCLOSURE**

A cell voltage detection circuit 20 comprises a first input selecting means 22 connected to the positive and negative electrodes of each of cells connected in series to each other to acquire a voltage from either a selected positive or negative electrode of an intended cell, a second input selecting means 24 connected to the positive and negative electrodes of each of the cells to acquire a voltage from either a selected positive or negative electrode of an intended cell, a voltage detector 30 to acquire a detected cell output voltage from output voltages of the first and second input selecting means 22 and 24, respectively, and a processing means 40 for converting a detected cell output voltage of the voltage detector 30 from analog to digital for calculation to provide the voltage of each cell.

FIGURE 2