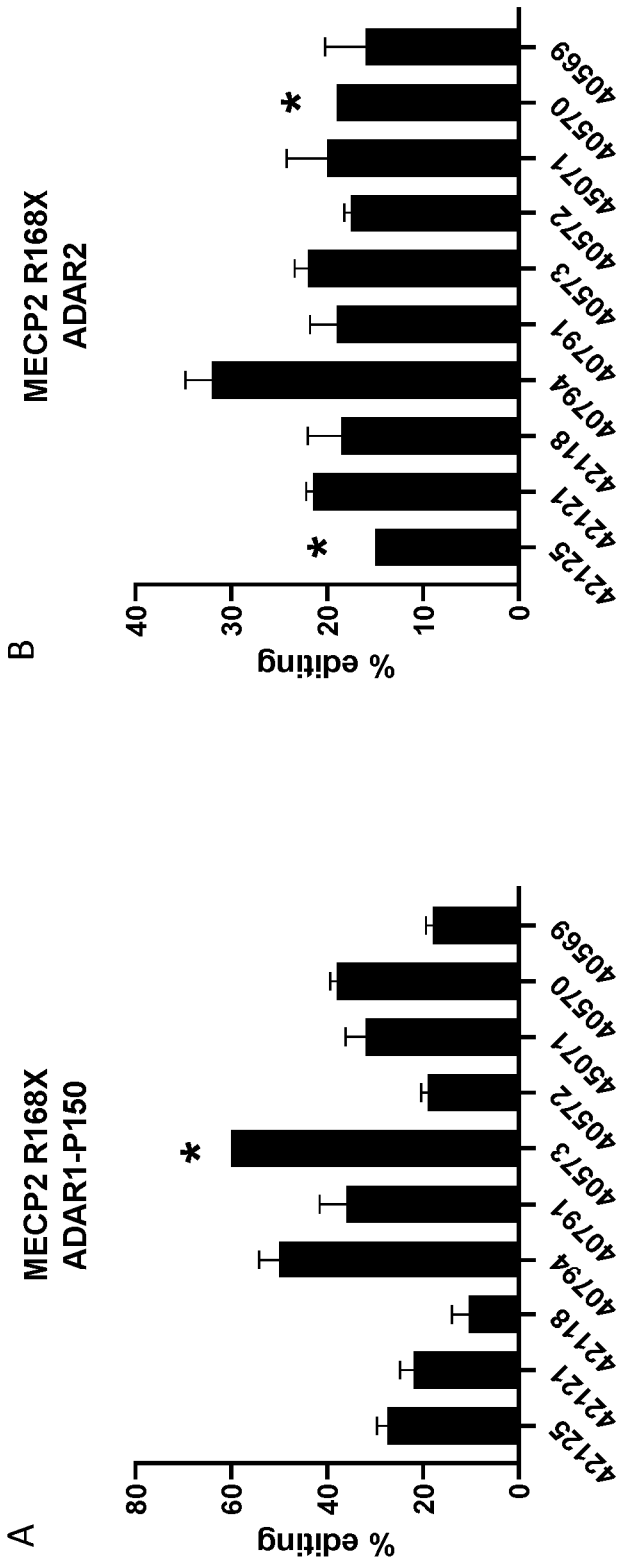

A
MECP2 R168X
ADAR1-P150
% editing
0
20
40
60
80
*
4212S
4212I
4211B
4079A
4079I
4057B
4057Z
4507I
4057O
4056S
B
MECP2 R168X
ADAR2
% editing
0
10
20
30
40
*
*
4212S
4212I
4211B
4079A
4079I
4057B
4057Z
4507I
4057O
4056S

FIG. 1

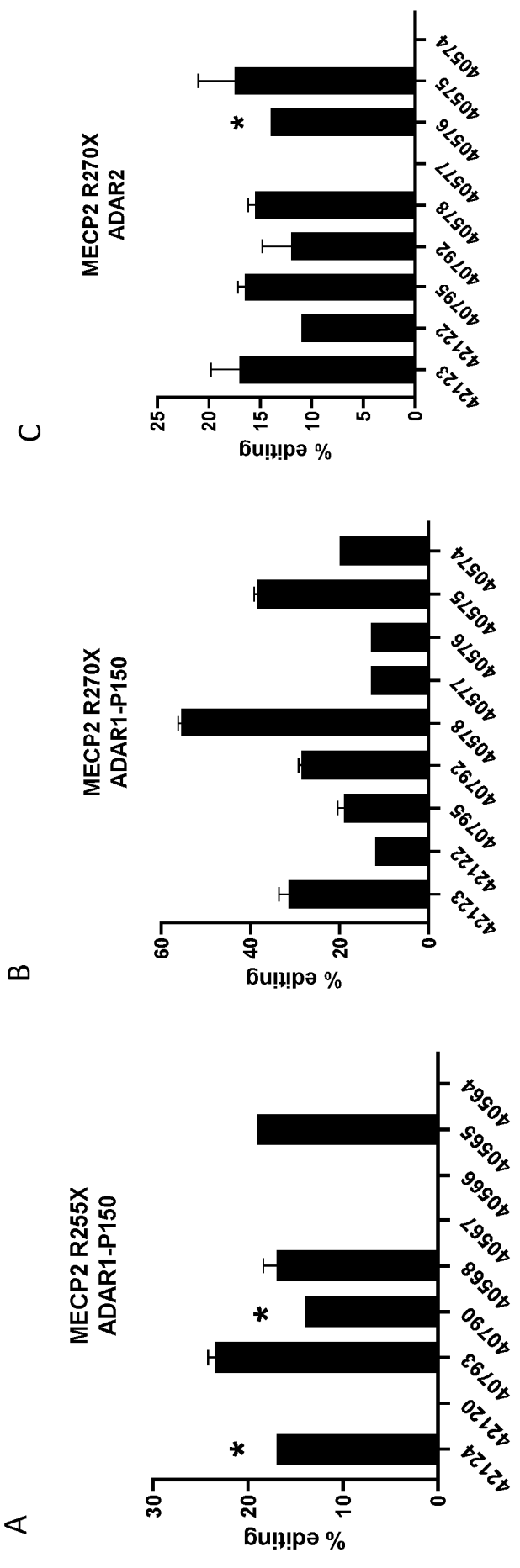

A
MECP2 R255X
ADAR1-P150
% editing
0
10
20
30
*
*
42124
42120
40793
40790
40568
40567
40566
40565
40564
B
MECP2 R270X
ADAR1-P150
% editing
0
20
40
60
42123
42122
40795
40792
40578
40577
40576
40575
40574
C
MECP2 R270X
ADAR2
% editing
0
5
10
15
20
25
*
42123
42122
40795
40792
40578
40577
40576
40575
40574

FIG. 2

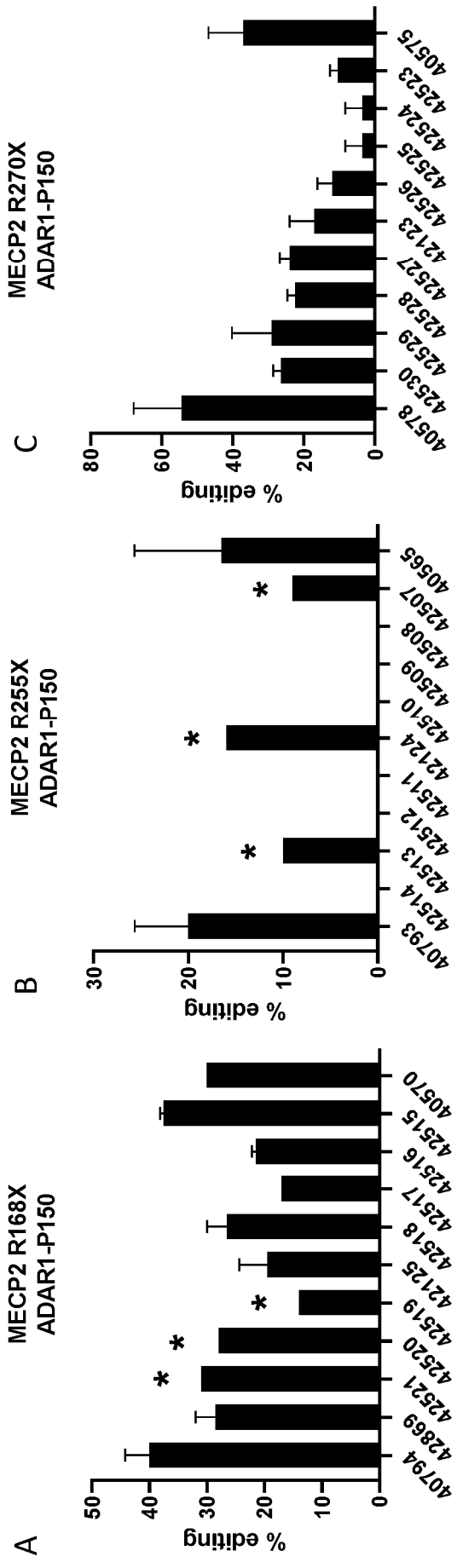

A
MECP2 R168X
ADAR1-P150
% editing
0
10
20
30
40
50
4079 4
4286 9
4252 1
4252 0
4251 9
4212 5
4251 8
4251 7
4251 6
4251 5
4057 0
B
MECP2 R255X
ADAR1-P150
% editing
0
10
20
30
4079 3
4251 4
4251 3
4251 2
4251 1
4212 4
4251 0
4250 9
4250 8
4250 7
4056 5
C
MECP2 R270X
ADAR1-P150
% editing
0
20
40
60
80
4057 8
4253 0
4252 9
4252 8
4252 7
4212 3
4252 6
4252 5
4252 4
4252 3
4057 5

FIG. 3

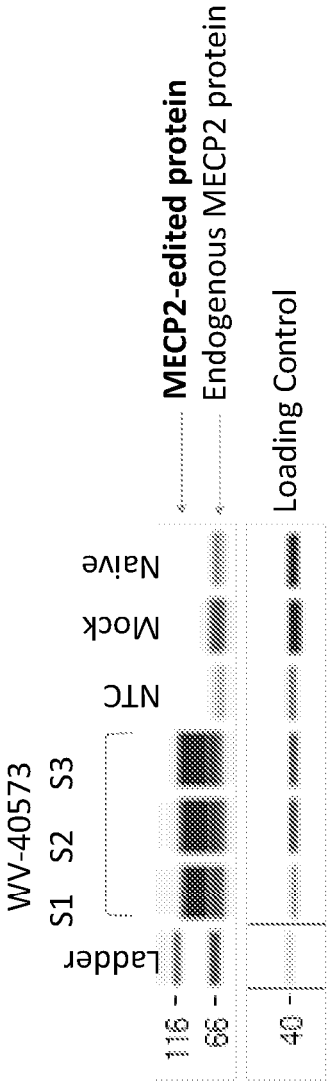

MECP2-edited protein
Endogenous MECP2 protein
Loading Control
Naive
Mock
NTC
WV-40573
S1 S2 S3
ladder
116 -
66 -
40 -

FIG. 4

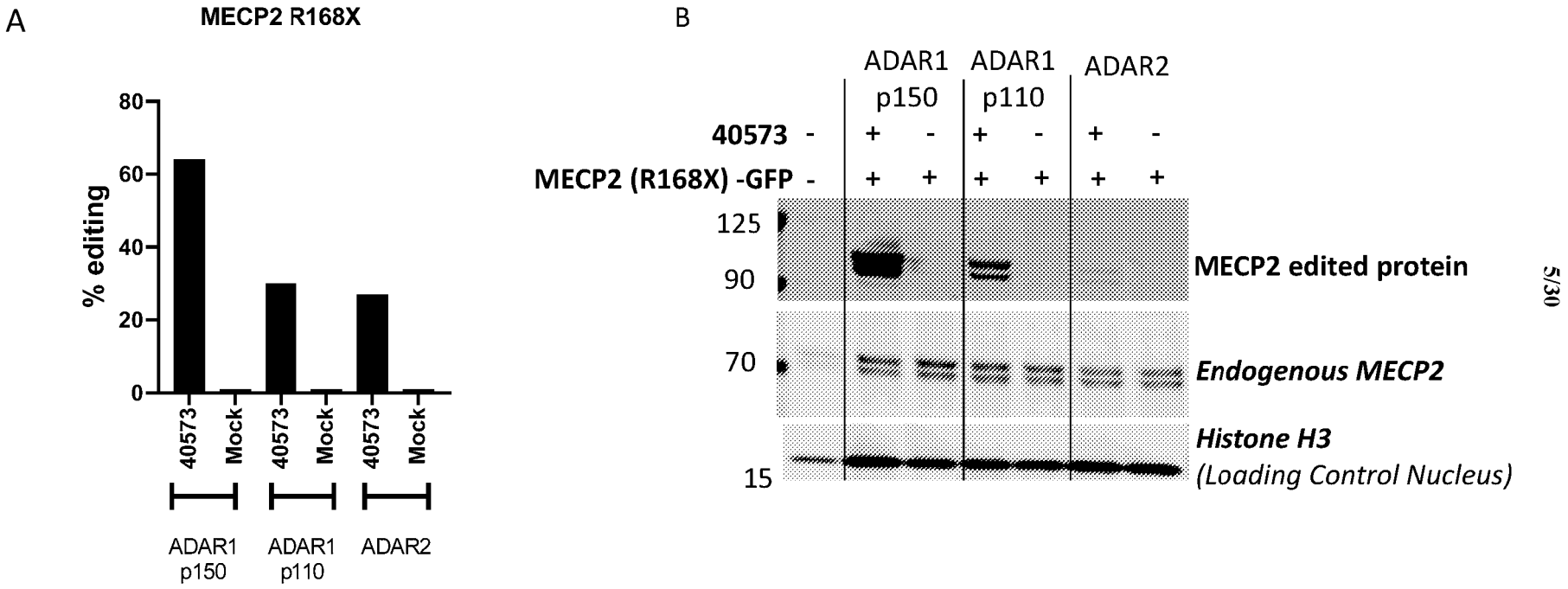

A
MECP2 R168X
% editing
80
60
40
20
0
40573
Mock
40573
Mock
40573
Mock
ADAR1 p150
ADAR1 p110
ADAR2
B
ADAR1 p150
ADAR1 p110
ADAR2
40573 - + - + - + -
MECP2 (R168X) -GFP - + + + + + +
125
90
70
15
MECP2 edited protein
Endogenous MECP2
Histone H3
(Loading Control Nucleus)

FIG. 5

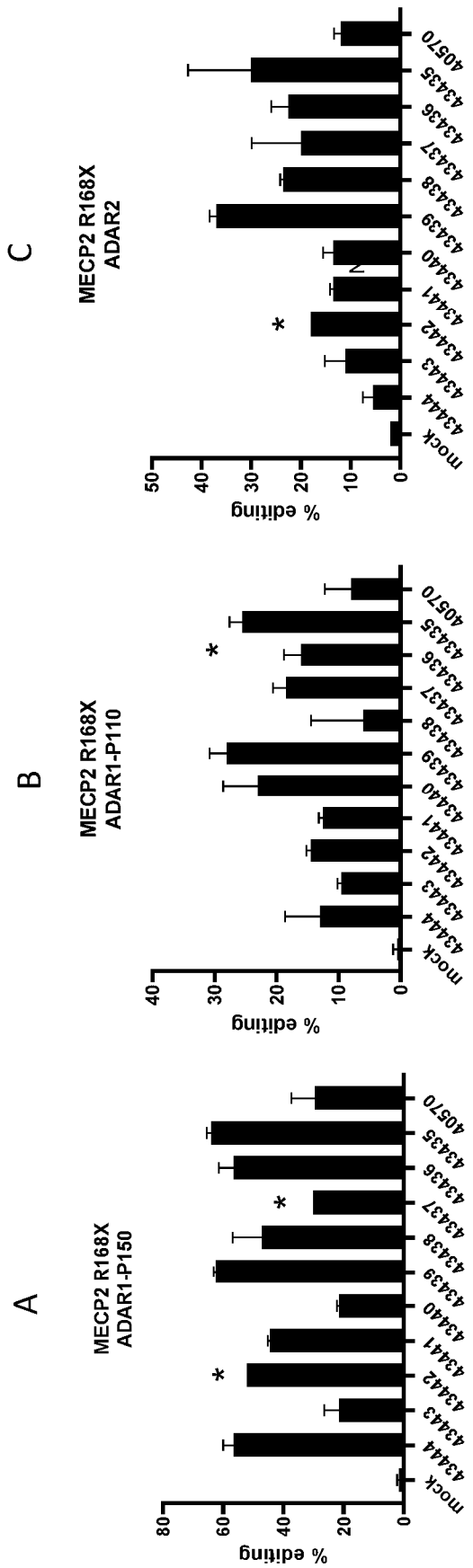

A
MECP2 R168X
ADAR1-P150
% editing
0
20
40
60
80
mock
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4057
*
*
B
MECP2 R168X
ADAR1-P110
% editing
0
10
20
30
40
mock
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4057
*
C
MECP2 R168X
ADAR2
% editing
0
10
20
30
40
50
mock
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4344
4057
*
N

FIG. 6

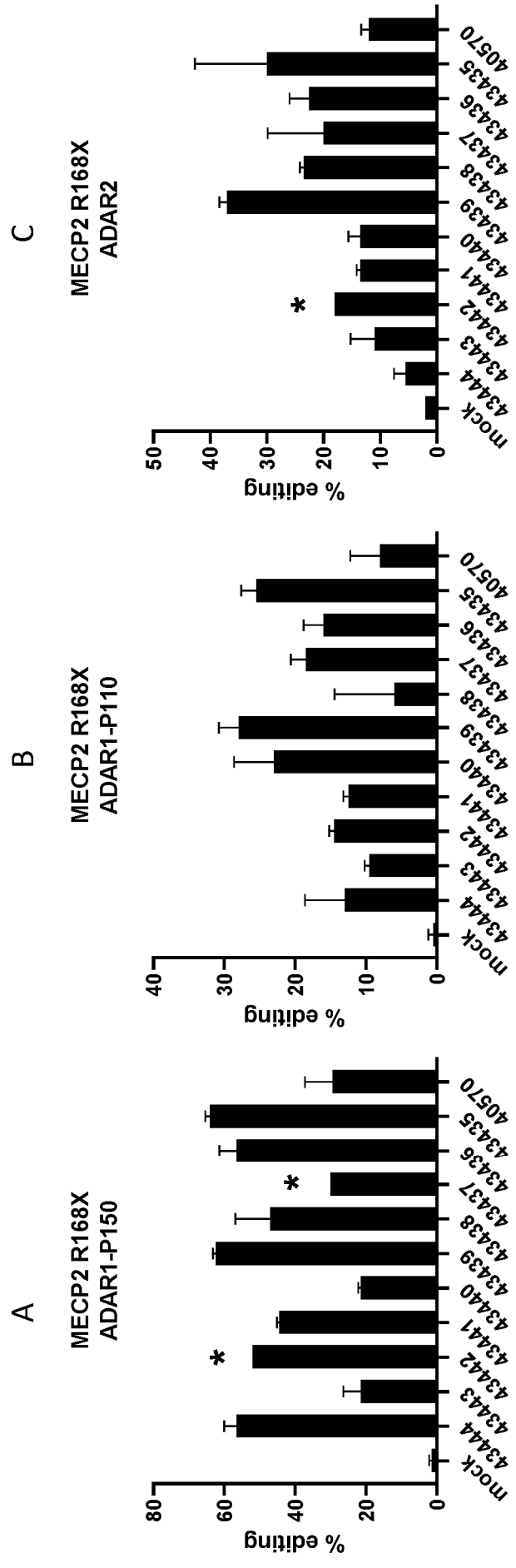

A
MECP2 R168X
ADAR1-P150
% editing
0
20
40
60
80
mock
4344
4343
4342
4341
4340
4339
4338
4337
4336
4335
4057
*
*
B
MECP2 R168X
ADAR1-P110
% editing
0
10
20
30
40
mock
4344
4343
4342
4341
4340
4339
4338
4337
4336
4335
4057
C
MECP2 R168X
ADAR2
% editing
0
10
20
30
40
50
mock
4344
4343
4342
4341
4340
4339
4338
4337
4336
4335
4057
*

FIG. 7

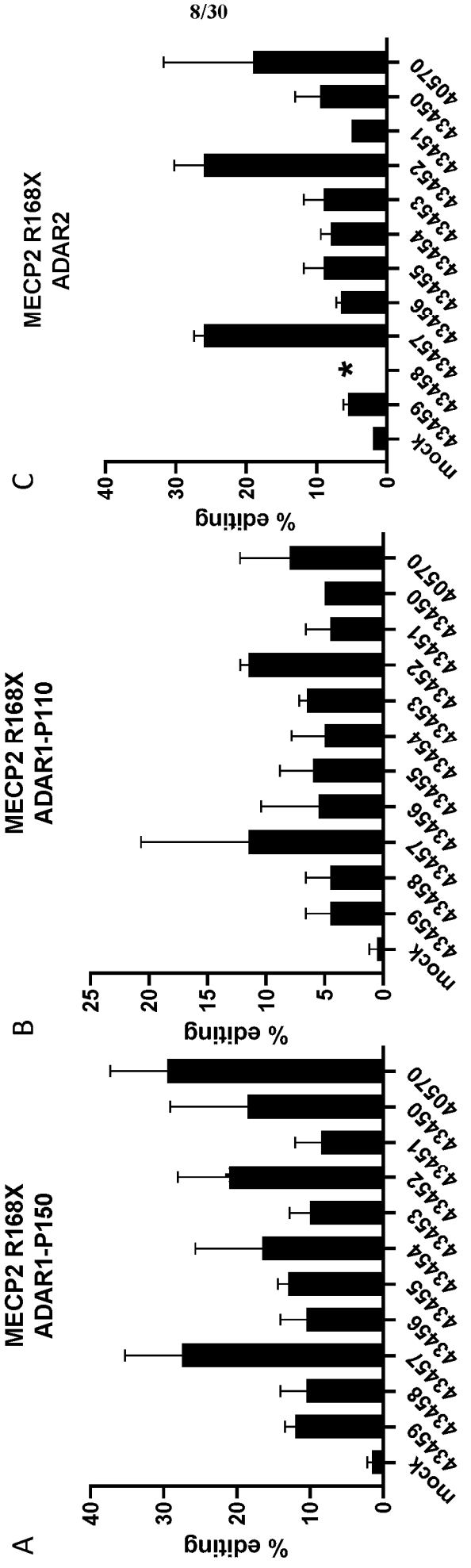

A
MECP2 R168X
ADAR1-P150
% editing
0
10
20
30
40
mock
4345 9
4345 8
4345 7
4345 6
4345 5
4345 4
4345 3
4345 2
4345 1
4345 0
4057 0
B
MECP2 R168X
ADAR1-P110
% editing
0
5
10
15
20
25
mock
4345 9
4345 8
4345 7
4345 6
4345 5
4345 4
4345 3
4345 2
4345 1
4345 0
4057 0
C
MECP2 R168X
ADAR2
% editing
0
10
20
30
40
*
mock
4345 9
4345 8
4345 7
4345 6
4345 5
4345 4
4345 3
4345 2
4345 1
4345 0
4057 0

FIG. 8

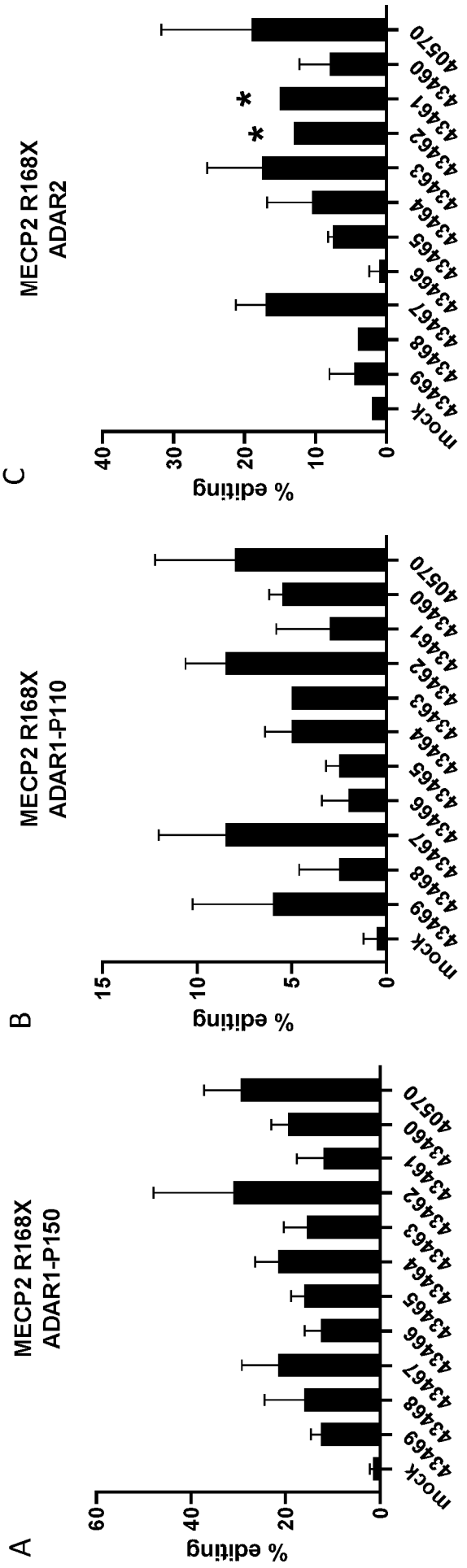

A
MECP2 R168X
ADAR1-P150
% editing
0
20
40
60
mock
43469
43468
43467
43466
43465
43464
43463
43462
43461
43460
40570
B
MECP2 R168X
ADAR1-P110
% editing
0
5
10
15
mock
43469
43468
43467
43466
43465
43464
43463
43462
43461
43460
40570
C
MECP2 R168X
ADAR2
% editing
0
10
20
30
40
mock
43469
43468
43467
43466
43465
43464
43463
43462
43461
43460
40570
*
*

FIG. 9

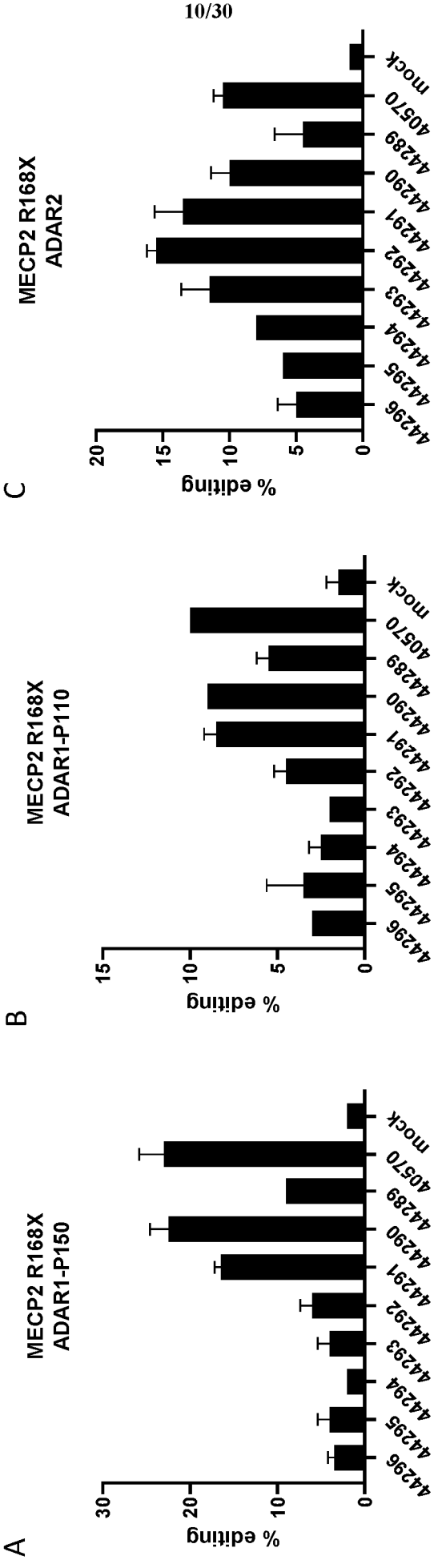

A
MECP2 R168X
ADAR1-P150
% editing
30
20
10
0
4296
4295
4294
4293
4292
4291
4290
4289
4057O
mock
B
MECP2 R168X
ADAR1-P110
% editing
15
10
5
0
4296
4295
4294
4293
4292
4291
4290
4289
4057O
mock
C
MECP2 R168X
ADAR2
% editing
20
15
10
5
0
4296
4295
4294
4293
4292
4291
4290
4289
4057O
mock

FIG. 10

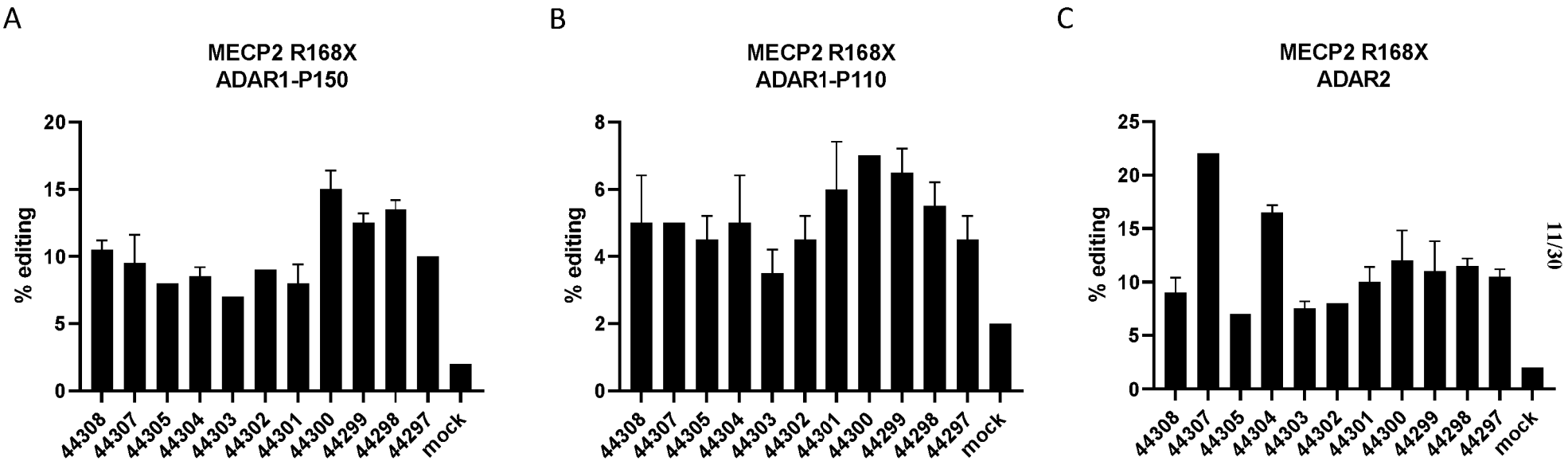

A
MECP2 R168X
ADAR1-P150
% editing
20
15
10
5
0
44308
44307
44305
44304
44303
44302
44301
44300
44299
44298
44297
mock
B
MECP2 R168X
ADAR1-P110
% editing
8
6
4
2
0
44308
44307
44305
44304
44303
44302
44301
44300
44299
44298
44297
mock
C
MECP2 R168X
ADAR2
% editing
25
20
15
10
5
0
44308
44307
44305
44304
44303
44302
44301
44300
44299
44298
44297
mock

FIG. 11

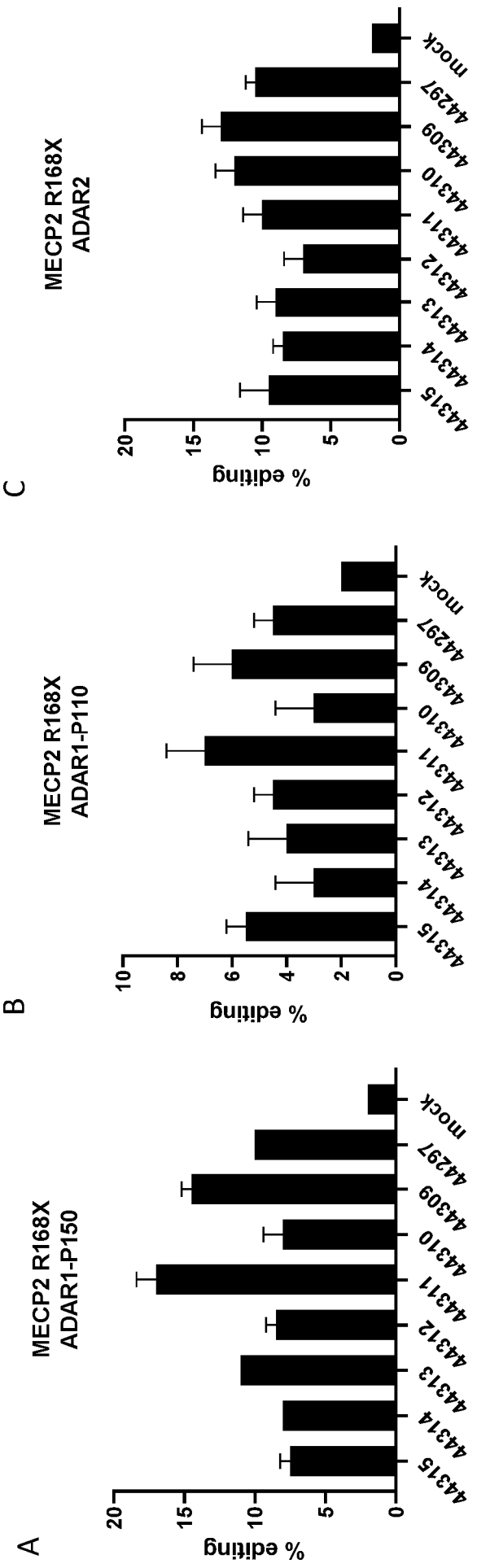

A
MECP2 R168X
ADAR1-P150
% editing
0
5
10
15
20
4315
4314
4313
4312
4311
4310
4309
4297
mock
B
MECP2 R168X
ADAR1-P110
% editing
0
2
4
6
8
10
4315
4314
4313
4312
4311
4310
4309
4297
mock
C
MECP2 R168X
ADAR2
% editing
0
5
10
15
20
4315
4314
4313
4312
4311
4310
4309
4297
mock

FIG. 12

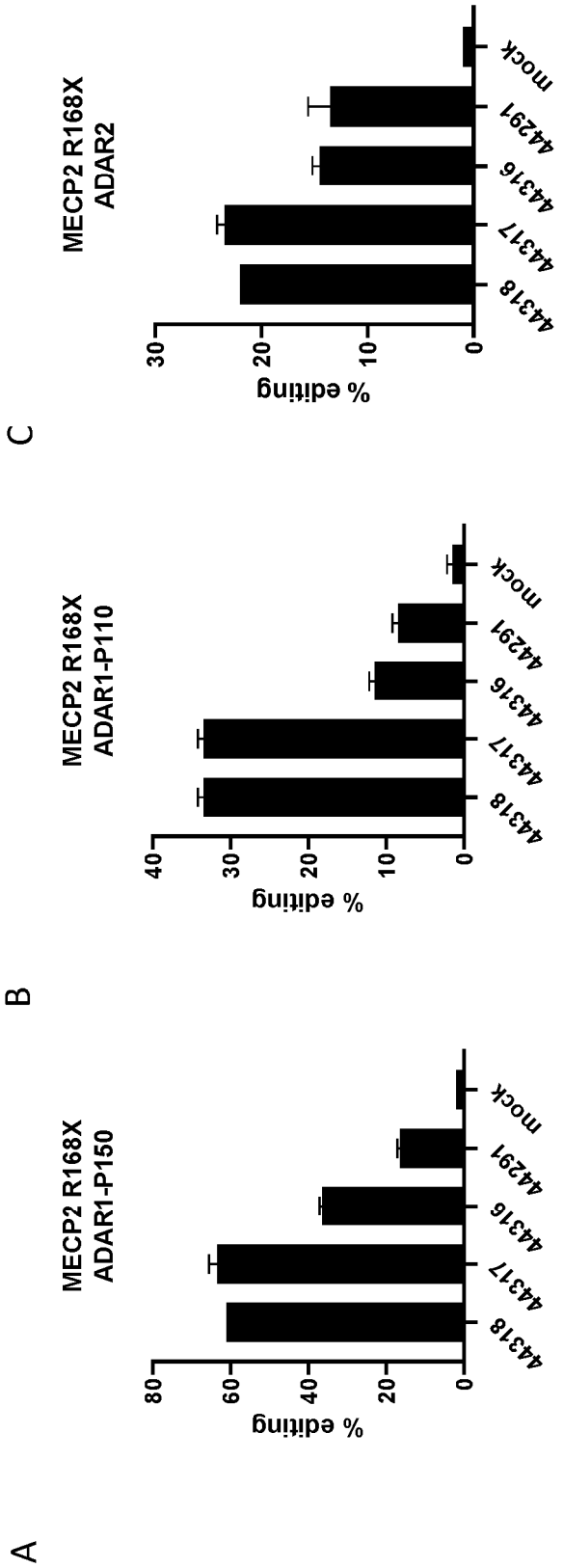

A
MECP2 R168X
ADAR1-P150
% editing
0
20
40
60
80
4318
4317
4316
4291
mock
B
MECP2 R168X
ADAR1-P110
% editing
0
10
20
30
40
4318
4317
4316
4291
mock
C
MECP2 R168X
ADAR2
% editing
0
10
20
30
4318
4317
4316
4291
mock

FIG. 13

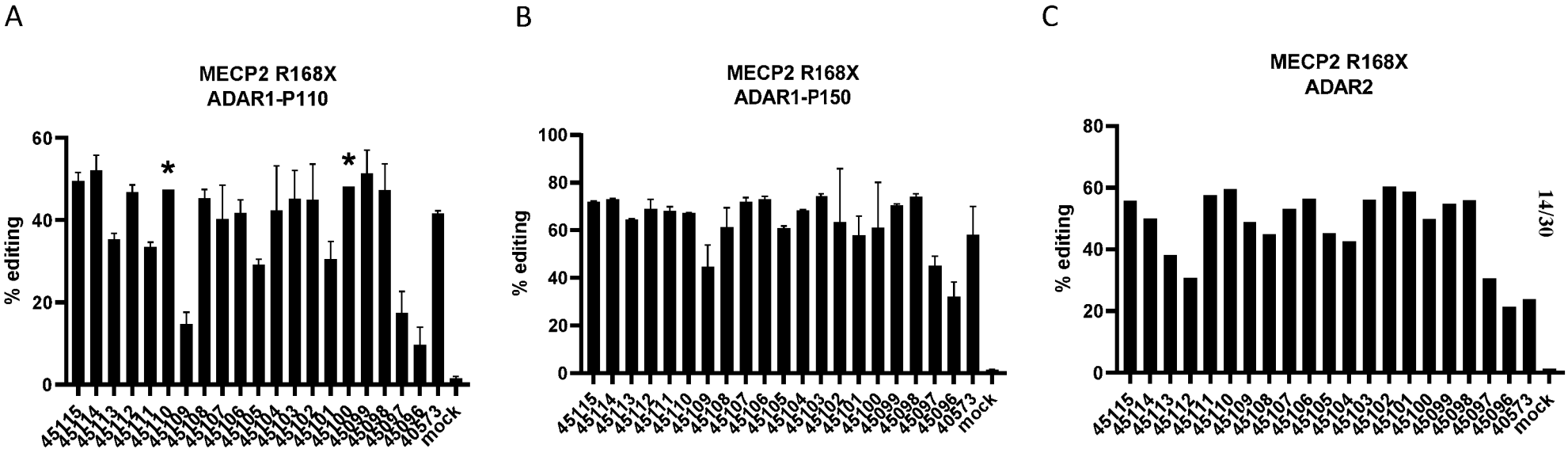

A
MECP2 R168X
ADAR1-P110
% editing
60
40
20
0
*
*
45115
45114
45113
45112
45111
45110
45109
45108
45107
45106
45105
45104
45103
45102
45101
45100
45099
45098
45097
45096
40573
mock
B
MECP2 R168X
ADAR1-P150
% editing
100
80
60
40
20
0
45115
45114
45113
45112
45111
45110
45109
45108
45107
45106
45105
45104
45103
45102
45101
45100
45099
45098
45097
45096
40573
mock
C
MECP2 R168X
ADAR2
% editing
80
60
40
20
0
14/30
45115
45114
45113
45112
45111
45110
45109
45108
45107
45106
45105
45104
45103
45102
45101
45100
45099
45098
45097
45096
40573
mock

FIG. 14

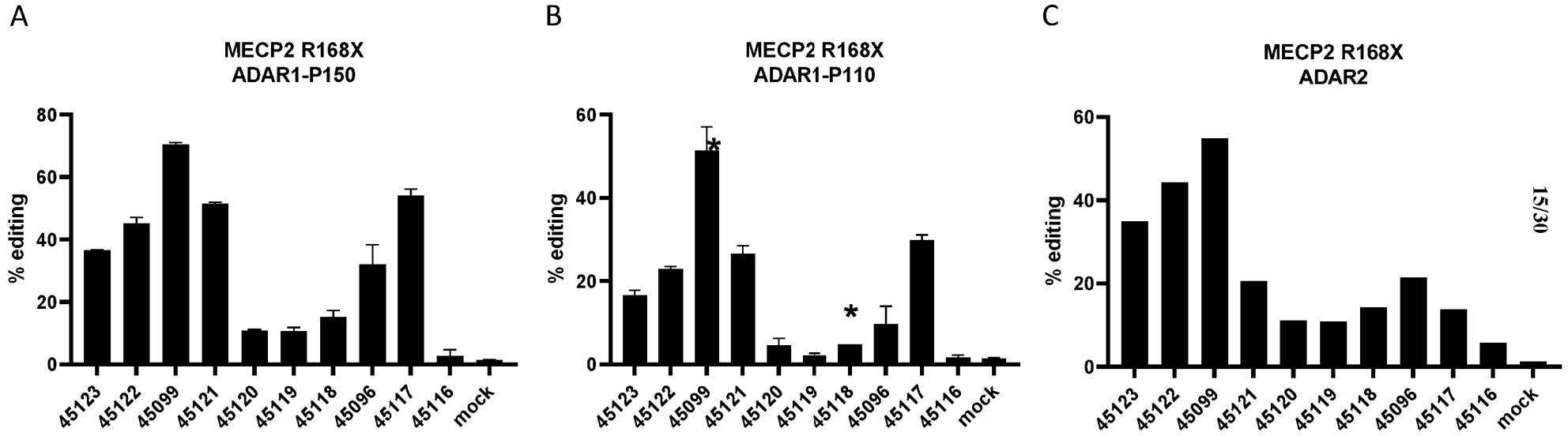

A
MECP2 R168X
ADAR1-P150
% editing
80
60
40
20
0
45123
45122
45099
45121
45120
45119
45118
45096
45117
45116
mock
B
MECP2 R168X
ADAR1-P110
% editing
60
40
20
0
*
*
45123
45122
45099
45121
45120
45119
45118
45096
45117
45116
mock
C
MECP2 R168X
ADAR2
% editing
60
40
20
0
15/30
45123
45122
45099
45121
45120
45119
45118
45096
45117
45116
mock

FIG. 15

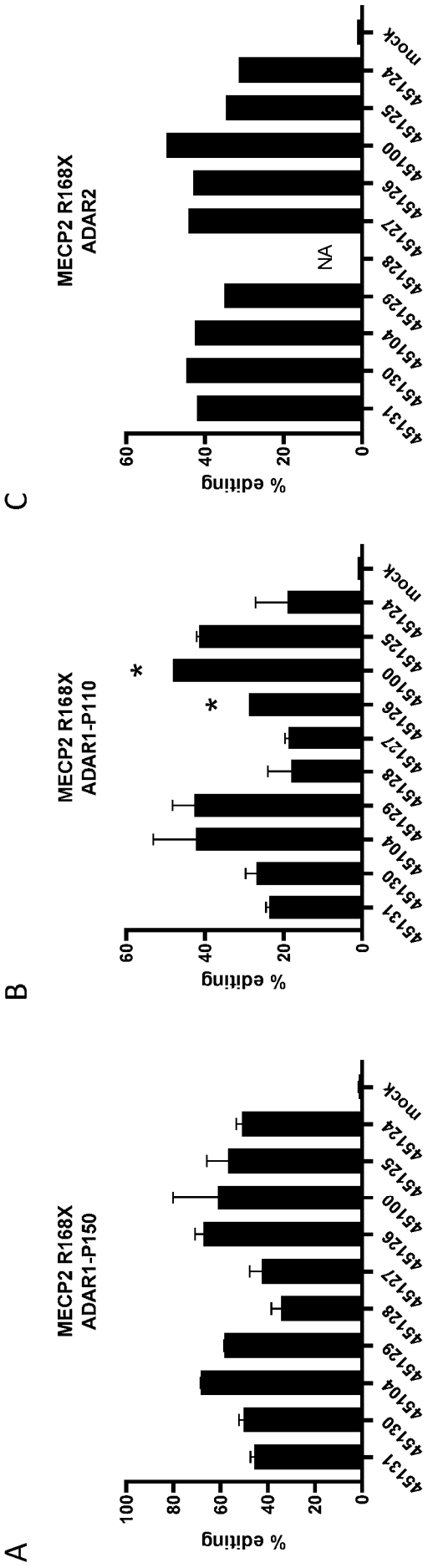

A
MECP2 R168X
ADAR1-P150
% editing
100
80
60
40
20
0
45131
45130
45104
45129
45128
45127
45126
45100
45125
45124
mock
B
MECP2 R168X
ADAR1-P110
% editing
60
40
20
0
*
*
45131
45130
45104
45129
45128
45127
45126
45100
45125
45124
mock
C
MECP2 R168X
ADAR2
% editing
60
40
20
0
NA
45131
45130
45104
45129
45128
45127
45126
45100
45125
45124
mock

FIG. 16

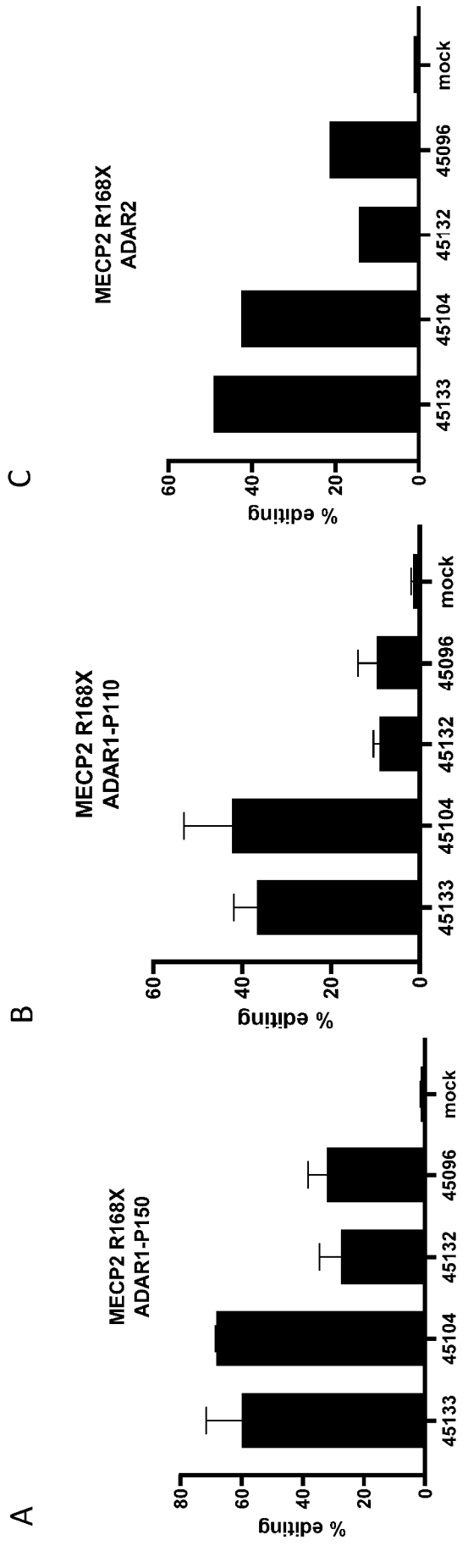

A
MECP2 R168X
ADAR1-P150
% editing
0
20
40
60
80
45133
45104
45132
45096
mock
B
MECP2 R168X
ADAR1-P110
% editing
0
20
40
60
45133
45104
45132
45096
mock
C
MECP2 R168X
ADAR2
% editing
0
20
40
60
45133
45104
45132
45096
mock

FIG. 17

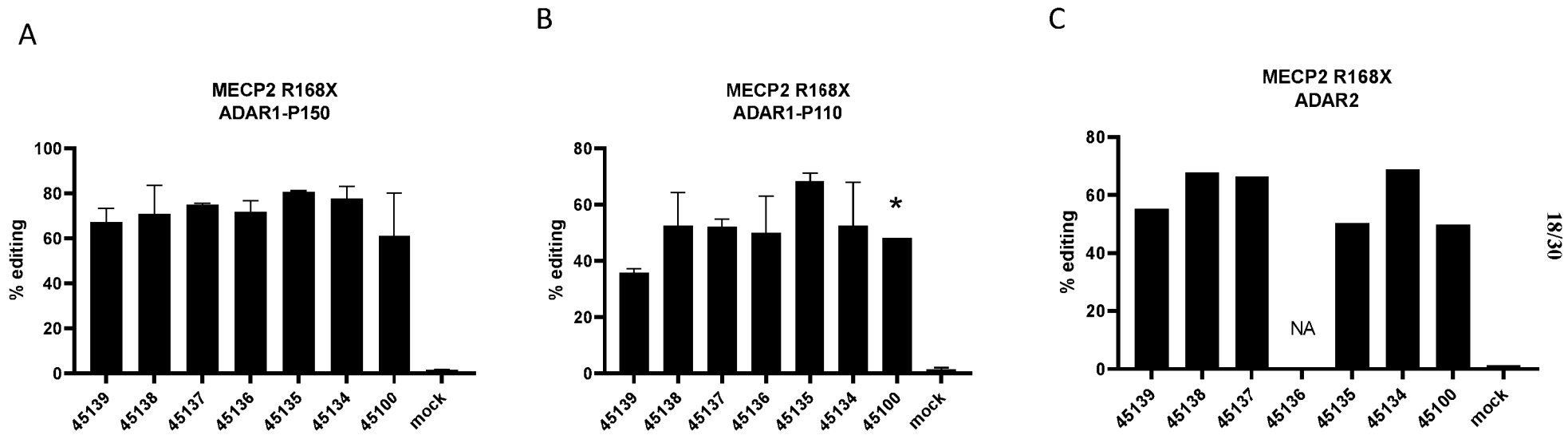

A
MECP2 R168X
ADAR1-P150
% editing
100
80
60
40
20
0
45139
45138
45137
45136
45135
45134
45100
mock
B
MECP2 R168X
ADAR1-P110
% editing
80
60
40
20
0
*
45139
45138
45137
45136
45135
45134
45100
mock
C
MECP2 R168X
ADAR2
% editing
80
60
40
20
0
NA
18/30
45139
45138
45137
45136
45135
45134
45100
mock

FIG. 18

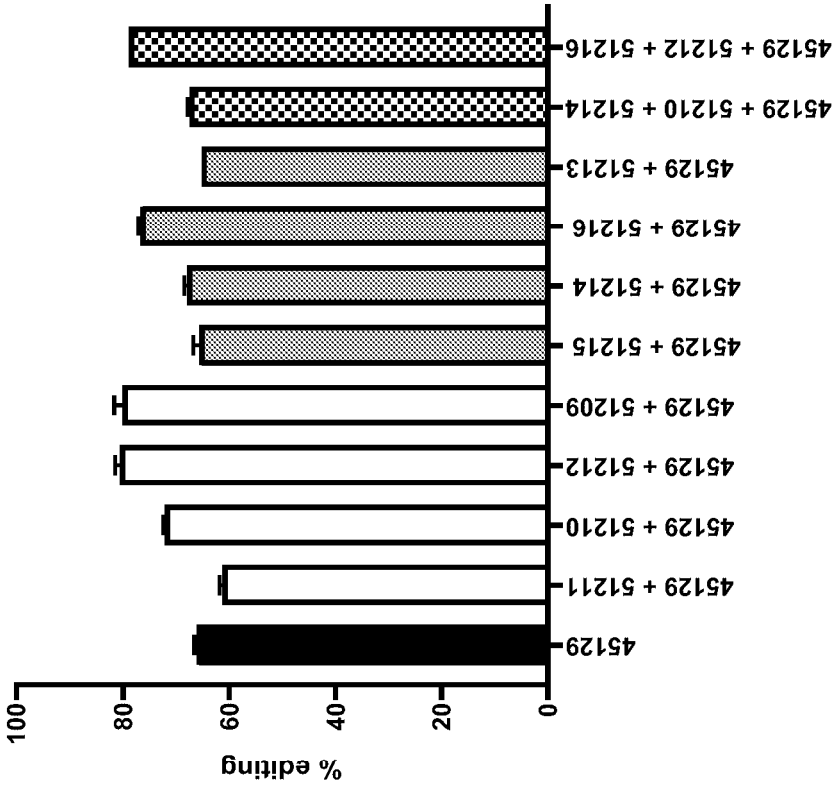

% editing
100
80
60
40
20
0
45129
45129 + 51211
45129 + 51210
45129 + 51212
45129 + 51209
45129 + 51215
45129 + 51214
45129 + 51216
45129 + 51213
45129 + 51210 + 51214
45129 + 51212 + 51216

FIG. 19

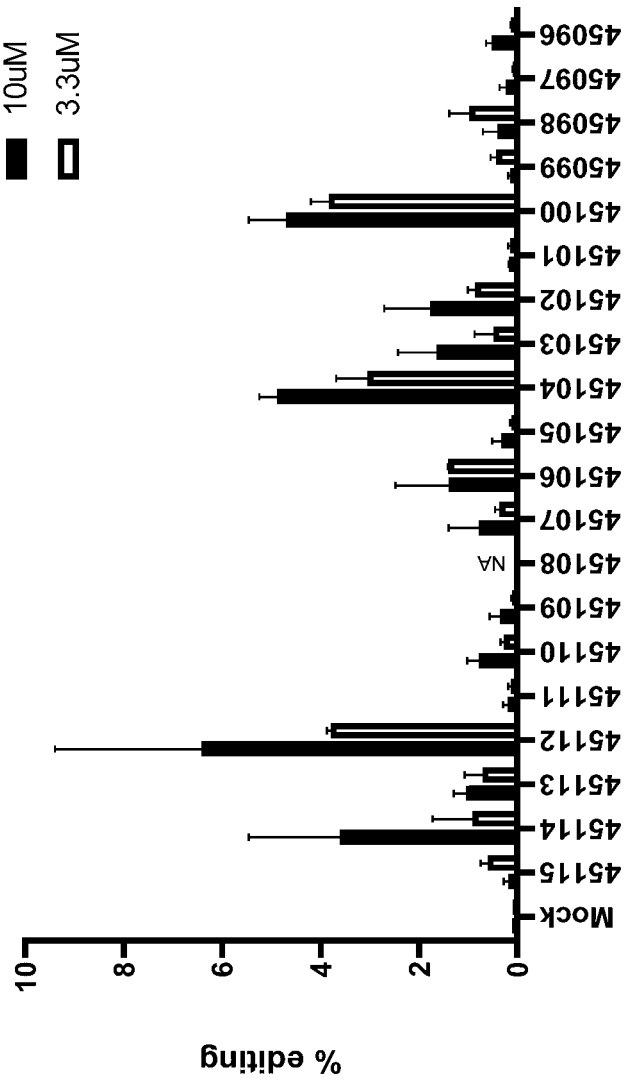

10uM
3.3uM
% editing
0
2
4
6
8
10
Mock
45115
45114
45113
45112
45111
45110
45109
45108
NA
45107
45106
45105
45104
45103
45102
45101
45100
45099
45098
45097
45096

FIG. 20

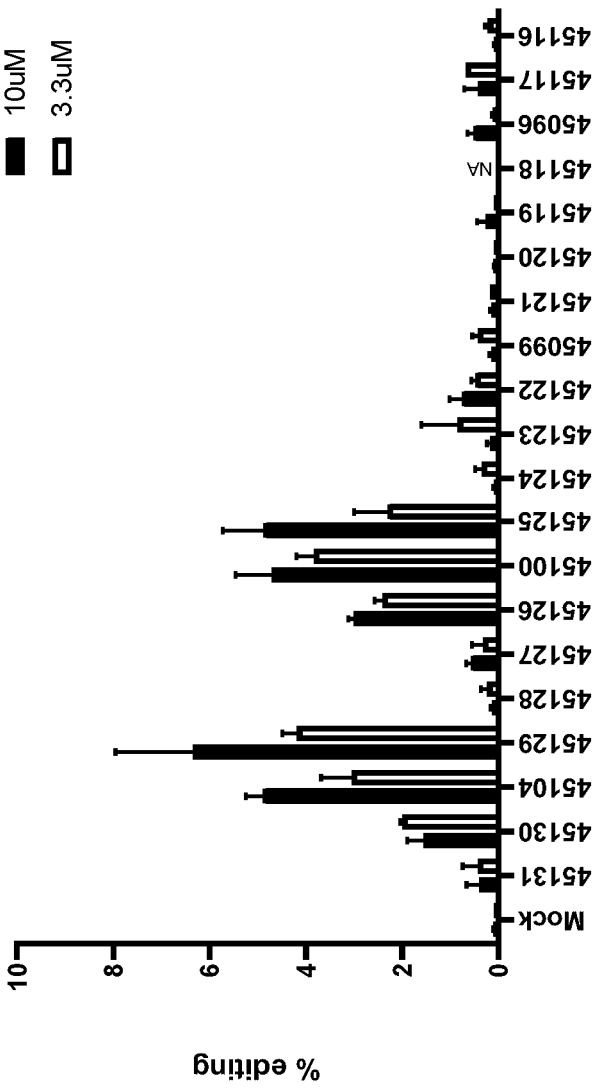

10uM
3.3uM
% editing
0
2
4
6
8
10
Mock
45131
45130
45104
45129
45128
45127
45126
45100
45125
45124
45123
45122
45099
45121
45120
45119
45118
NA
45096
45117
45116

FIG. 21

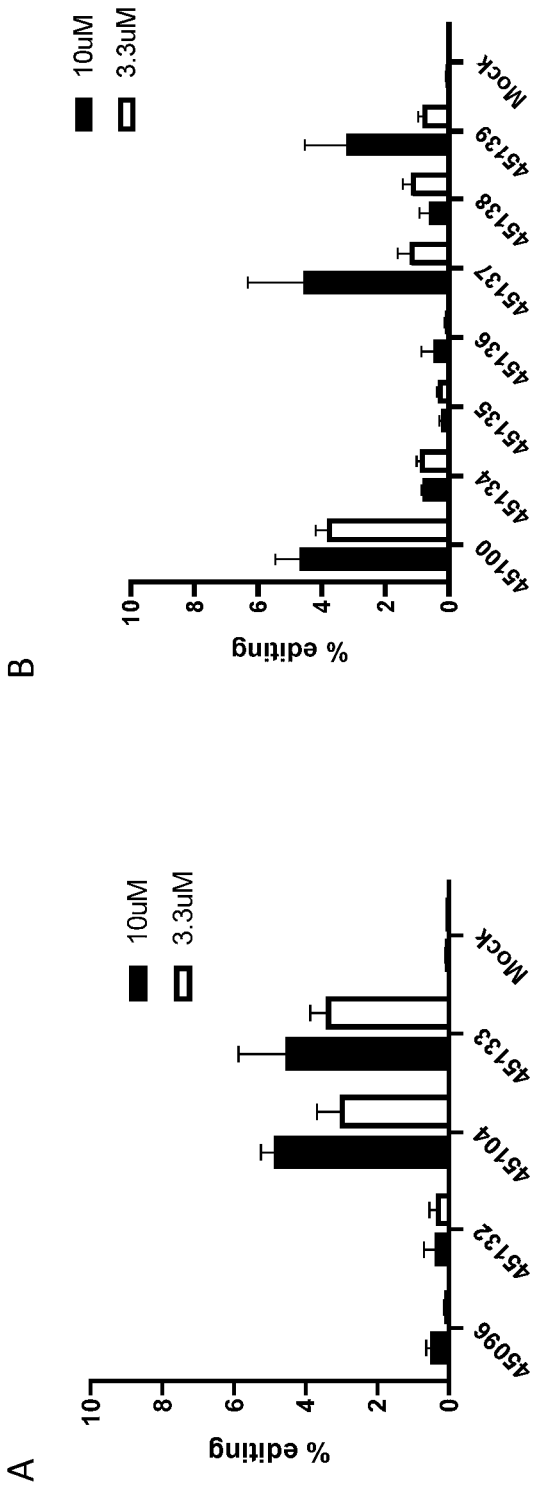

A
% editing
10uM
3.3uM
0
2
4
6
8
10
45096
45132
45104
45133
Mock
B
% editing
10uM
3.3uM
0
2
4
6
8
10
45100
45134
45135
45136
45137
45138
45139
Mock

FIG. 22

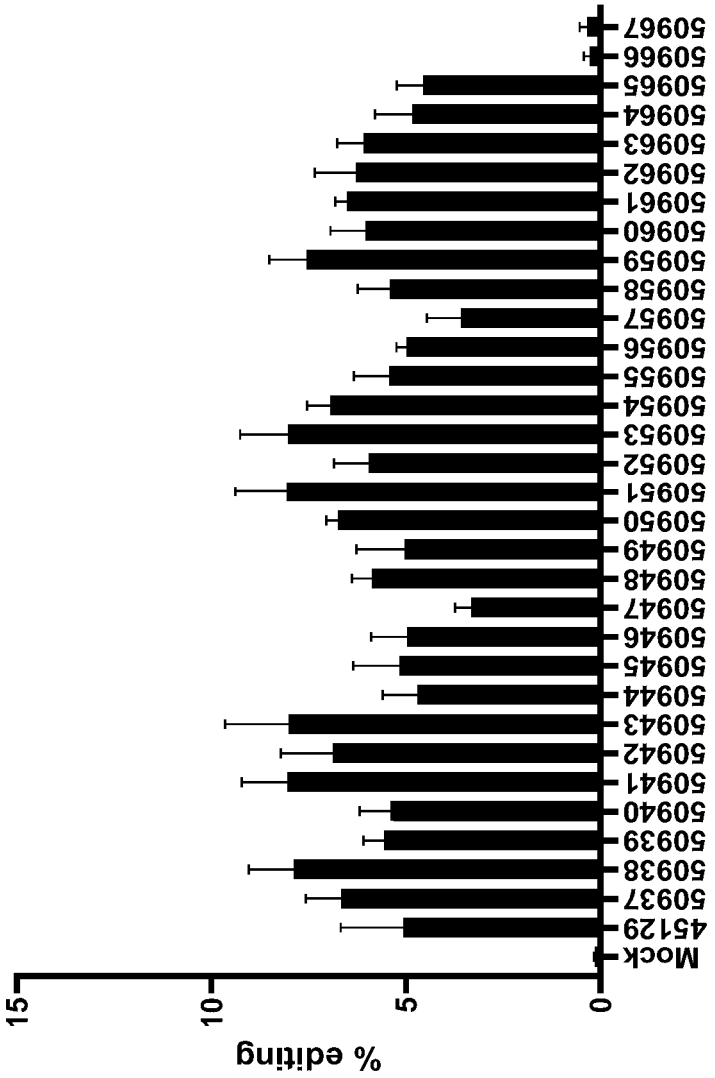

% editing
0
5
10
15
Mock
45129
50937
50938
50939
50940
50941
50942
50943
50944
50945
50946
50947
50948
50949
50950
50951
50952
50953
50954
50955
50956
50957
50958
50959
50960
50961
50962
50963
50964
50965
50966
50967

FIG. 23

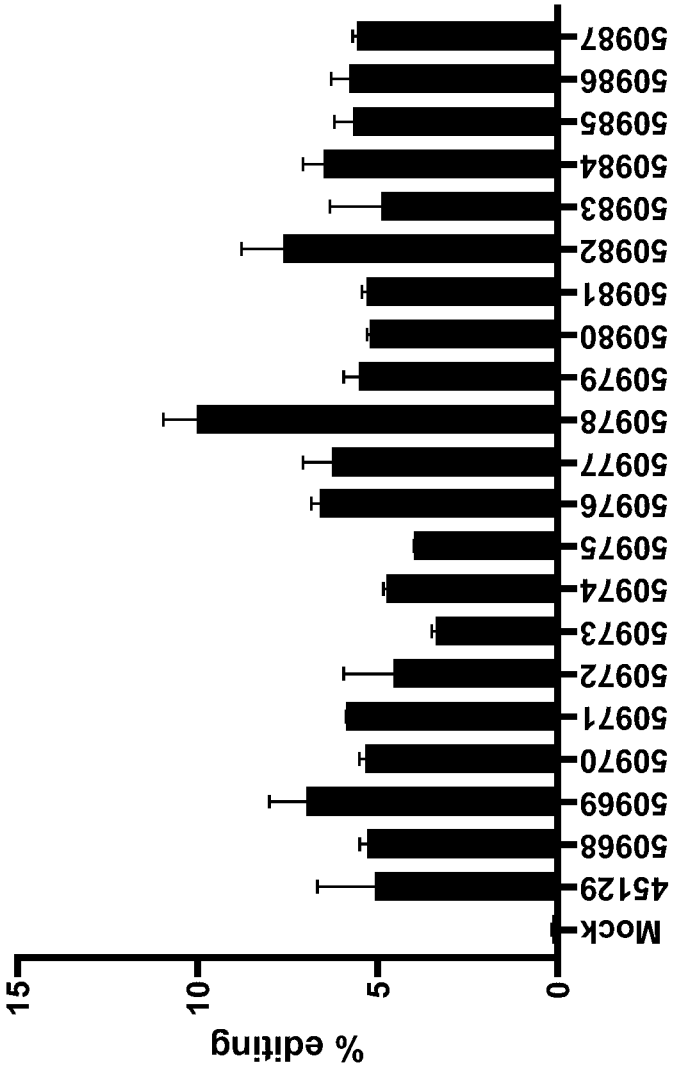

% editing
0
5
10
15
Mock
45129
50968
50969
50970
50971
50972
50973
50974
50975
50976
50977
50978
50979
50980
50981
50982
50983
50984
50985
50986
50987

FIG. 24

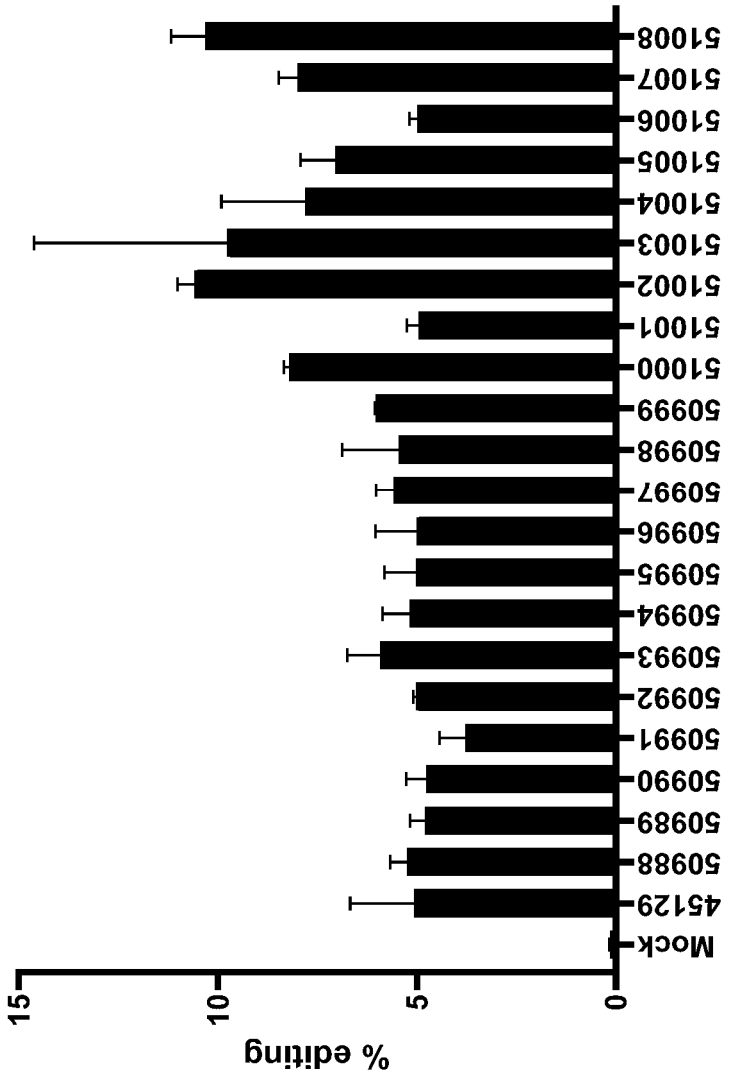

% editing
0
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Mock
45129
50988
50989
50990
50991
50992
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51001
51002
51003
51004
51005
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51007
51008

FIG. 25

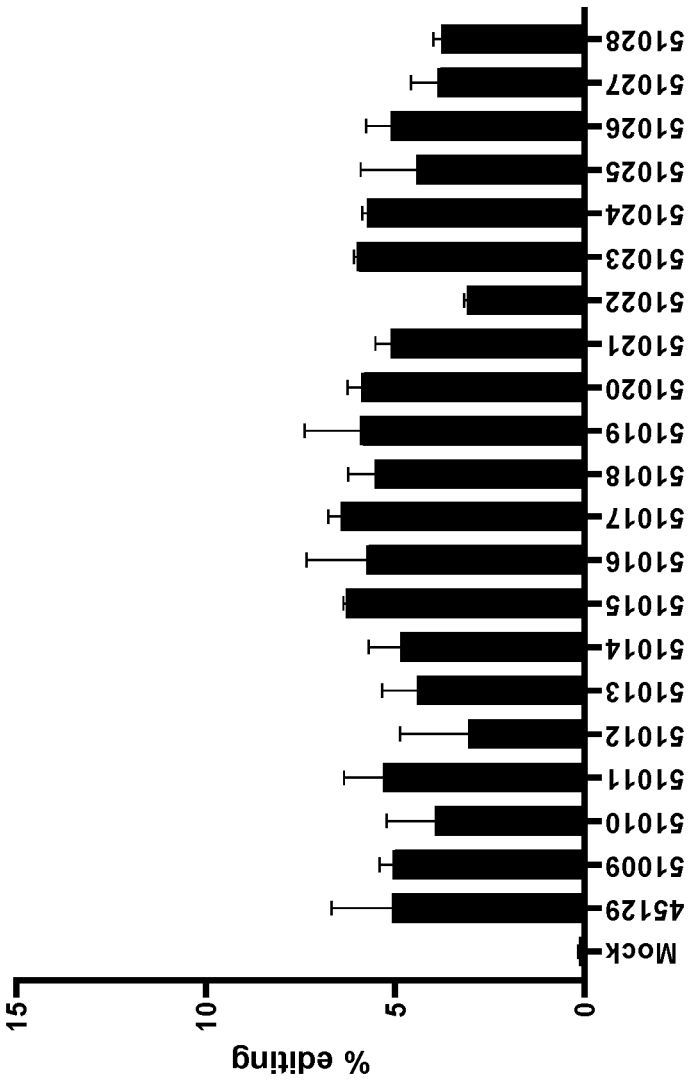

% editing
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Mock
45129
51009
51010
51011
51012
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51021
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51024
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FIG. 26

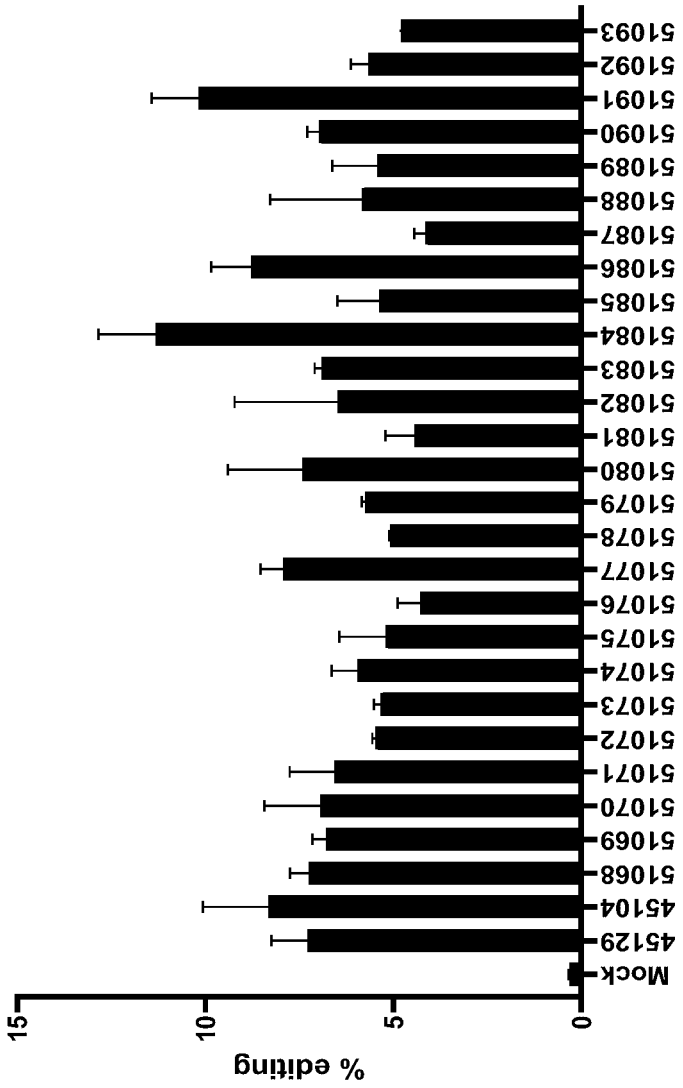

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Mock
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45104
51068
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FIG. 27

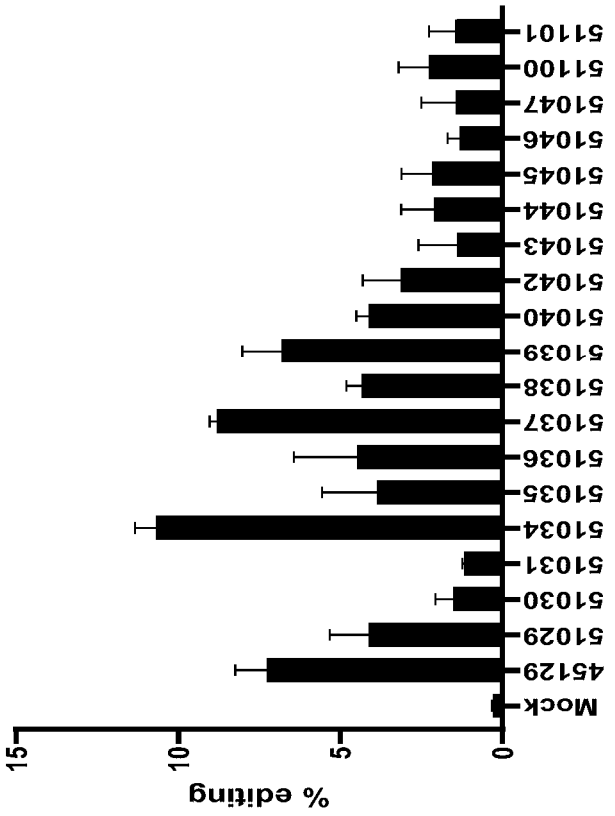

% editing
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Mock
45129
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51100
51101

FIG. 28

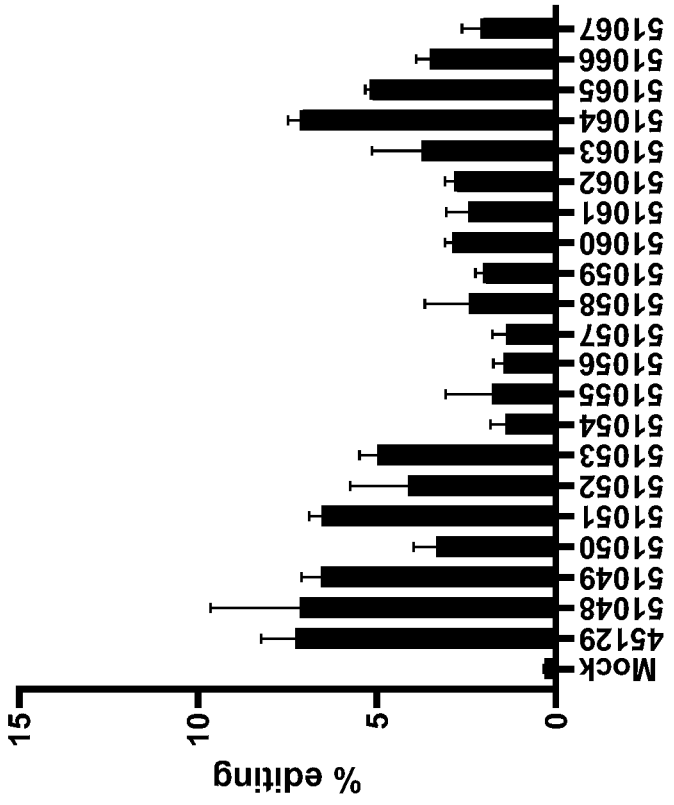

% editing
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Mock
45129
51048
51049
51050
51051
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FIG. 29

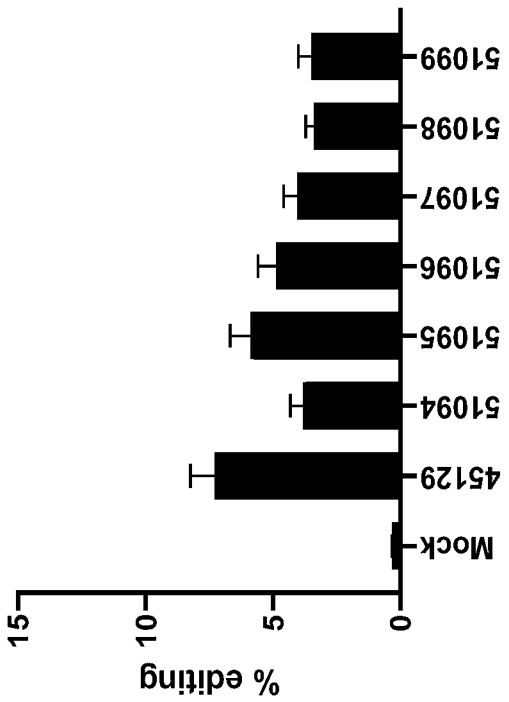

% editing
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Mock
45129
51094
51095
51096
51097
51098
51099

FIG. 30