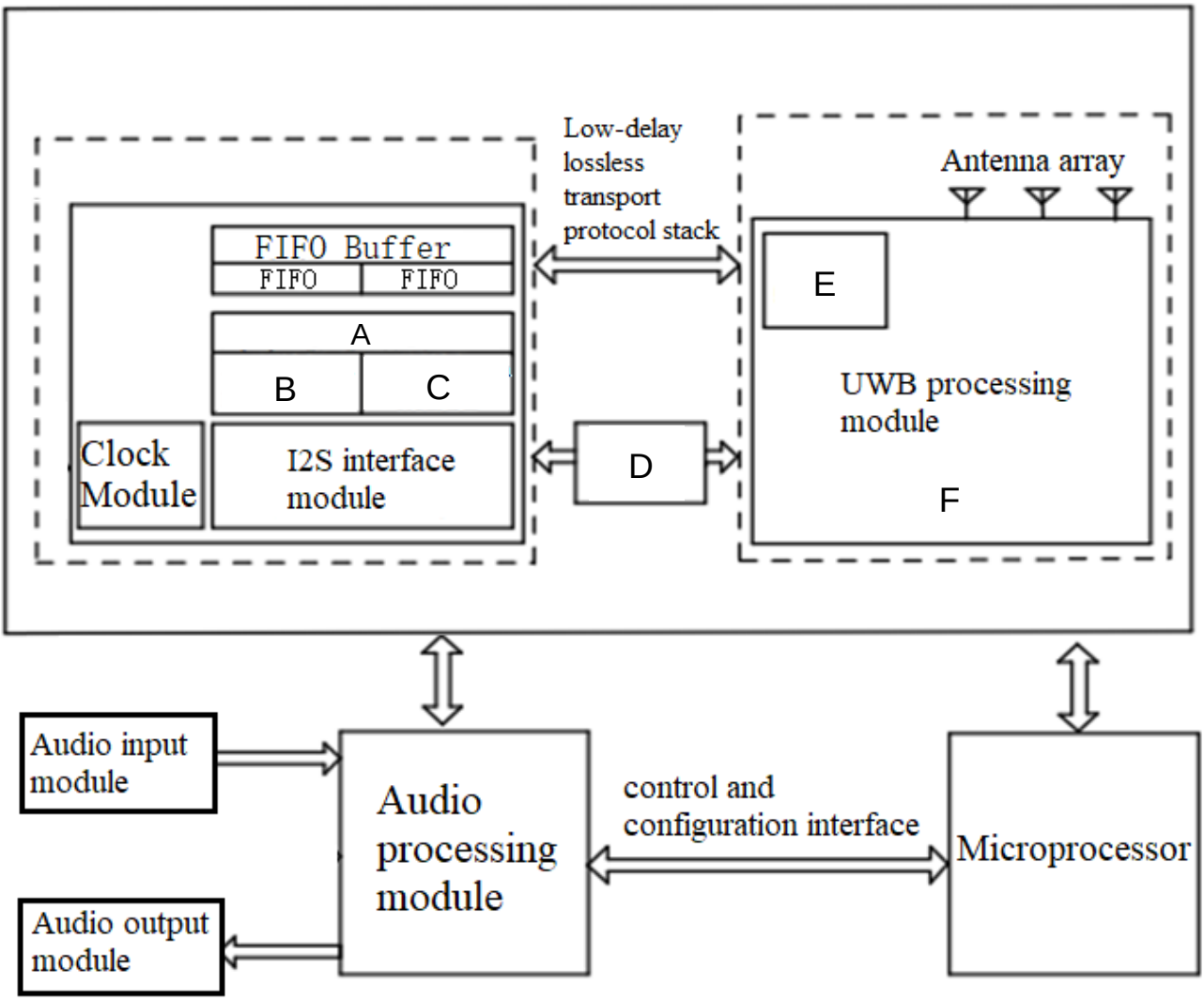
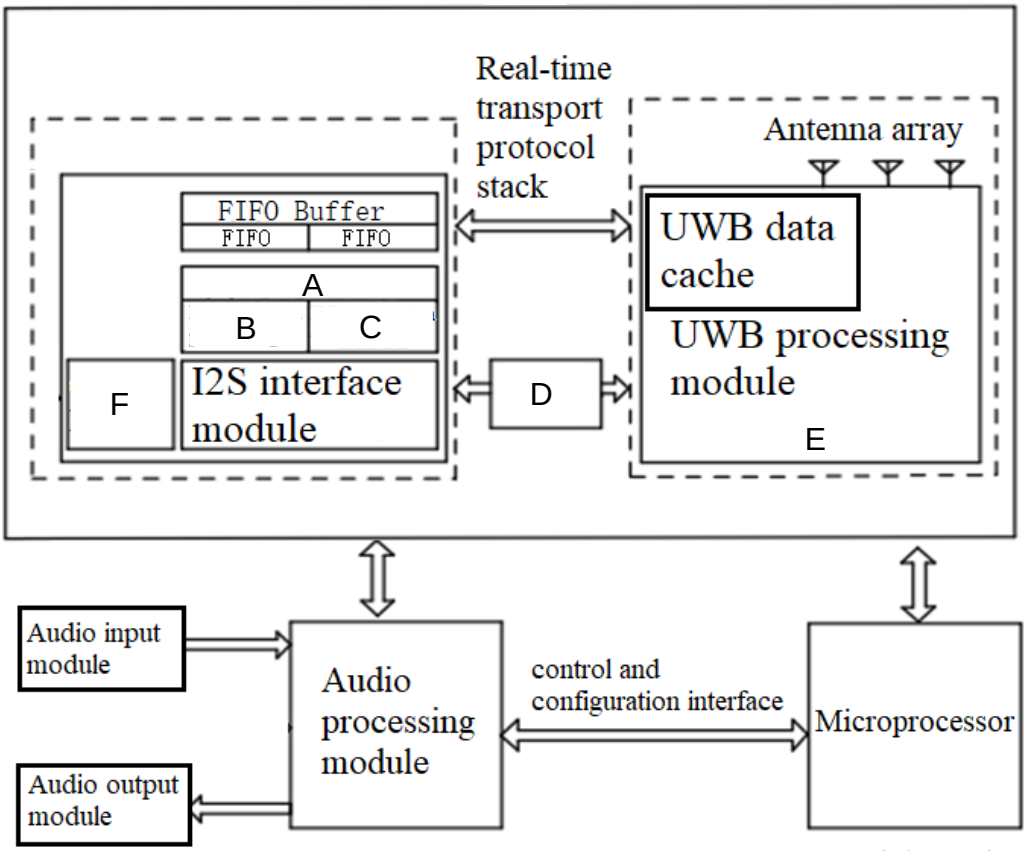


A: I2S data processing module
B: I2S input data processing
C: I2S output data processing
D: Power supply module
E: UWB data cache
F: Status information interface module

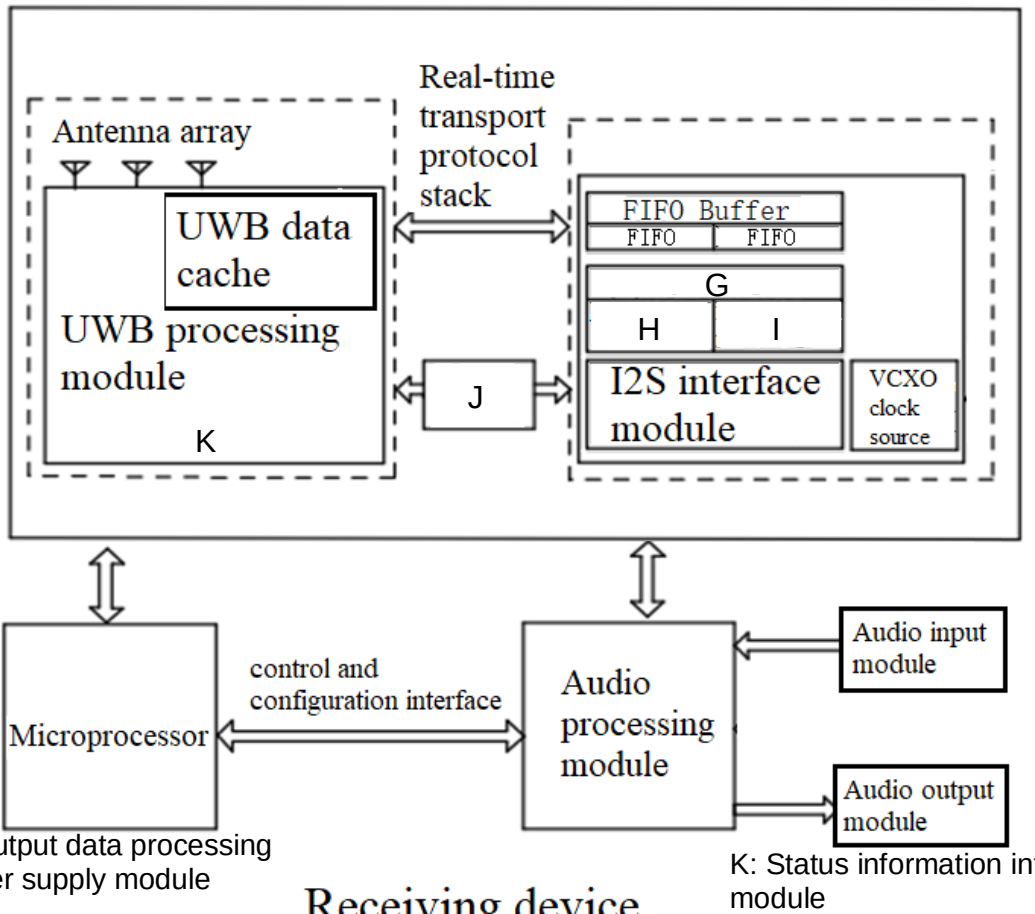
FIG.1



A: I2S data processing module
B: I2S input data processing
C: I2S output data processing
D: Power supply module

Transmitting device

E: Status information interface module
F: Reference clock source
G: I2S data processing module
H: I2S input data processing



I: I2S output data processing
J: Power supply module

Receiving device

K: Status information interface module

FIG.2

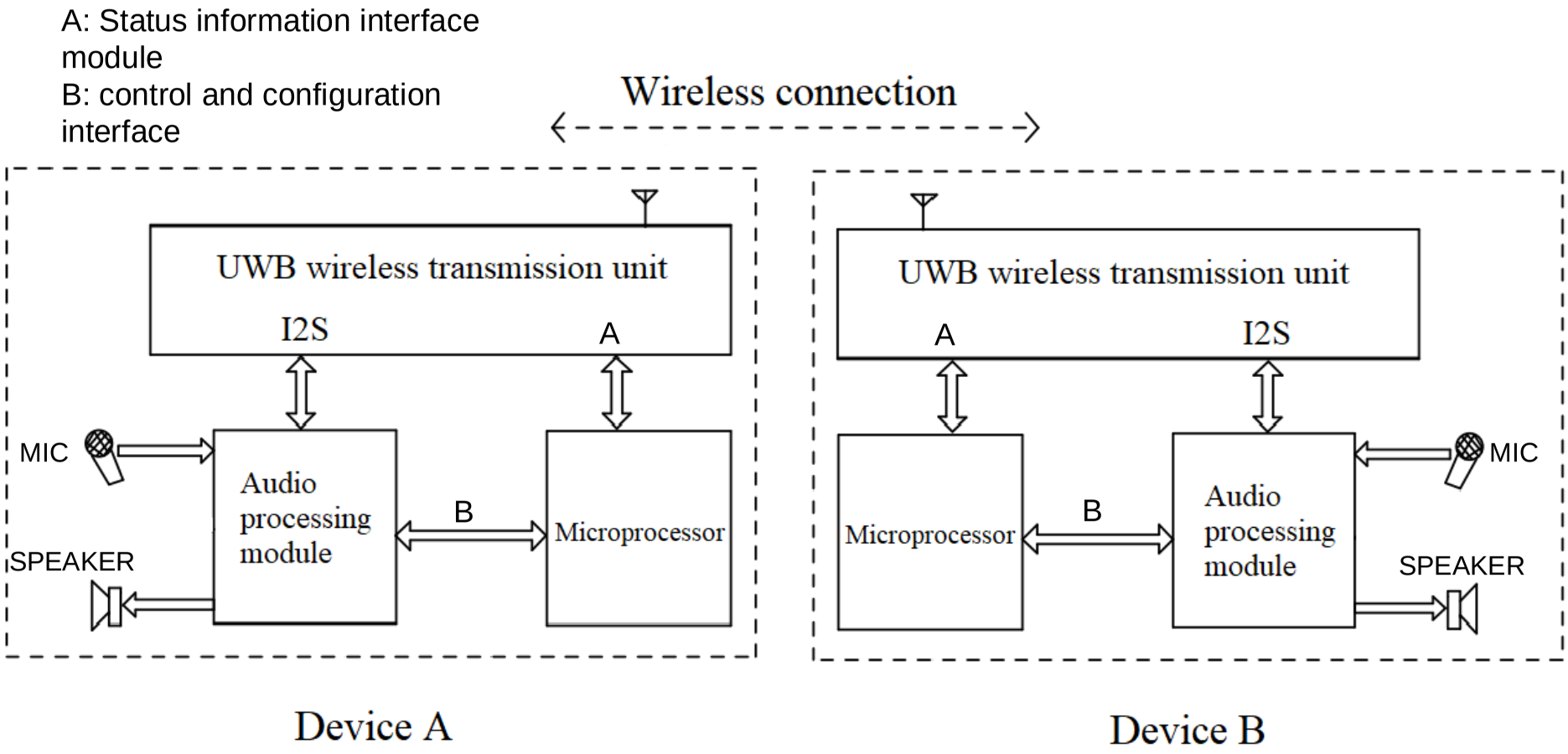
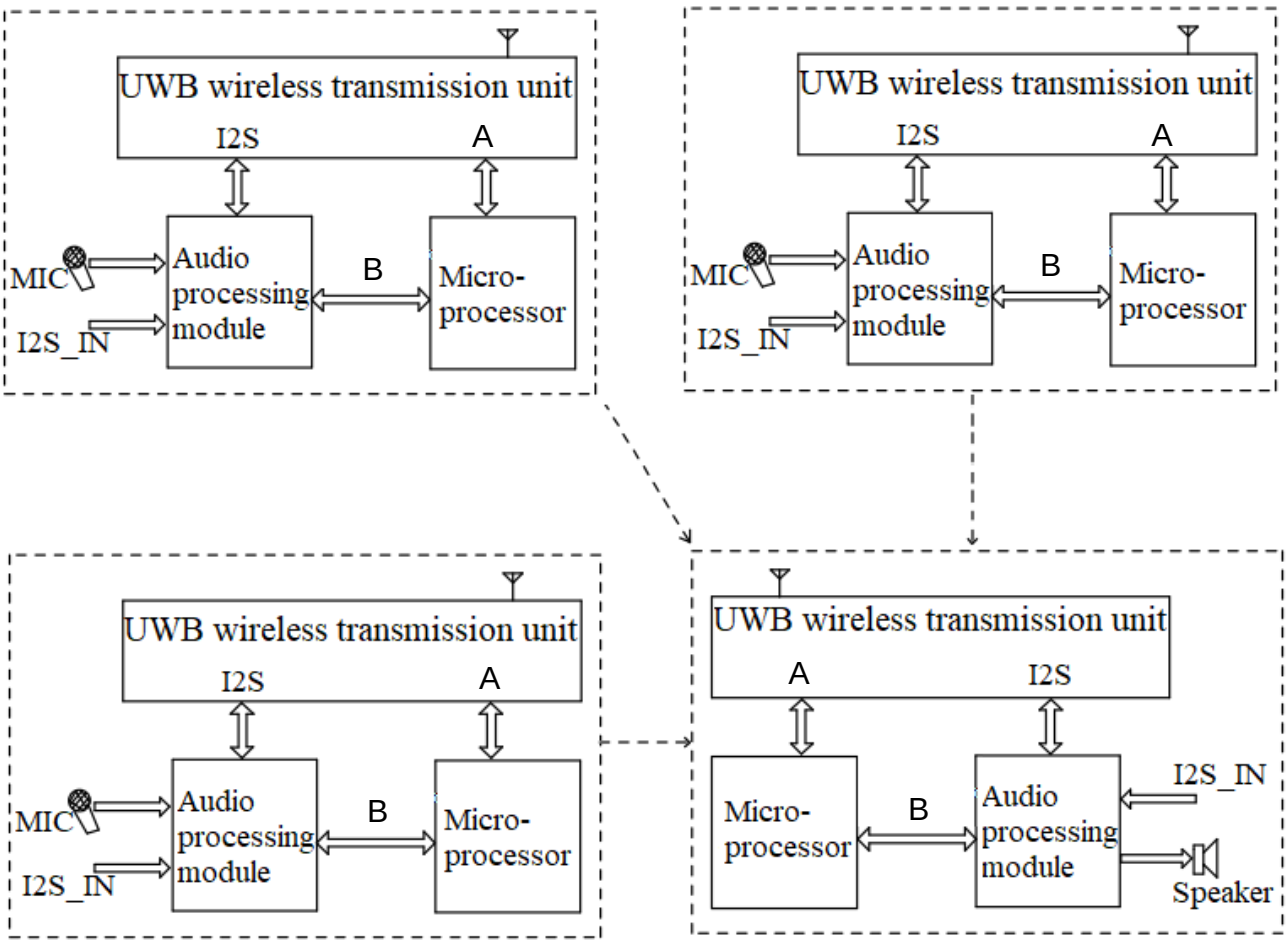
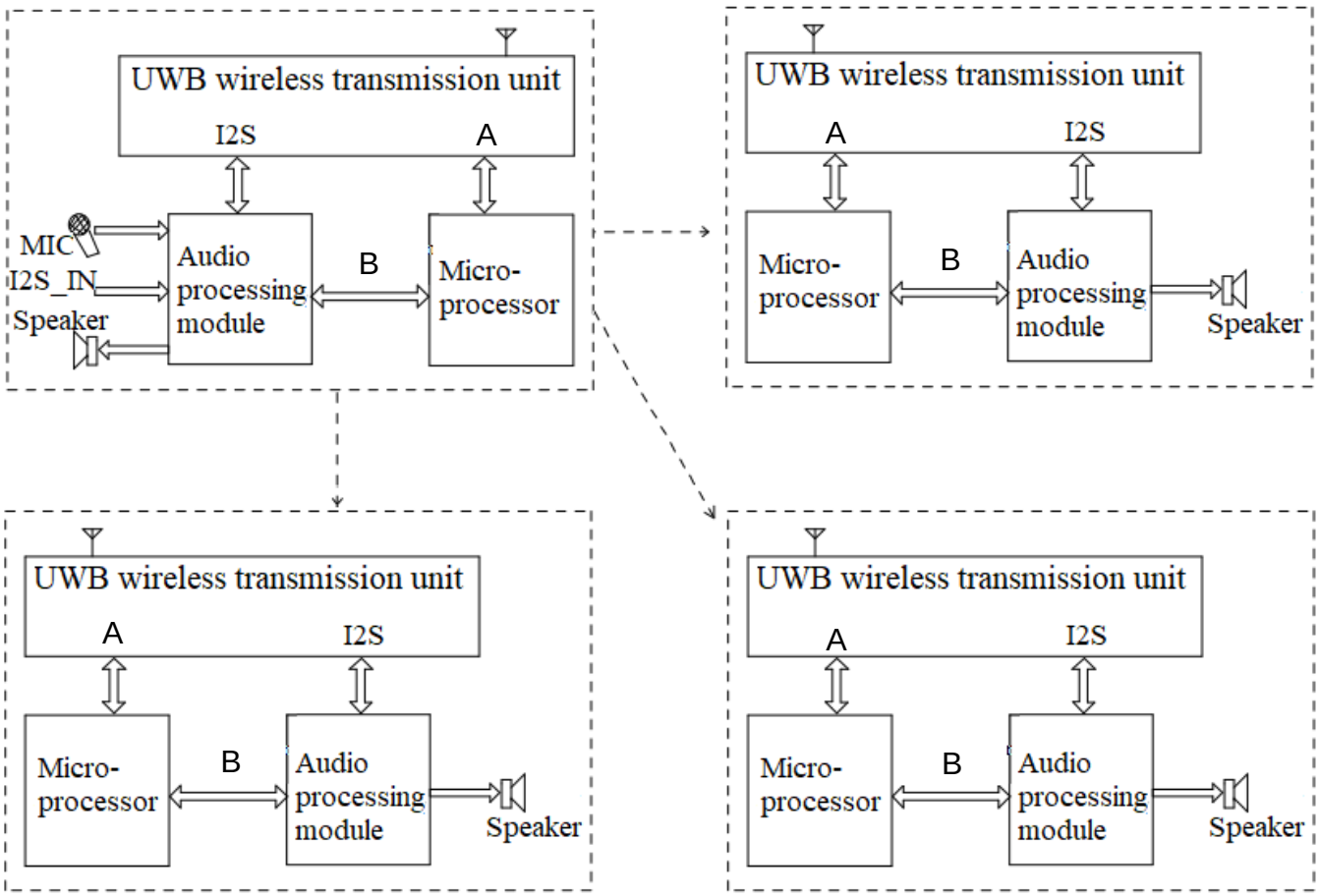

A: Status information interface module
B: control and configuration interface
Wireless connection
UWB wireless transmission unit
I2S
A
MIC
Audio processing module
B
Microprocessor
SPEAKER
Device A
UWB wireless transmission unit
A
I2S
Microprocessor
B
Audio processing module
MIC
SPEAKER
Device B

FIG.3



A: Status information interface module
B: Control and configuration interface

FIG.4



A: A: Status information interface module
B: Control and configuration interface

FIG.5