

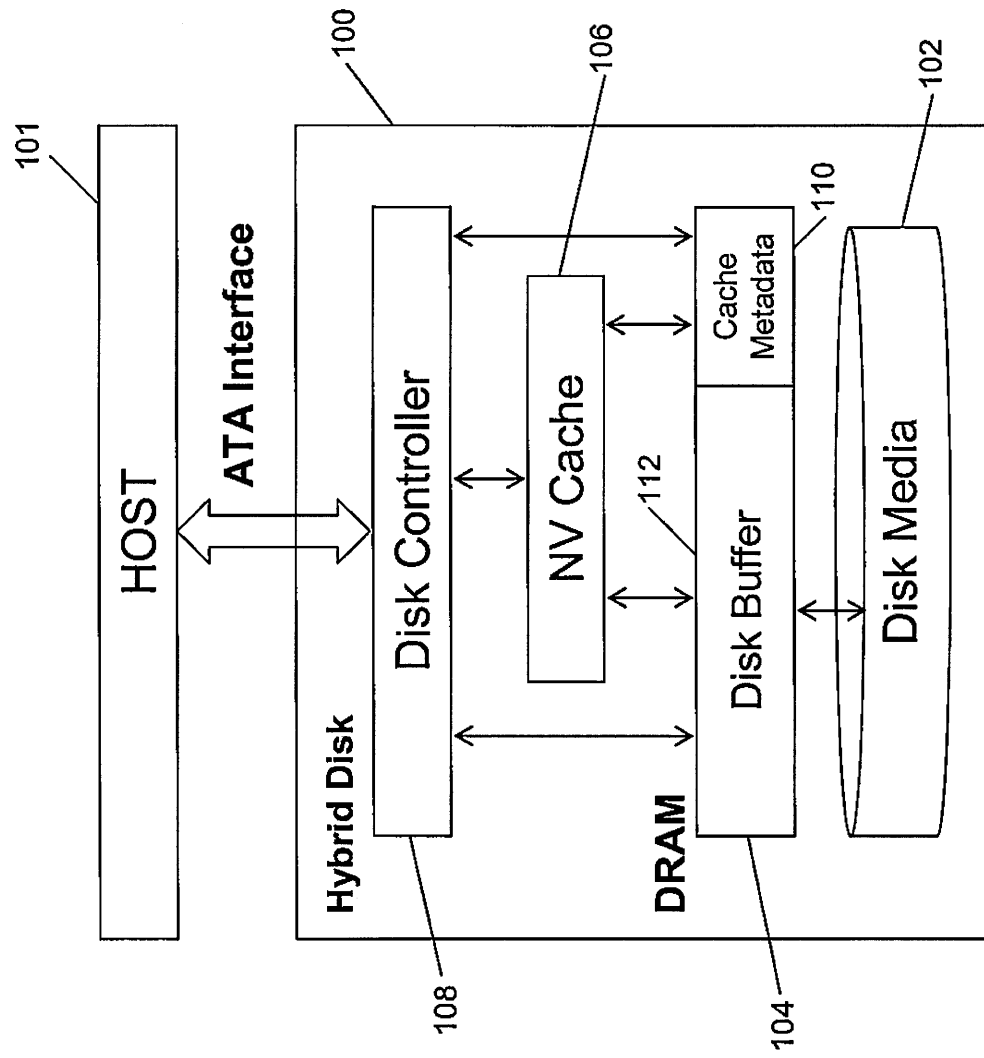


FIG. 1

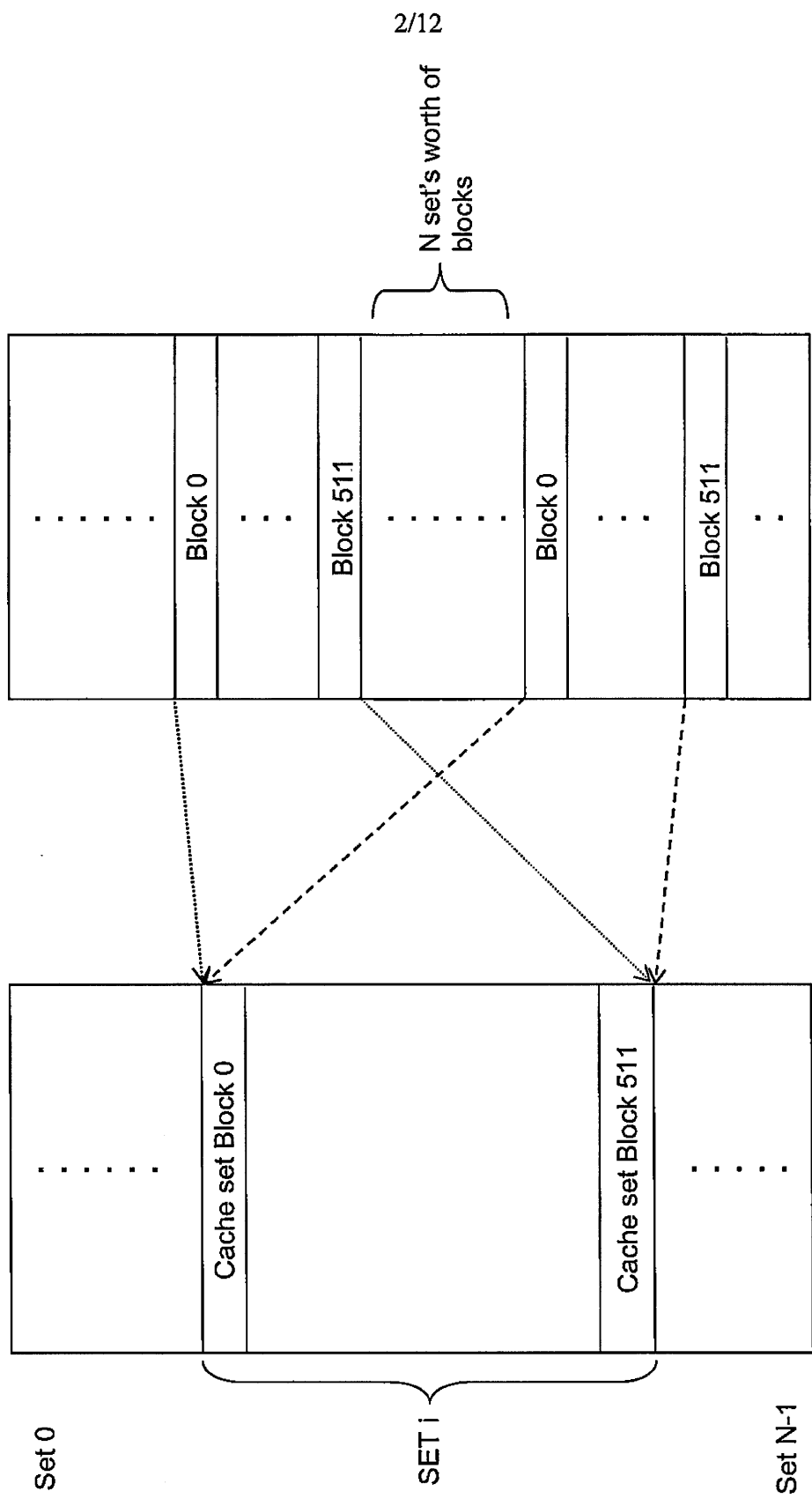


FIG. 2

300

	Logical Block Address (6 bytes for ATA standard)	Status (2 bits)
CBA 0	LBA i	clean
		
		dirty
		
CBA max 2^{21}	LBA j	free

FIG. 3

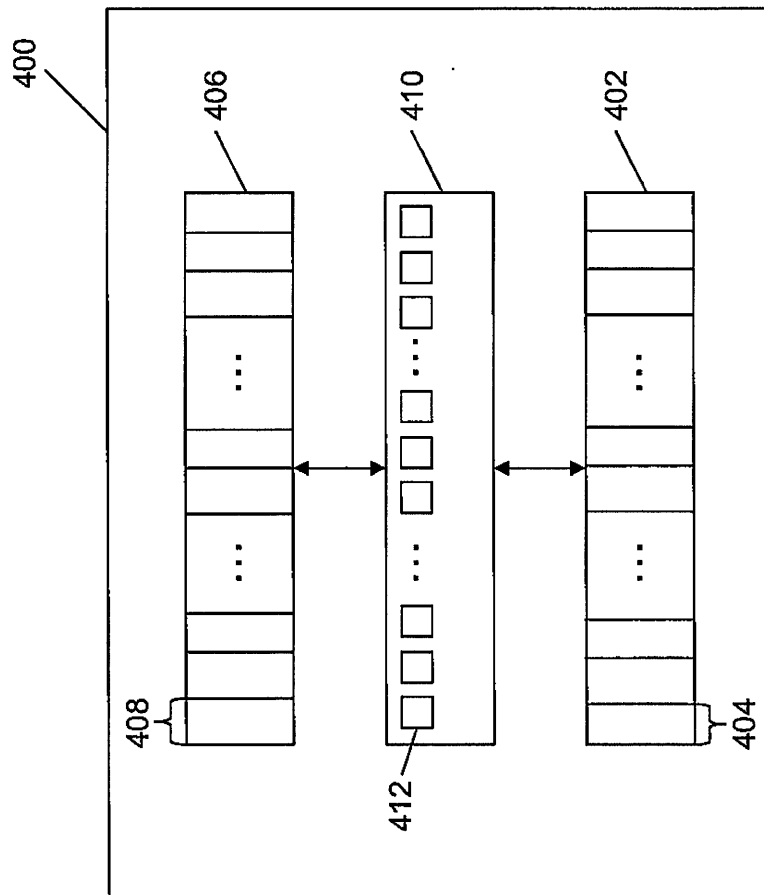


FIG. 4

506

LAB max 228

500

FIG. 5

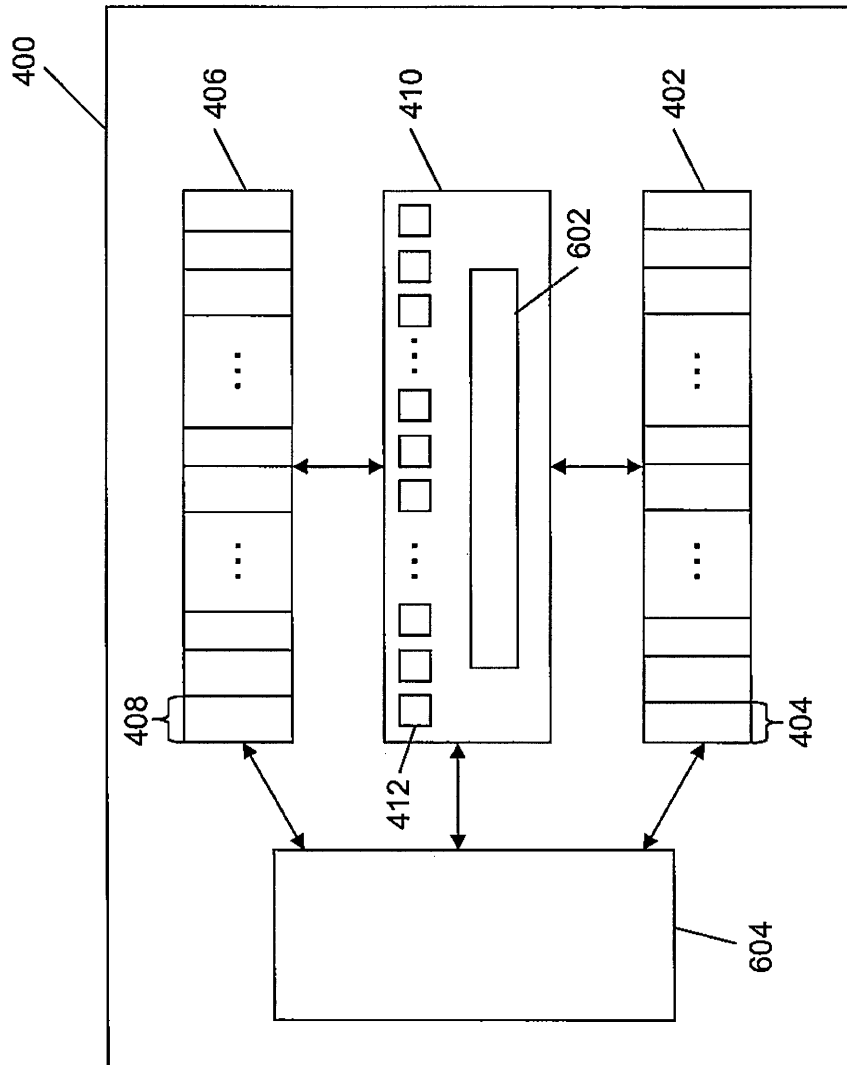


FIG. 6

700

2 bits for CBA 0 status		
.....		
.....		
.....		
.....		2 bits for CBA max 2^{21} status

FIG. 7

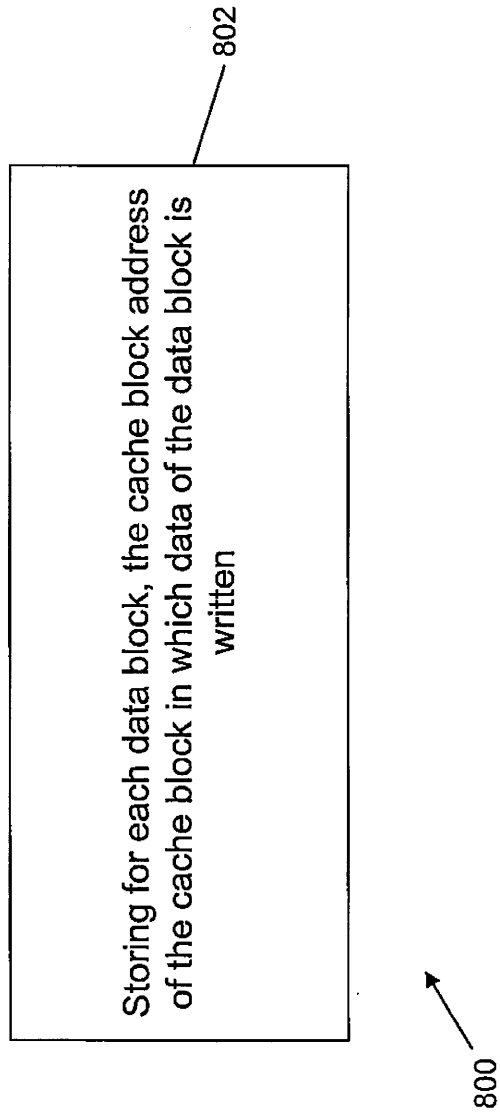


FIG. 8

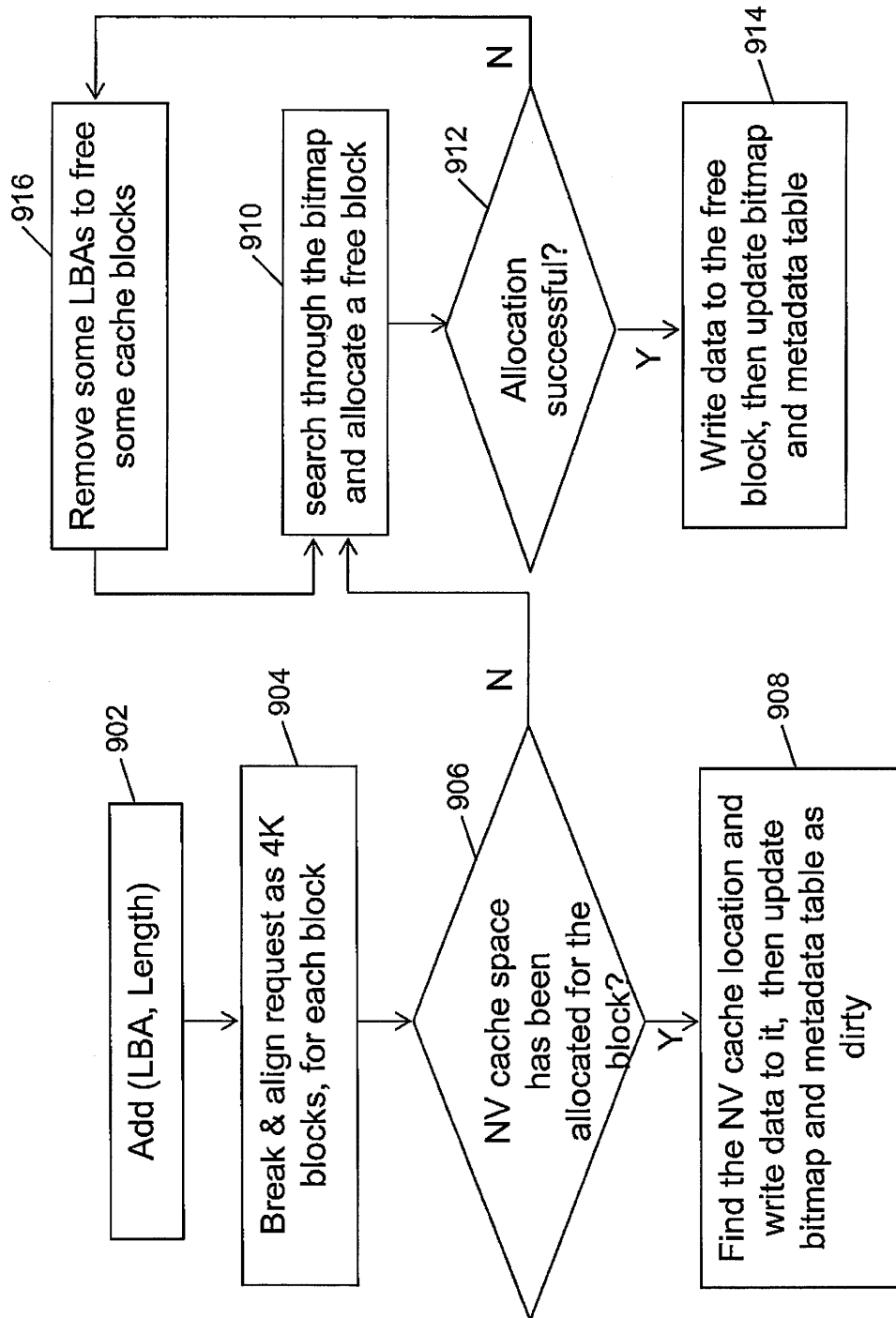


FIG. 9

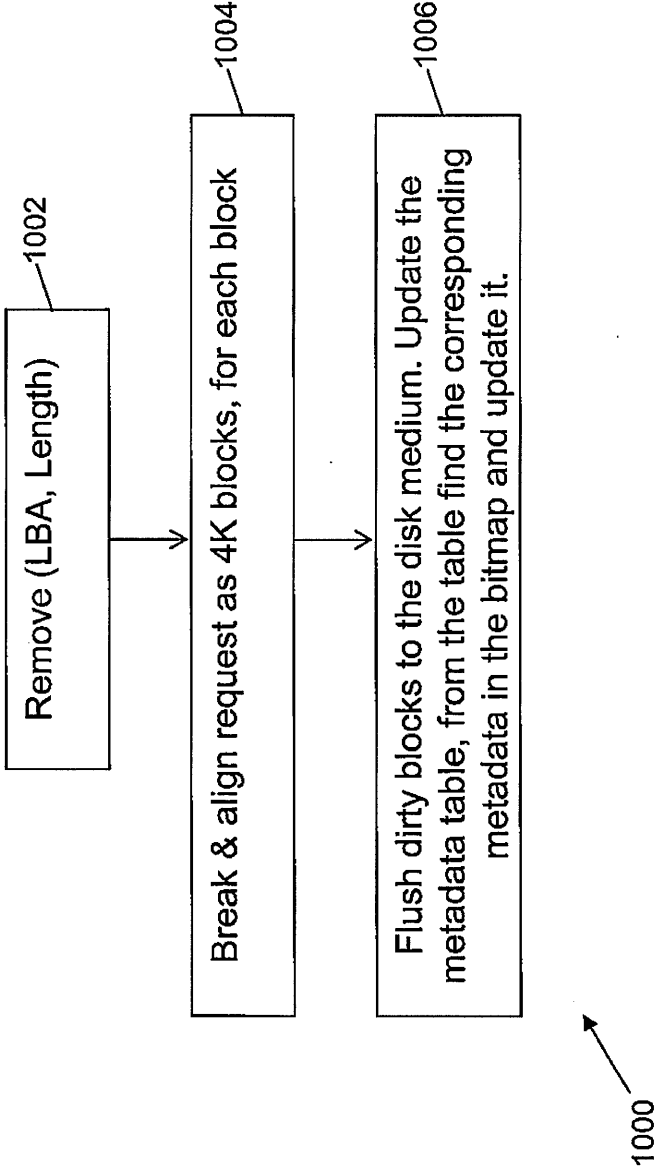


FIG. 10

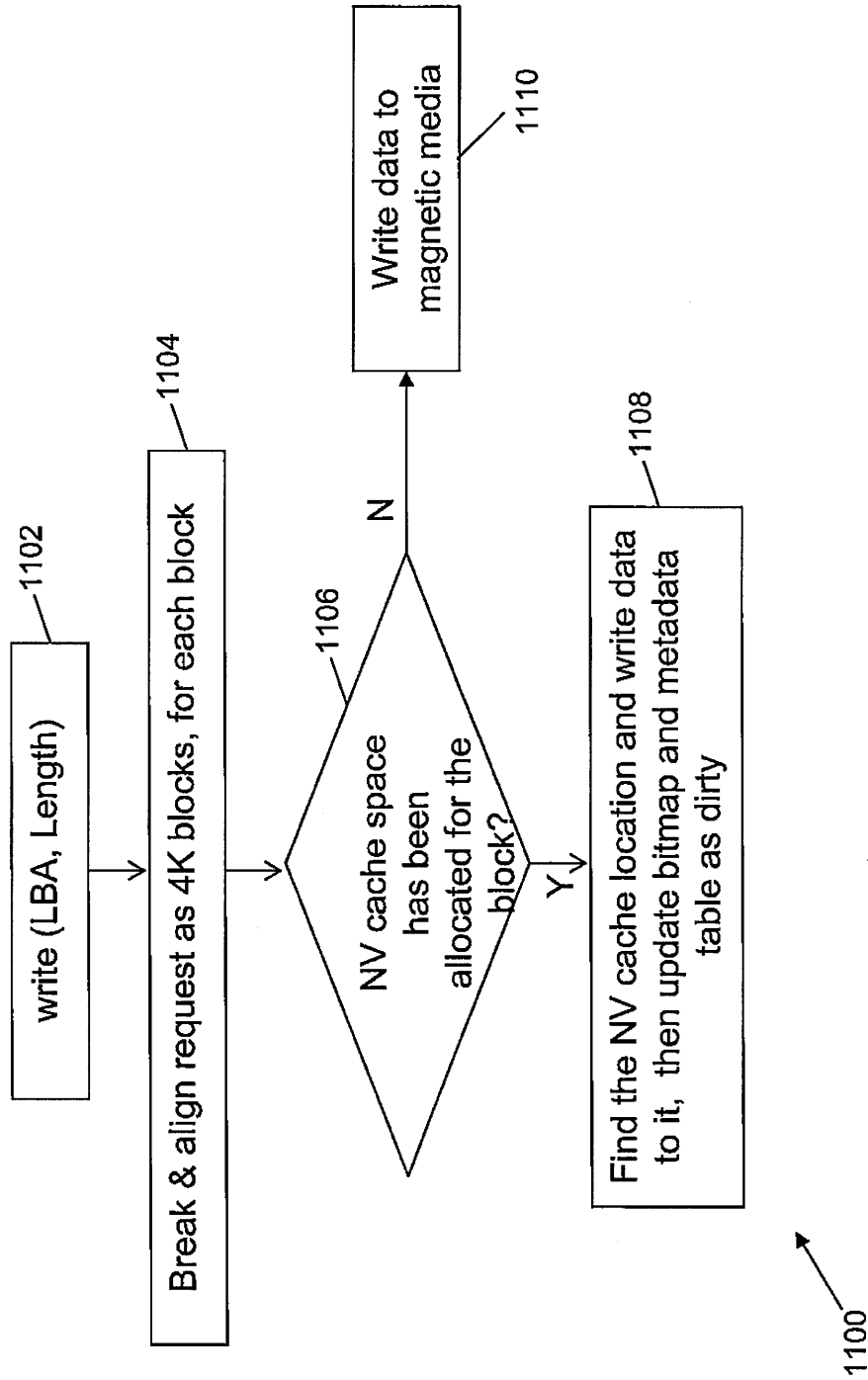


FIG. 11

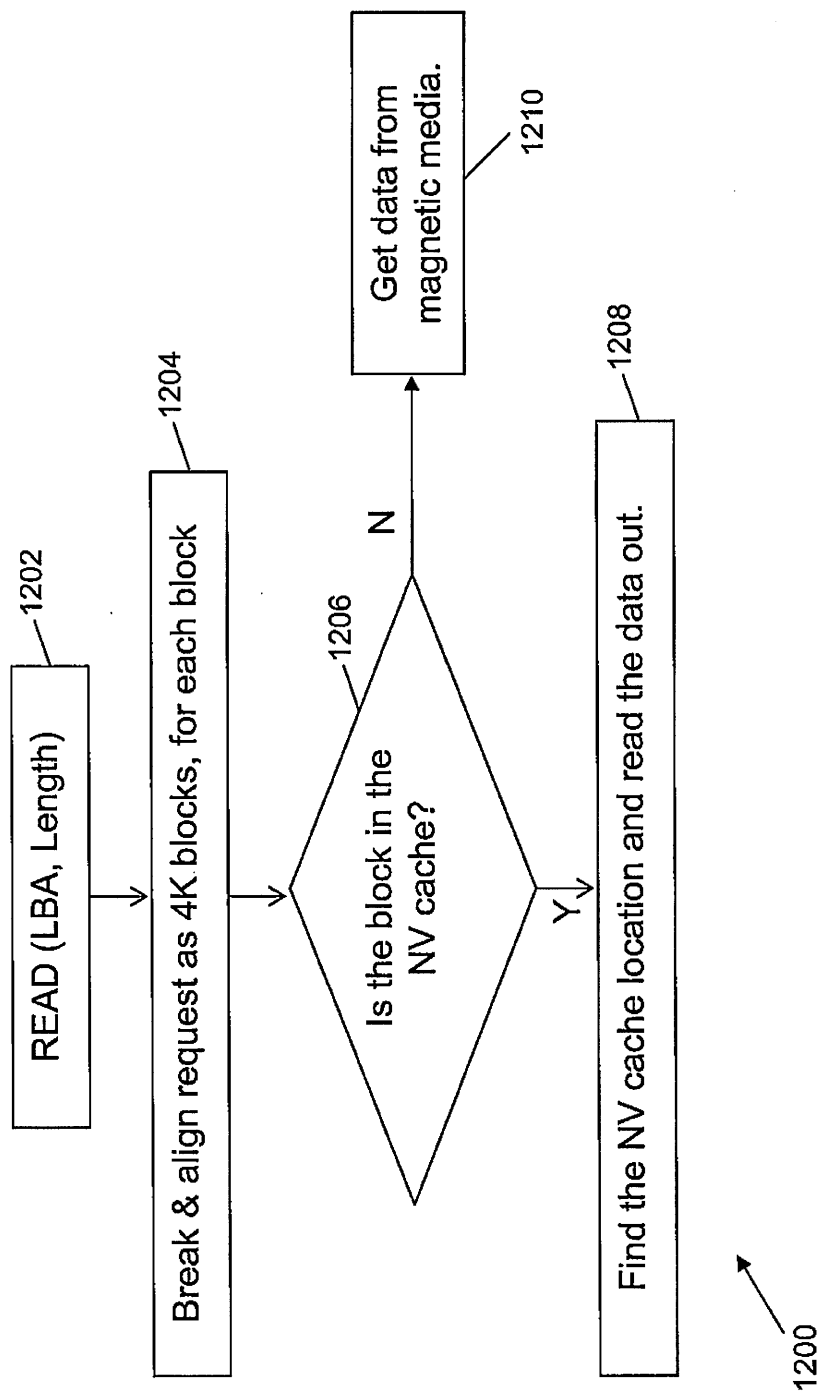


FIG. 12