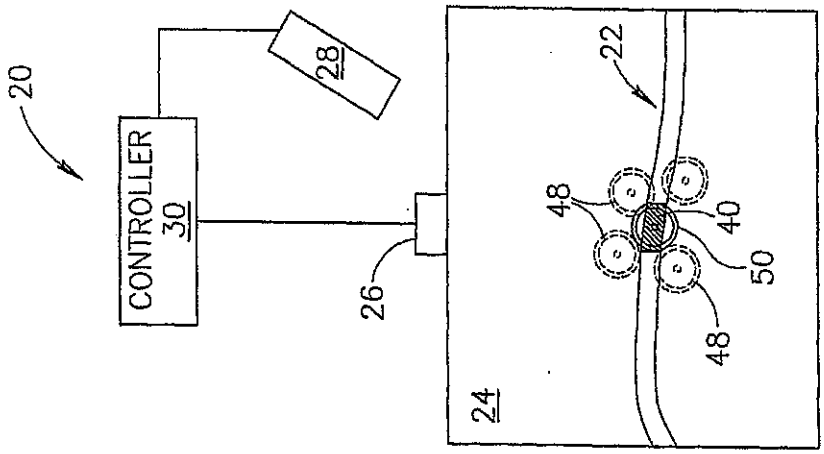


FIG.1C

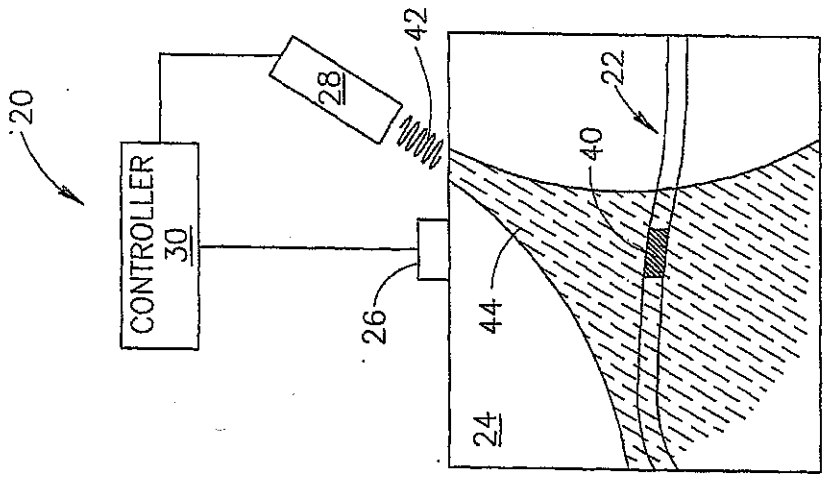


FIG.1B

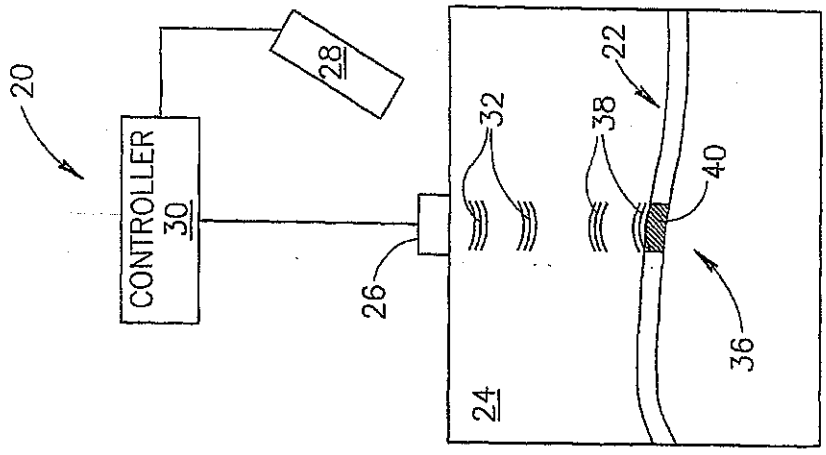


FIG.1A

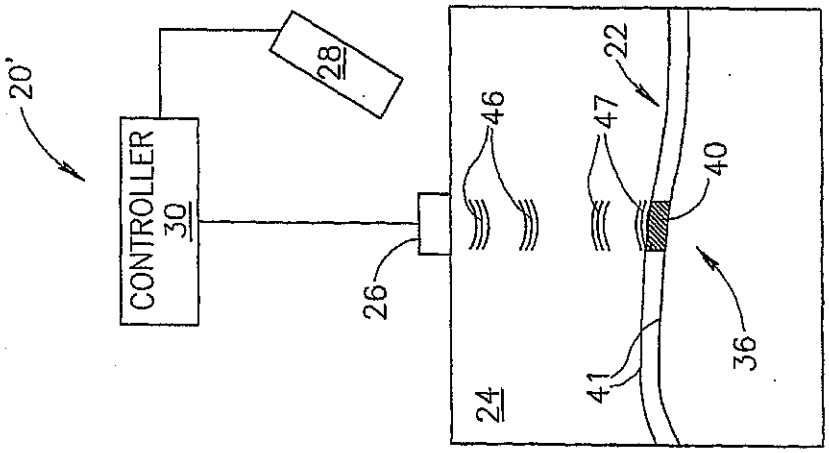


FIG.2C

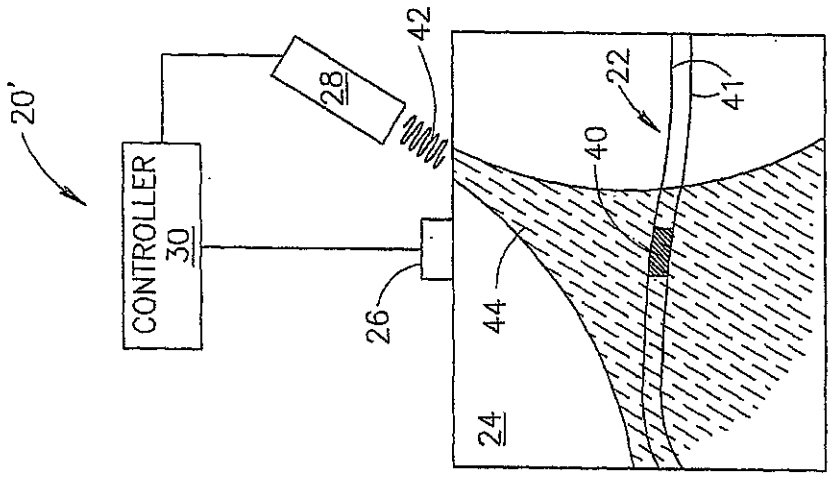


FIG.2B

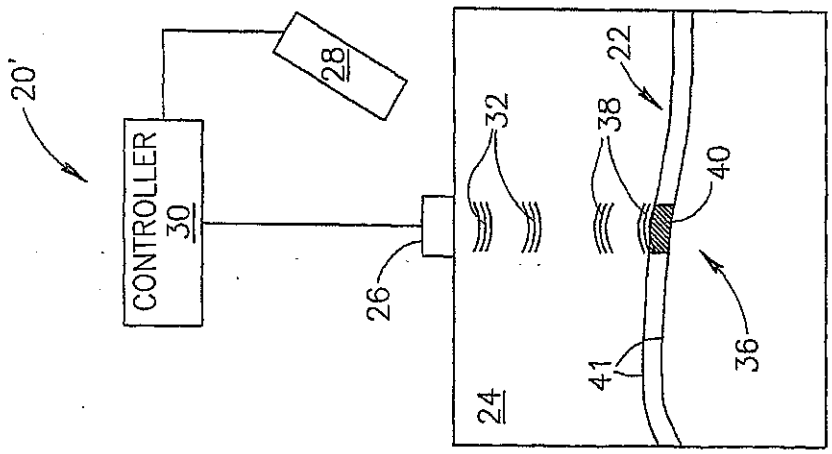


FIG.2A

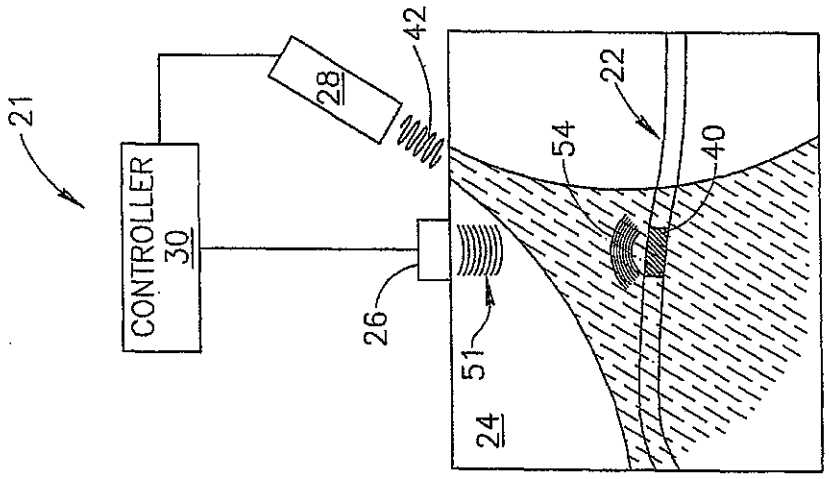

21
CONTROLLER
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42
26
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FIG.3B

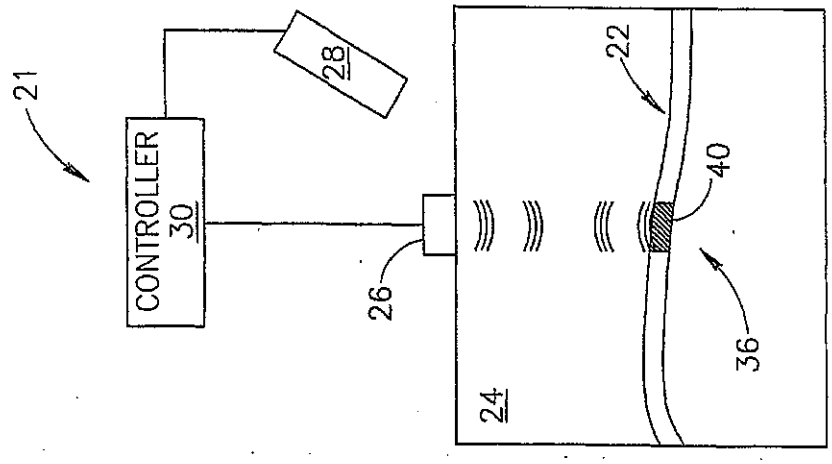

21
CONTROLLER
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FIG.3A

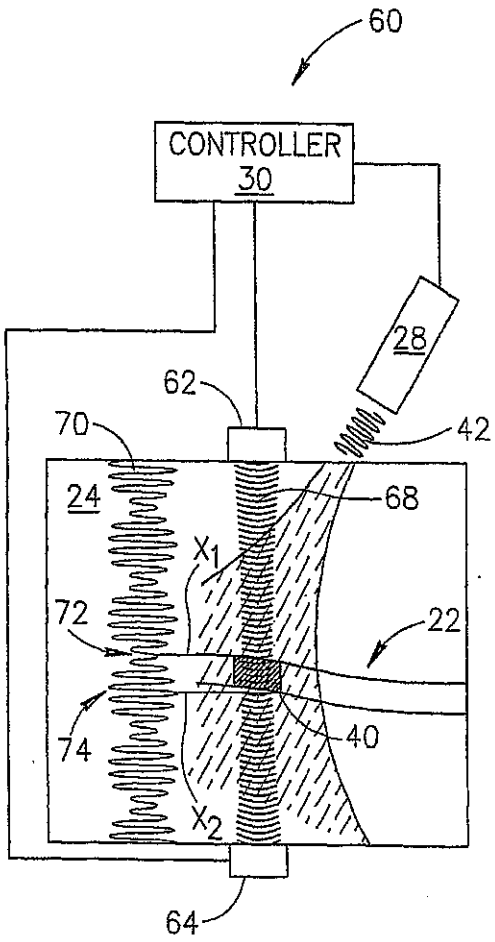


FIG.4

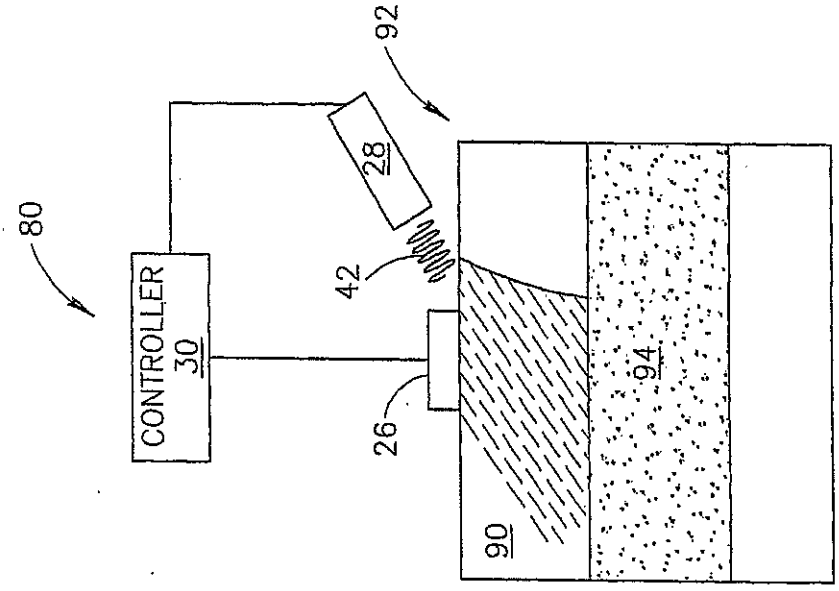

92
28
80
42
CONTROLLER
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26
94
90

FIG.5B

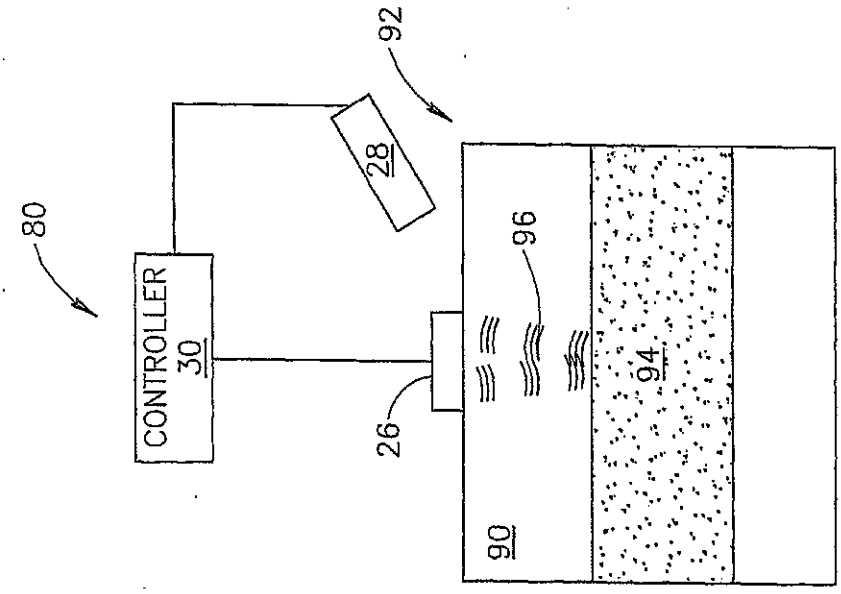

92
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96
CONTROLLER
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94
90

FIG.5A

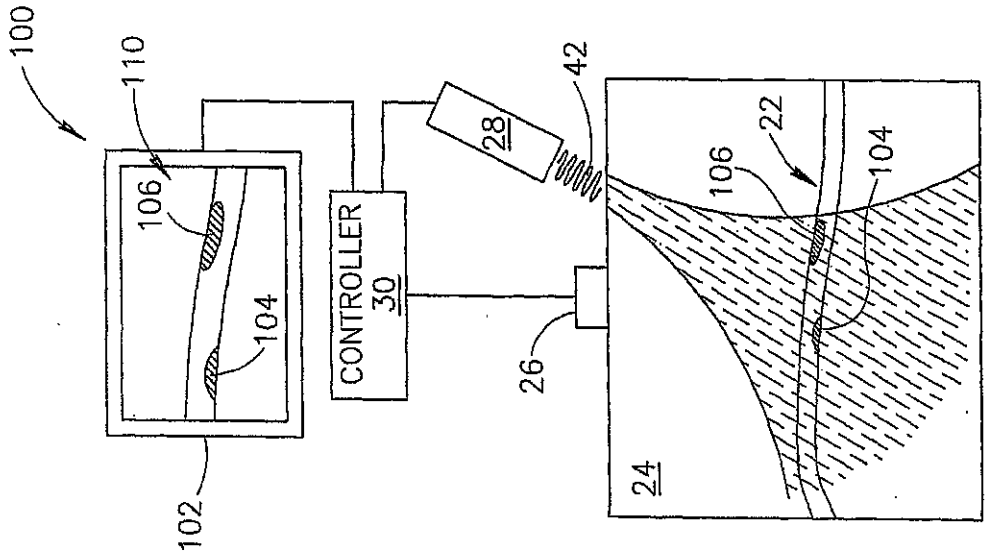

100
110
106
104
102
CONTROLLER
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24
22
106
104

FIG.6B

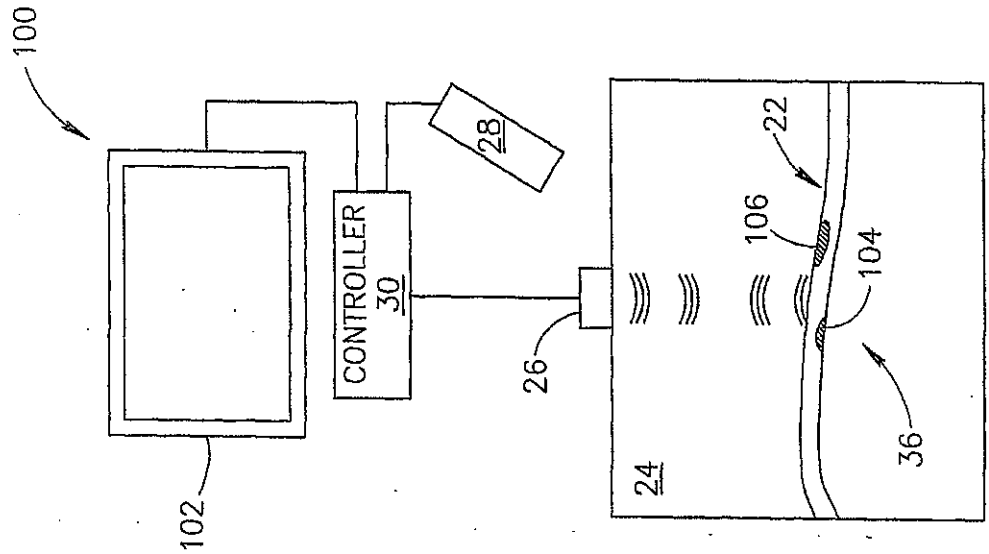

100
102
CONTROLLER
30
28
26
24
22
106
104
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FIG.6A