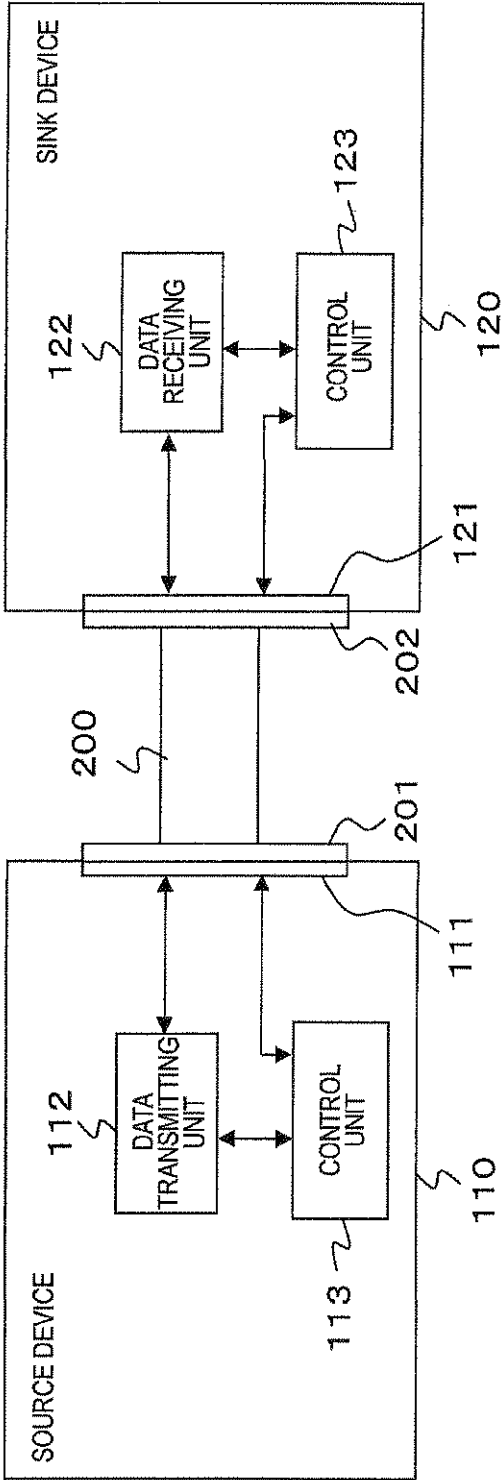


FIG. 1

100



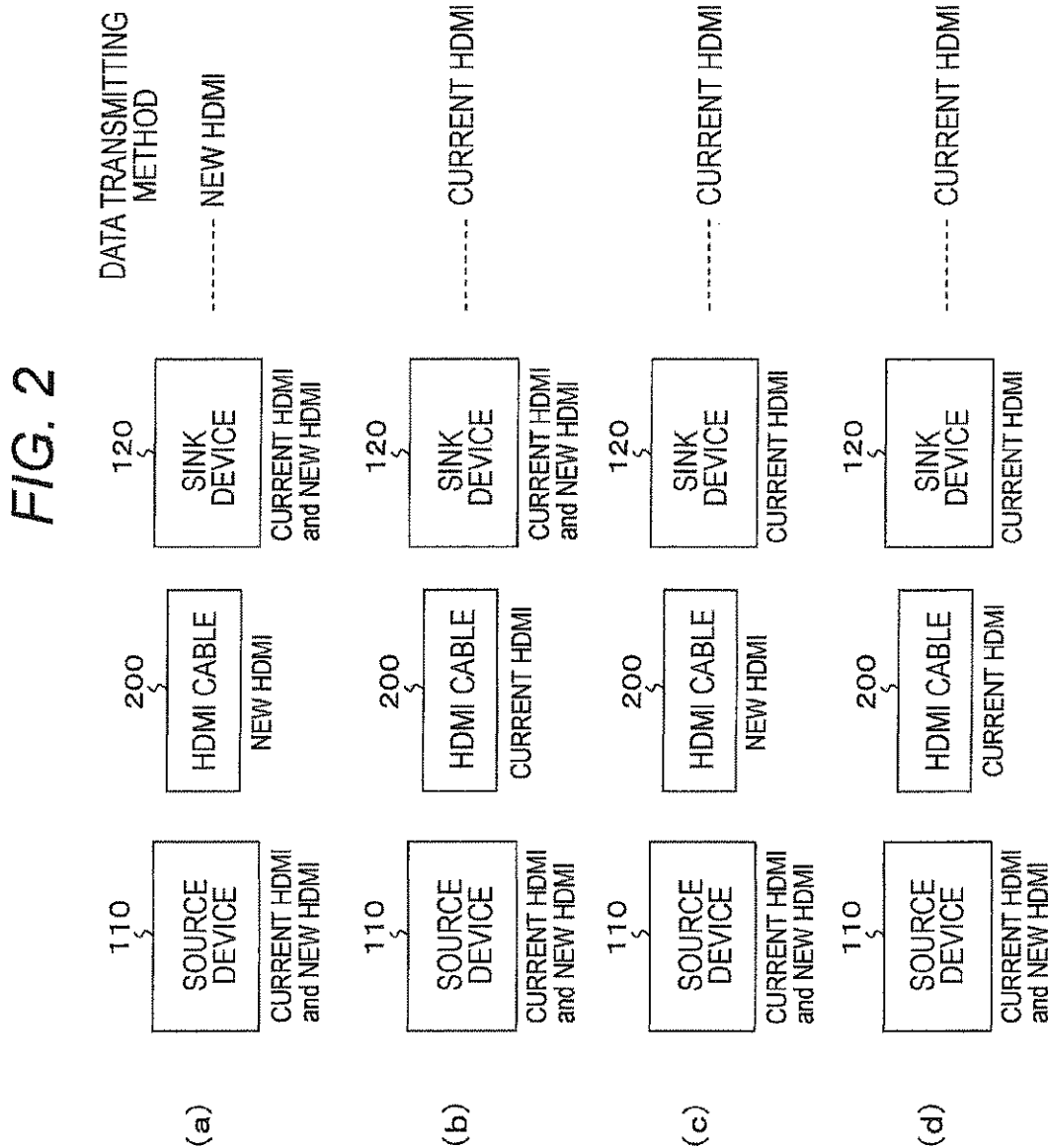


FIG. 3

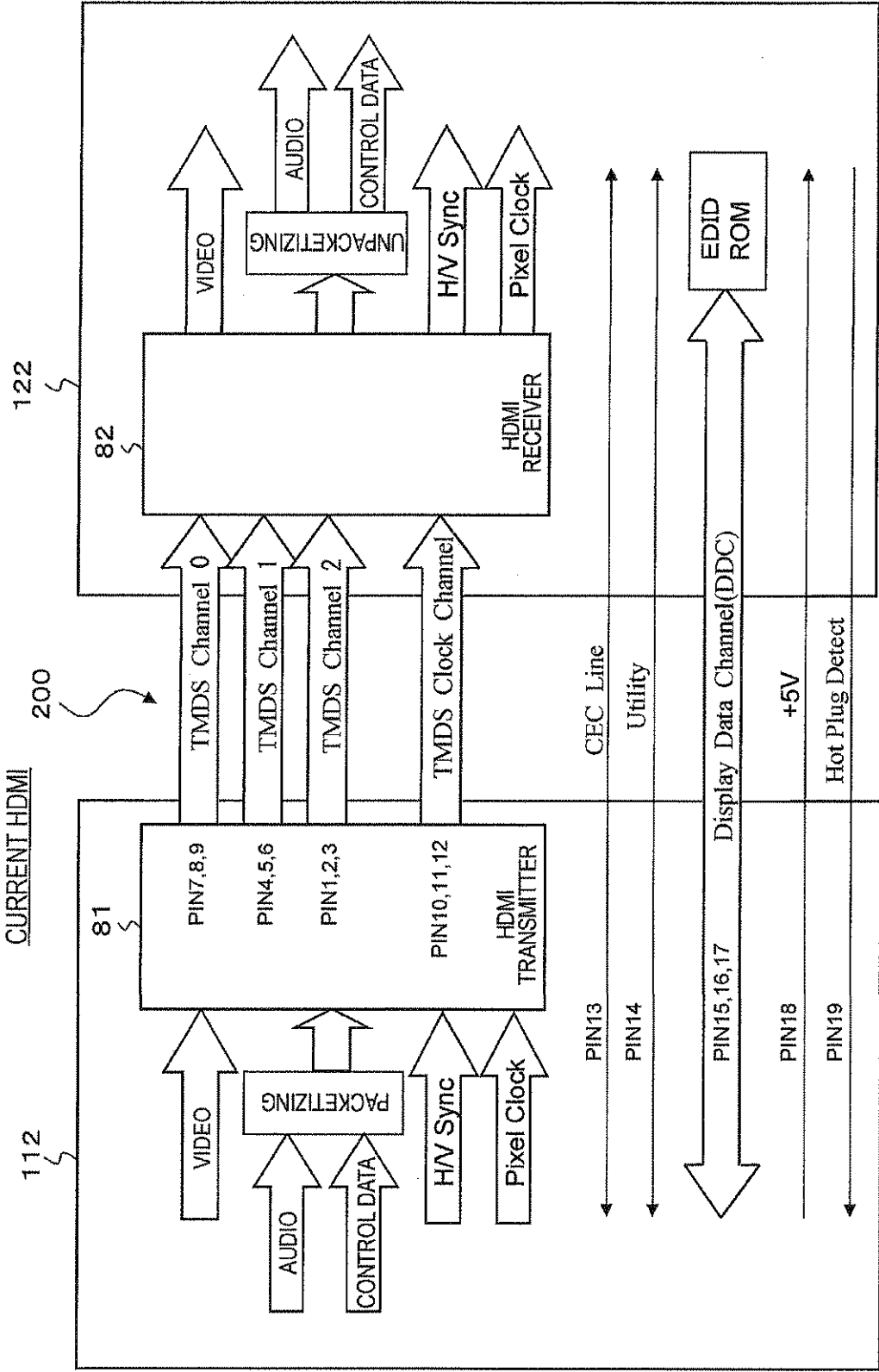


FIG. 4

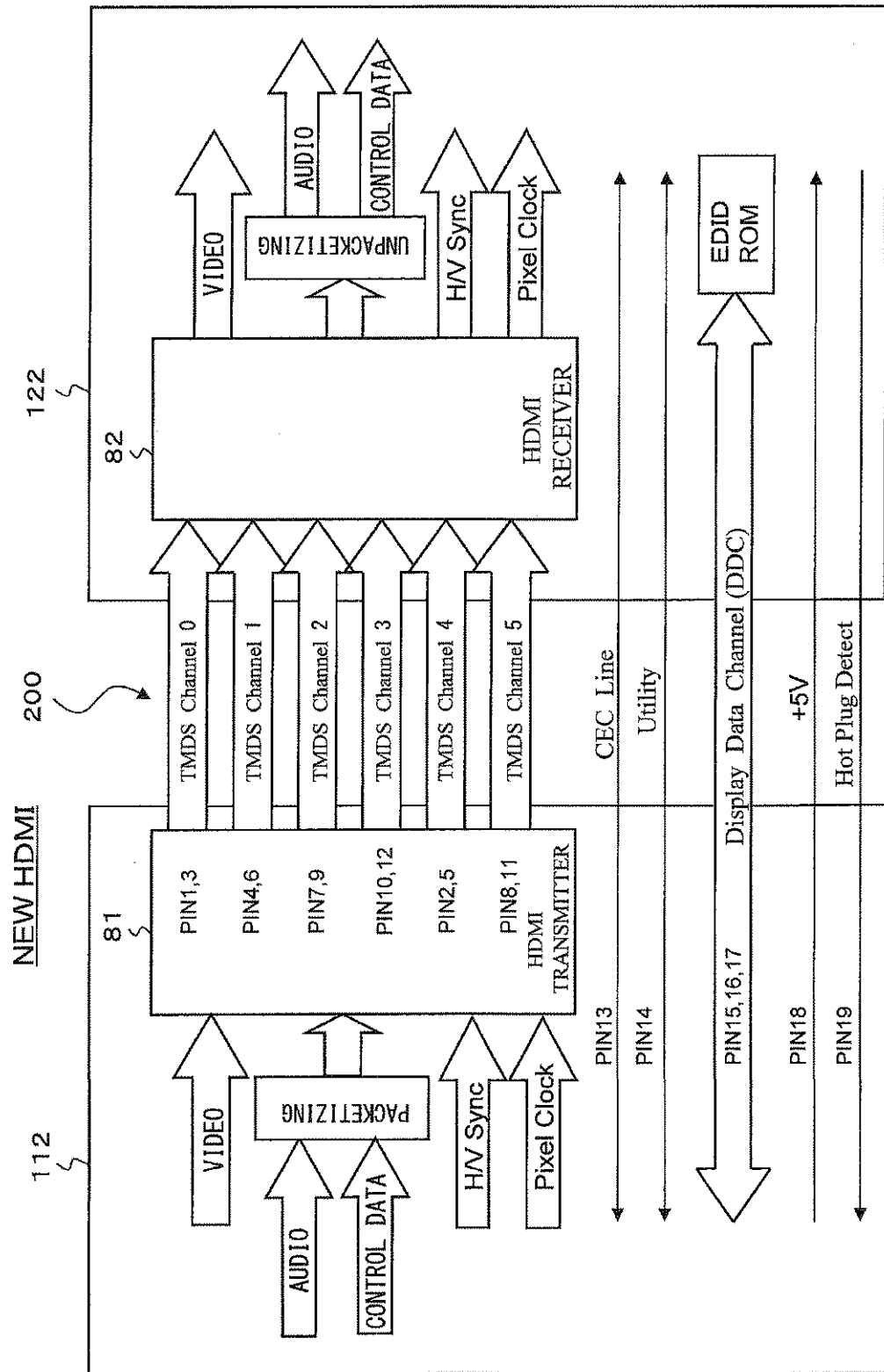


FIG. 5

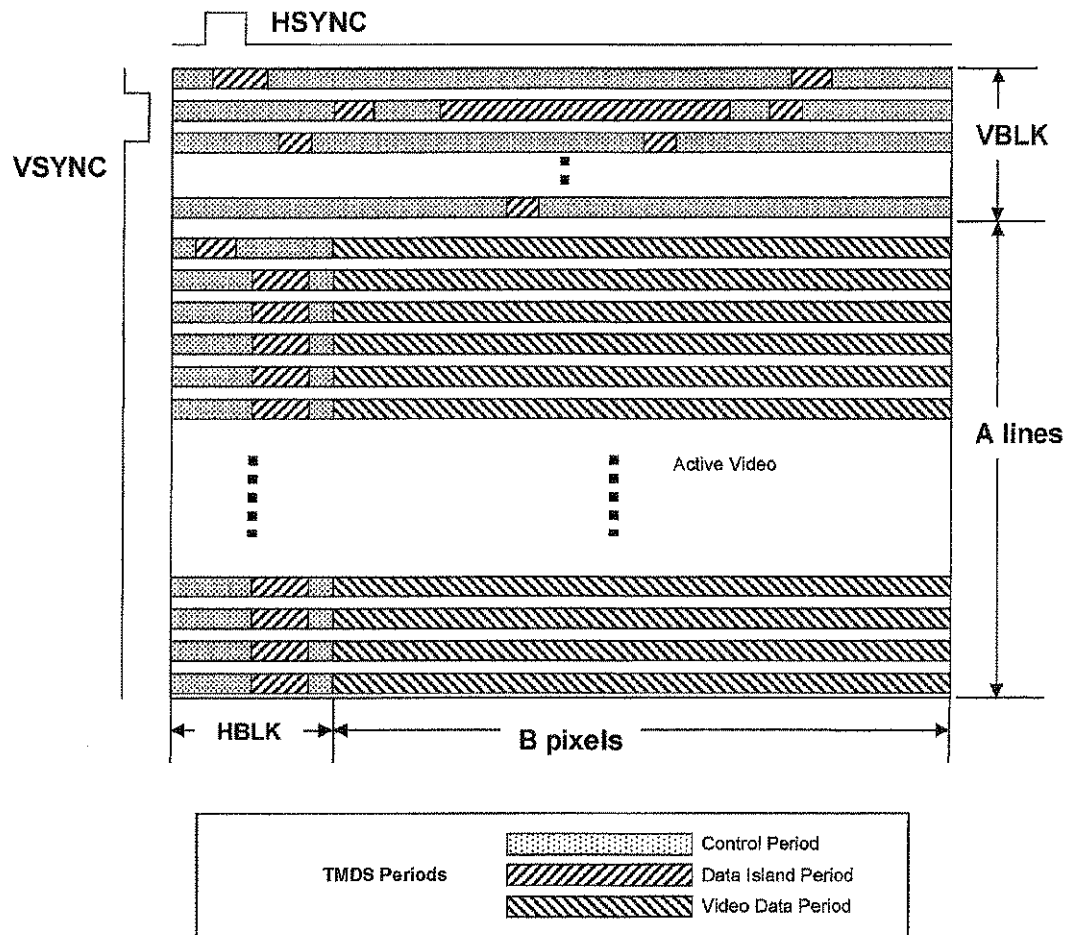
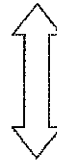


FIG. 6

(a) CURRENT HDMI PIN ASSIGNMENT
(Type-A)

PIN	Signal Assignment
1	TMDS Data#2+
2	TMDS Data#2 Shield
3	TMDS Data#2-
4	TMDS Data#1+
5	TMDS Data#1 Shield
6	TMDS Data#1-
7	TMDS Data#0+
8	TMDS Data#0 Shield
9	TMDS Data#0-
10	TMDS Clock+
11	TMDS Clock Shield
12	TMDS Clock-
13	CEC
14	Utility/HEAC+
15	SCL
16	SDA
17	DDC/CEC Ground /HEAC Shield
18	+5V Power
19	Hot Plug Detect/HEAC-

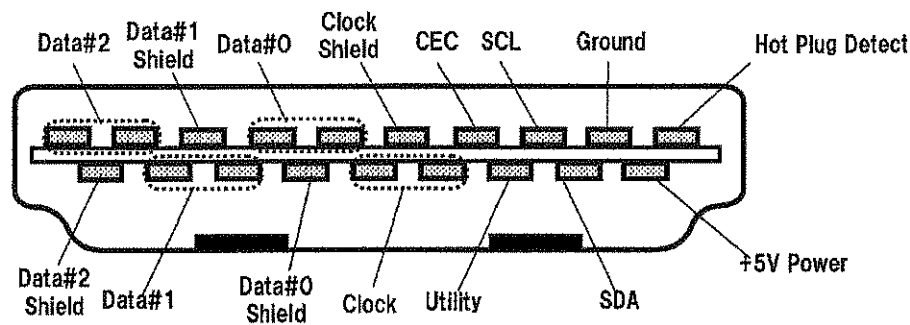


(b) NEW HDMI PIN ASSIGNMENT

PIN	Signal Assignment
1	TMDS Data#0+
2	TMDS Data#4 +
3	TMDS Data#0 -
4	TMDS Data#1 +
5	TMDS Data#4 -
6	TMDS Data#1 -
7	TMDS Data#2 +
8	TMDS Data#5 +
9	TMDS Data#2 -
10	TMDS Data#3 +
11	TMDS Data#5 -
12	TMDS Data#3 -
13	CEC
14	Utility/HEAC+
15	SCL
16	SDA
17	DDC/CEC Ground /HEAC Shield
18	+5V Power
19	Hot Plug Detect/HEAC-

FIG. 7

(a) CURRENT HDMI PIN LAYOUT



(b) NEW HDMI PIN LAYOUT

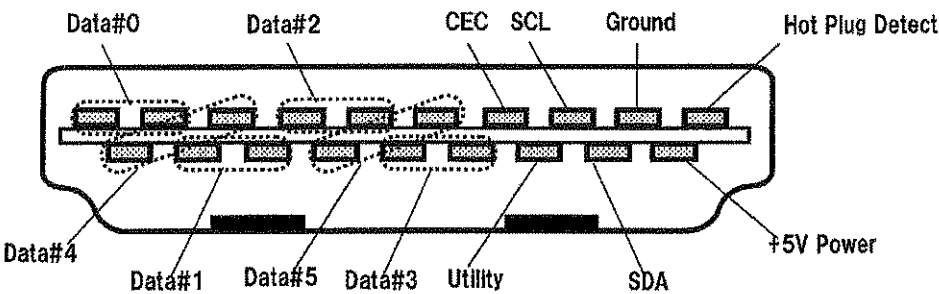
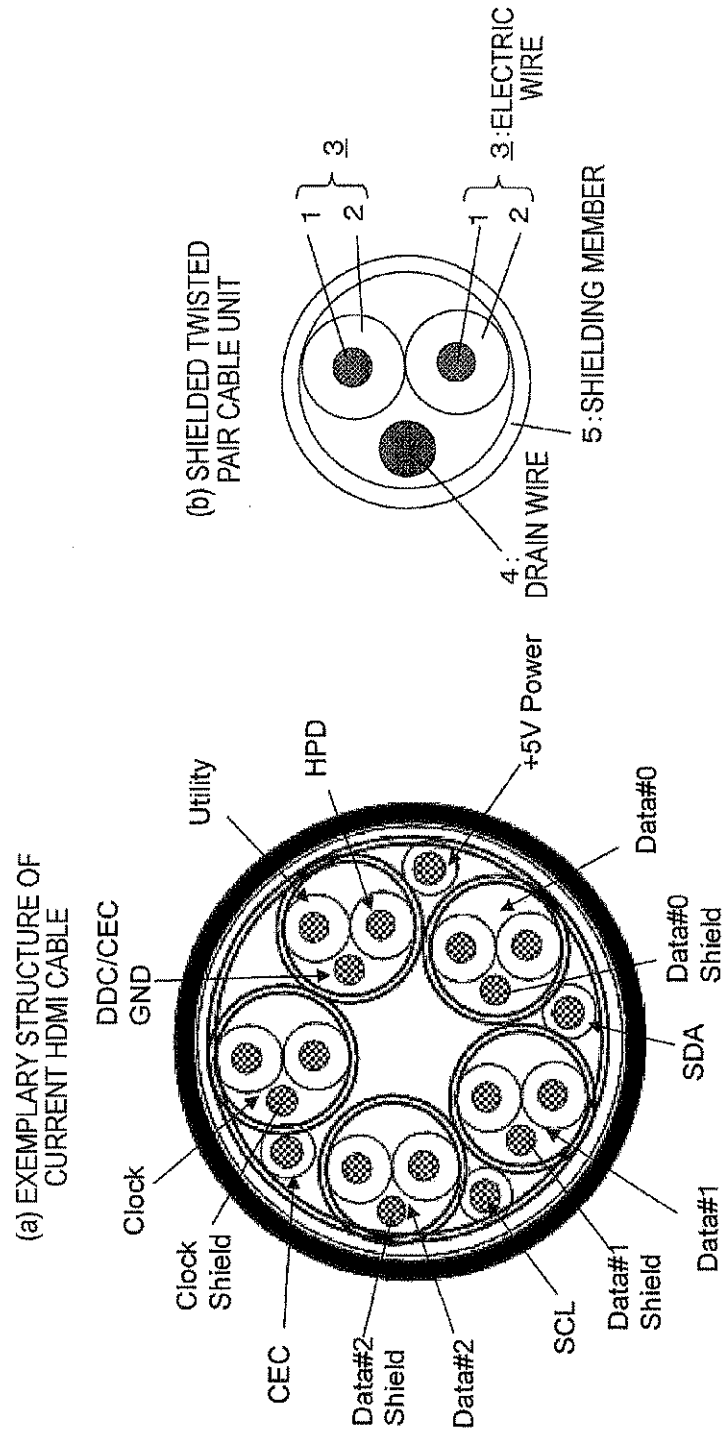


FIG. 8



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FIG. 9

EXEMPLARY STRUCTURE OF
NEW HDMI CABLE

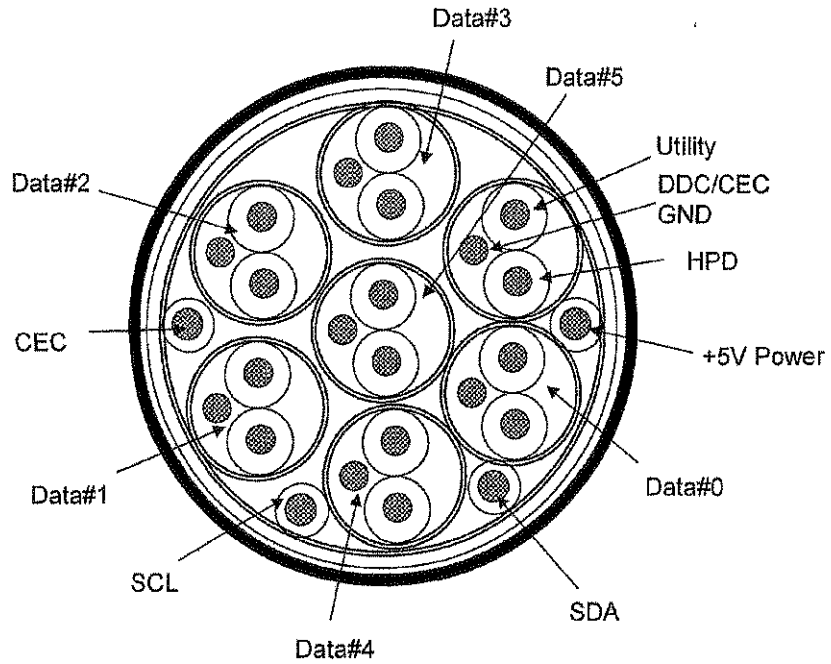
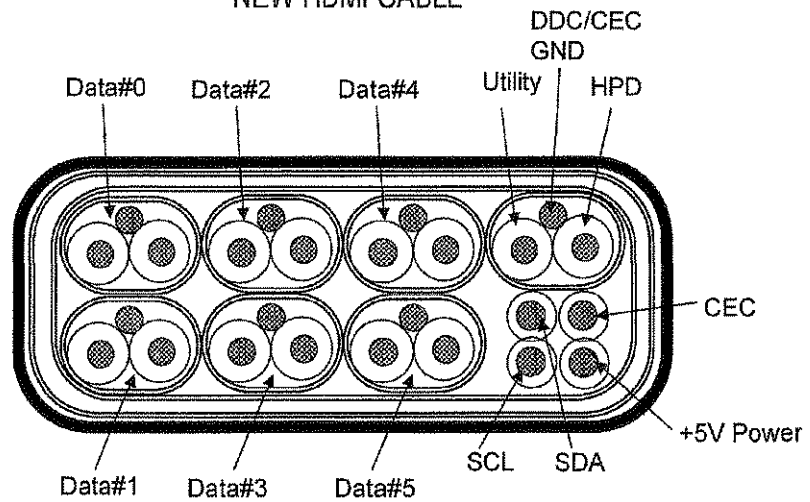


FIG. 10

EXEMPLARY STRUCTURE OF
NEW HDMI CABLE



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FIG. 11

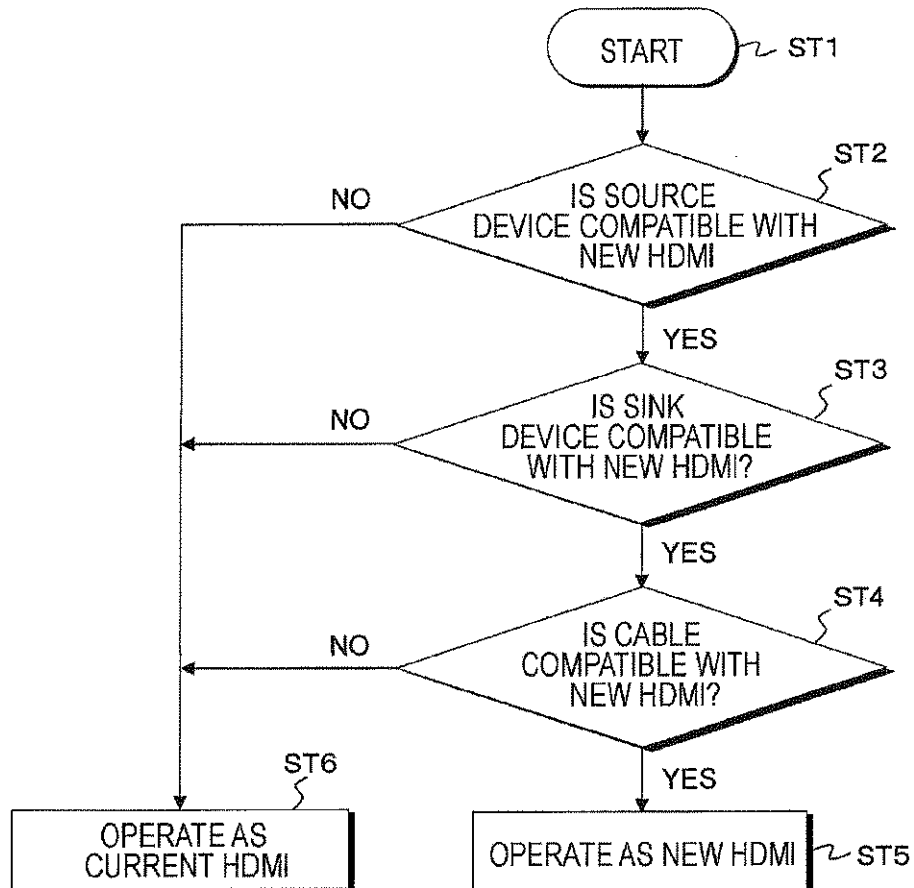
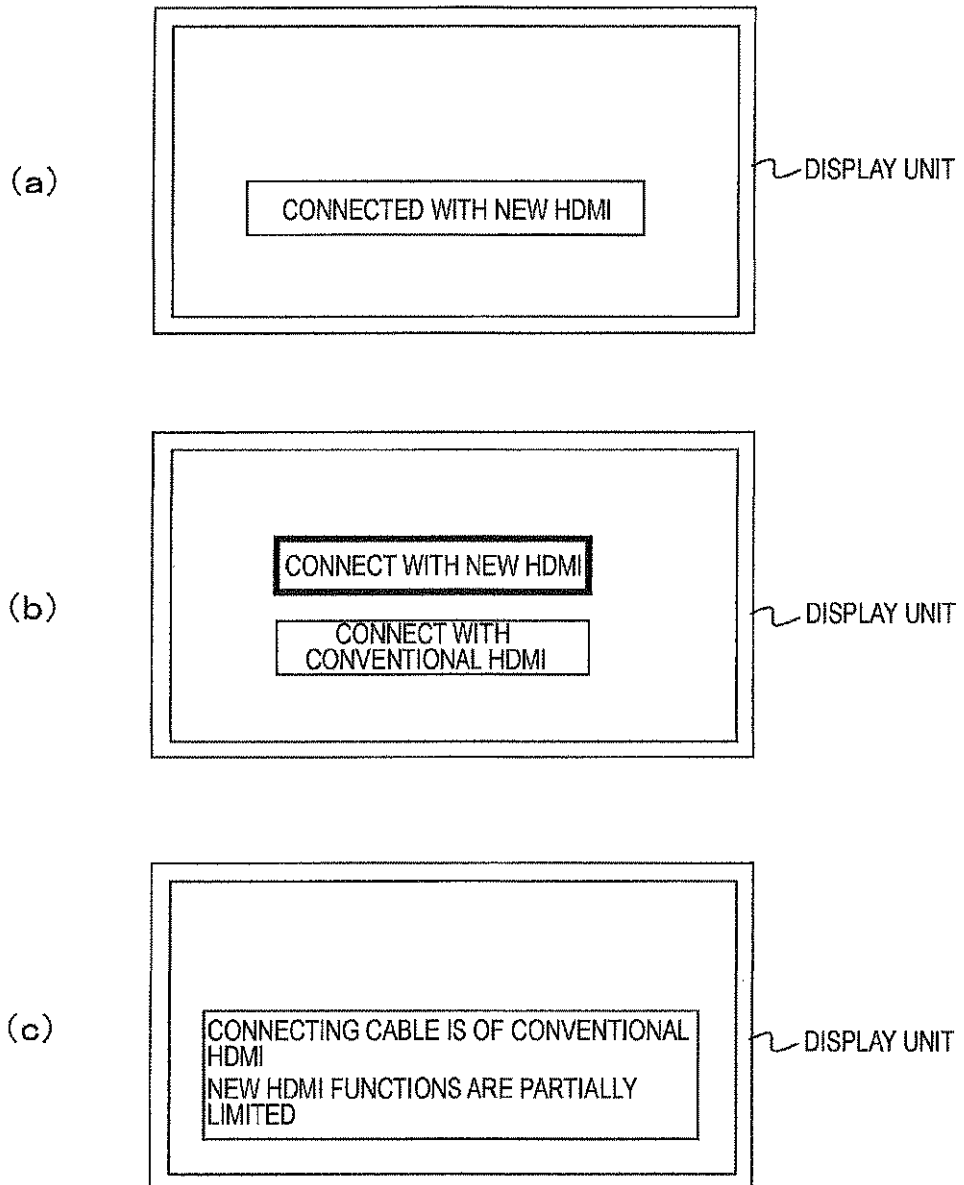


FIG. 12



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FIG. 13

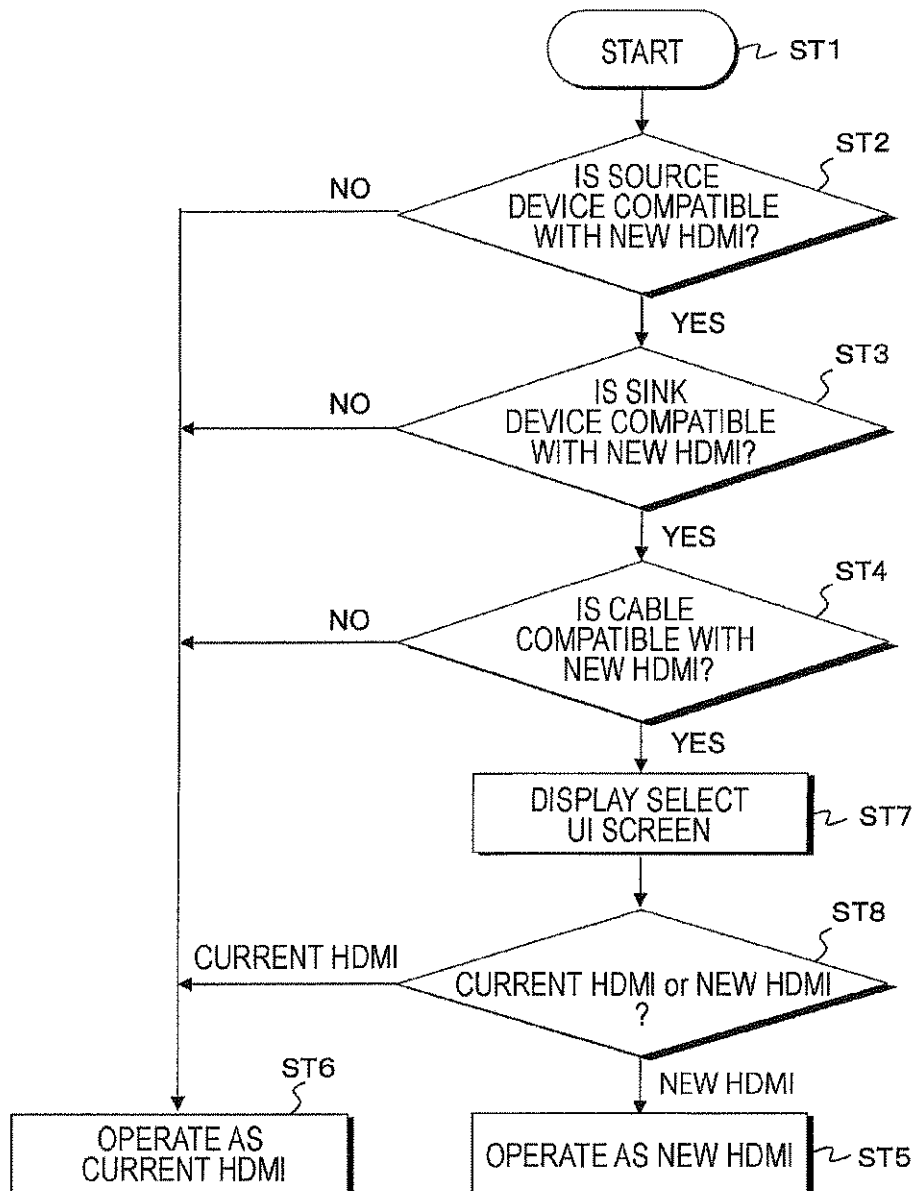


FIG. 14

EXEMPLARY EDID

Byte #	7	6	5	4	3	2	1	0
	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)	Rsvd (0)	New Rx Sink	New Cable	Rsvd (0)

FIG. 15

EXEMPLARY NEW HDMI CABLE

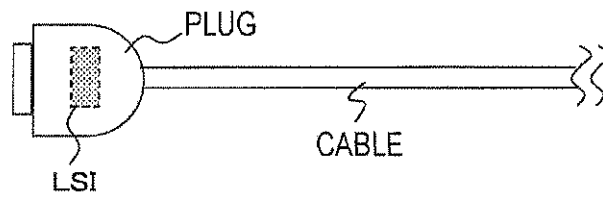


FIG. 16

EXAMPLE OF EDID DATA REWRITE
CIRCUIT IN LSI IN CABLE

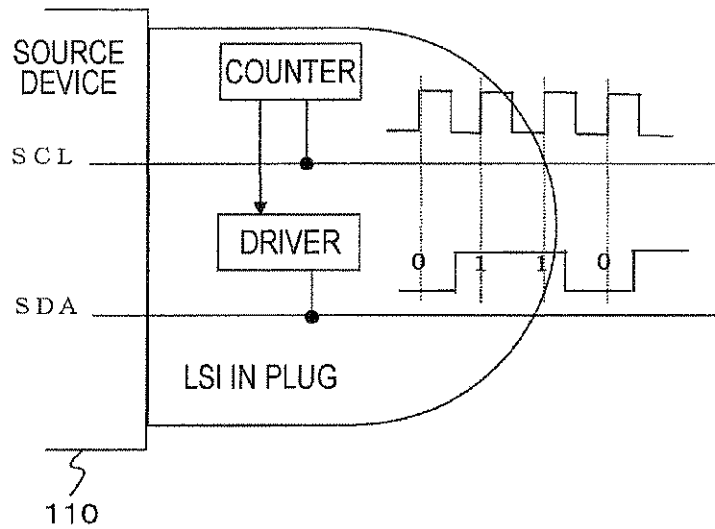


FIG. 17

EXAMPLE OF CABLE INFORMATION
NOTIFICATION CIRCUIT USING RF TAG CHIP

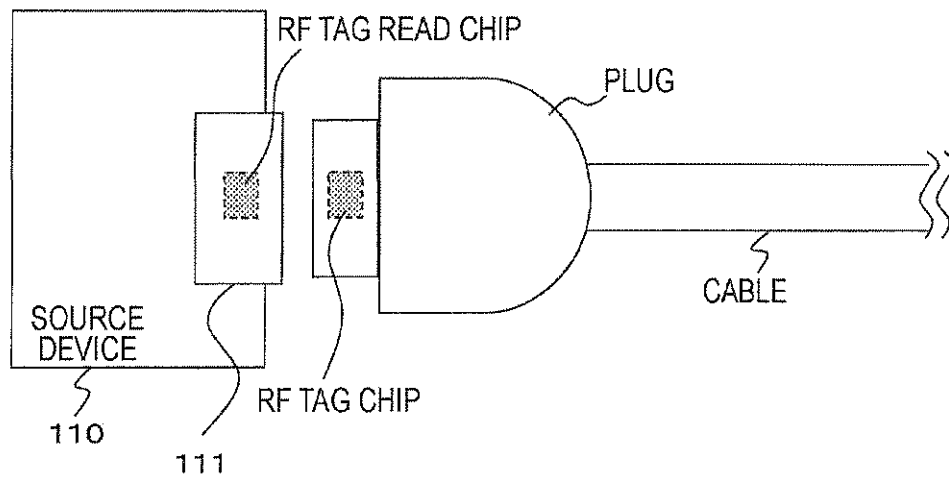


FIG. 18

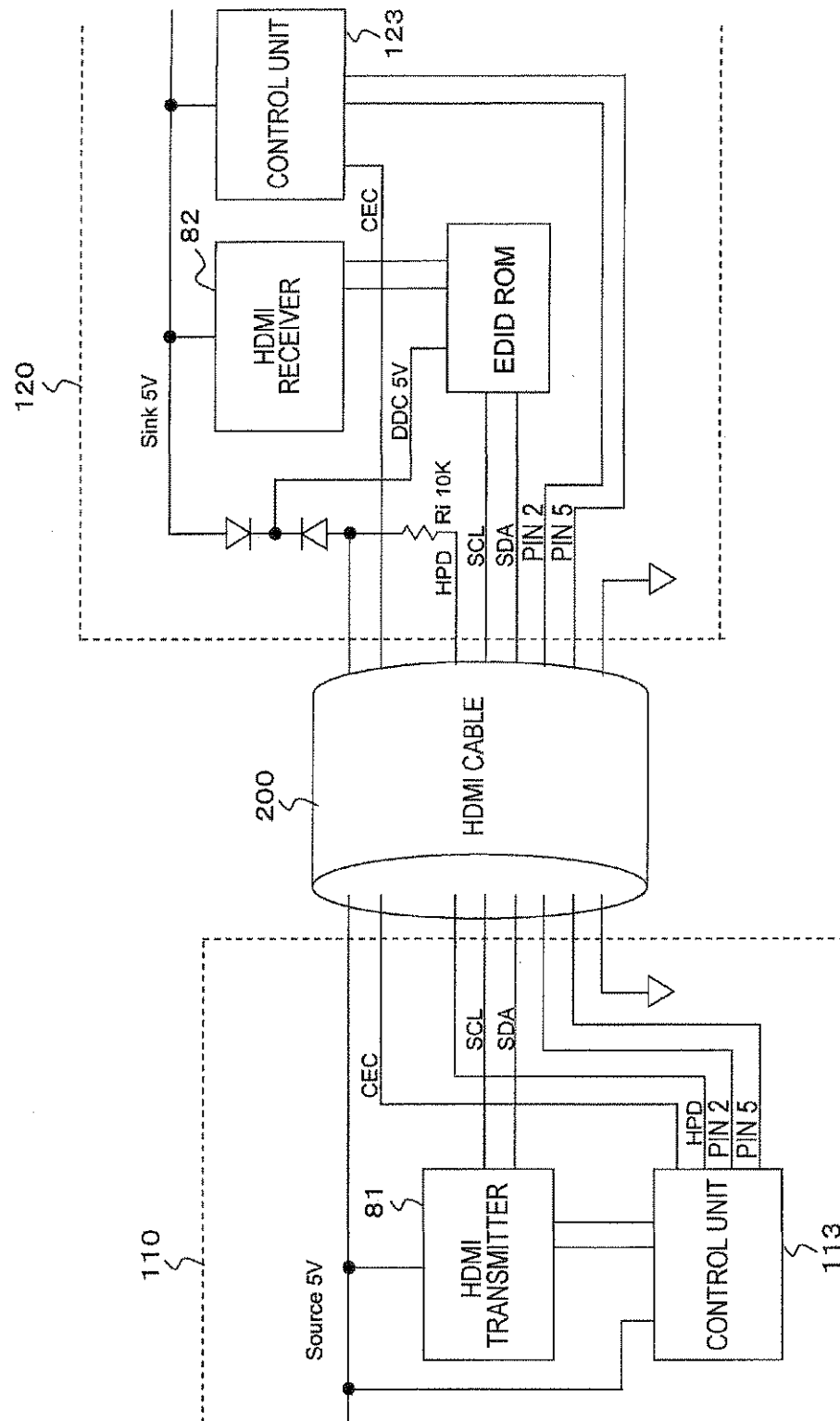
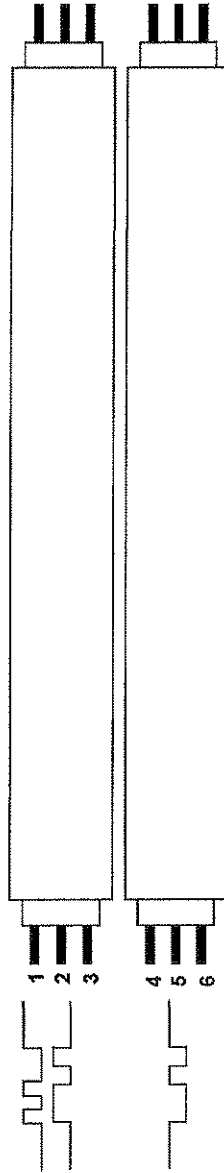


FIG. 19

(a) CURRENT HDMI CABLE



(b) NEW HDMI CABLE

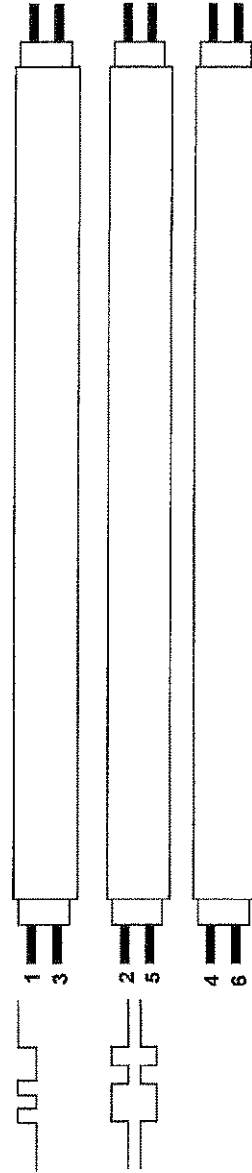
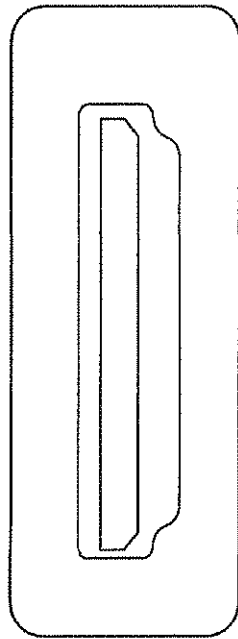
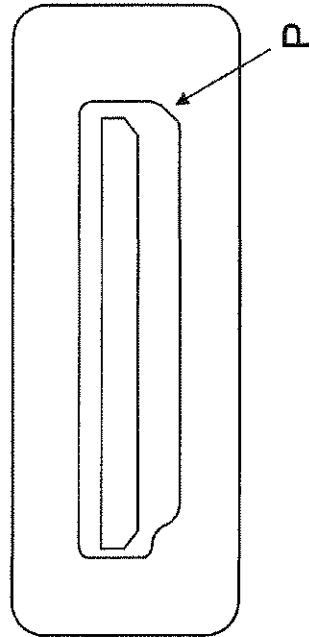


FIG. 20

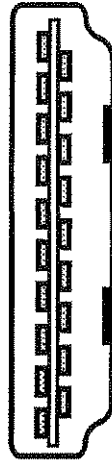
(a) CURRENT HDMI PLUG



(c) NEW HDMI PLUG



(b) CURRENT HDMI RECEPTACLE



(d) NEW HDMI RECEPTACLE

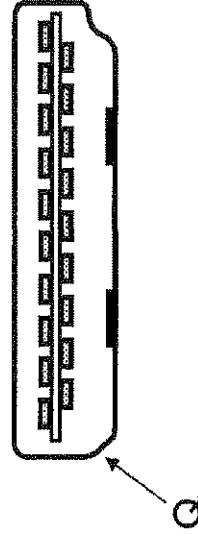
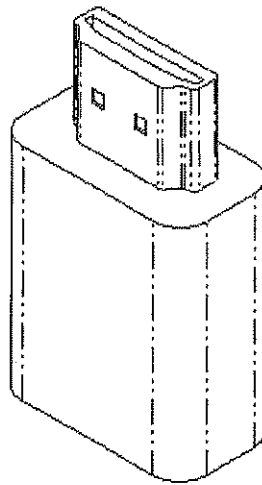


FIG. 21

(a) CONVENTIONAL
HDMI PLUG



(b) NEW HDMI PLUG

