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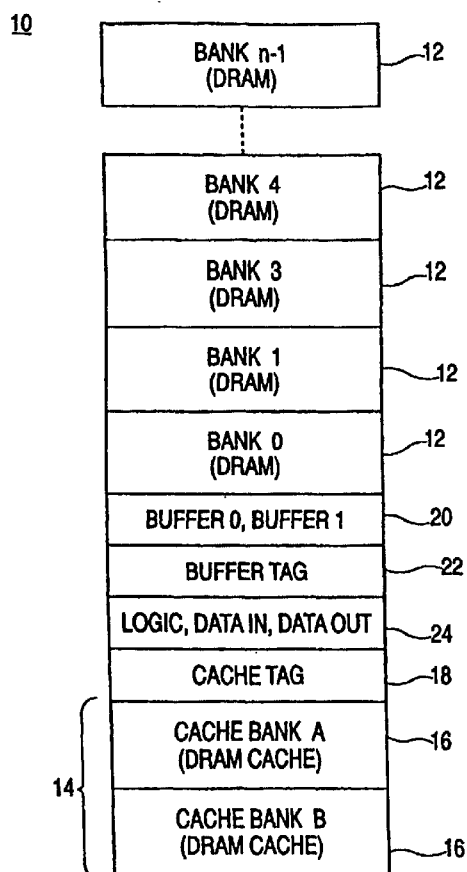
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(54) Title: DESTRUCTIVE-READ RANDOM ACCESS MEMORY SYSTEM BUFFERED WITH DESTRUCTIVE-READ MEMORY CACHE



(57) Abstract: A memory storage system (10) is disclosed. In an exemplary embodiment, the memory storage system includes a plurality of memory storage banks (12) and a cache (14) in communication therewith. Both the plurality of memory storage banks (12) and the cache (14) further include destructive read memory storage elements.



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