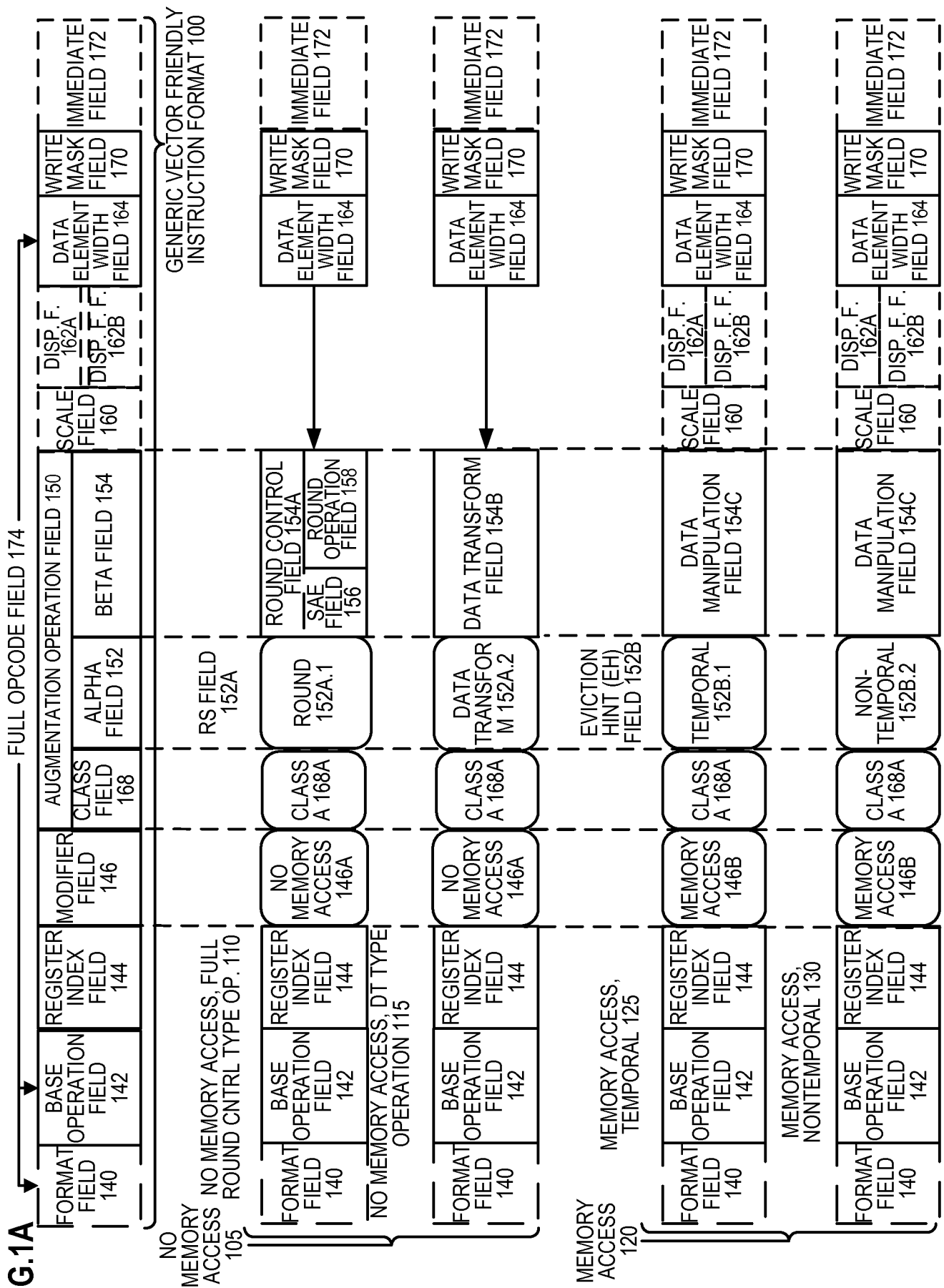


FIG.1A



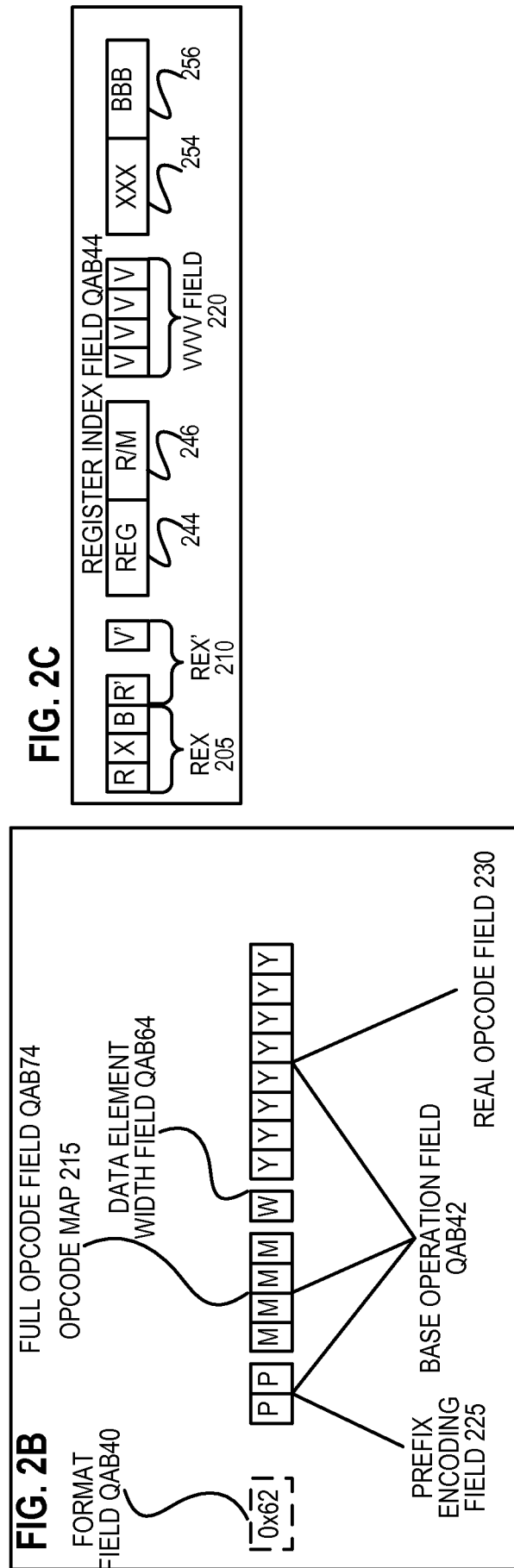
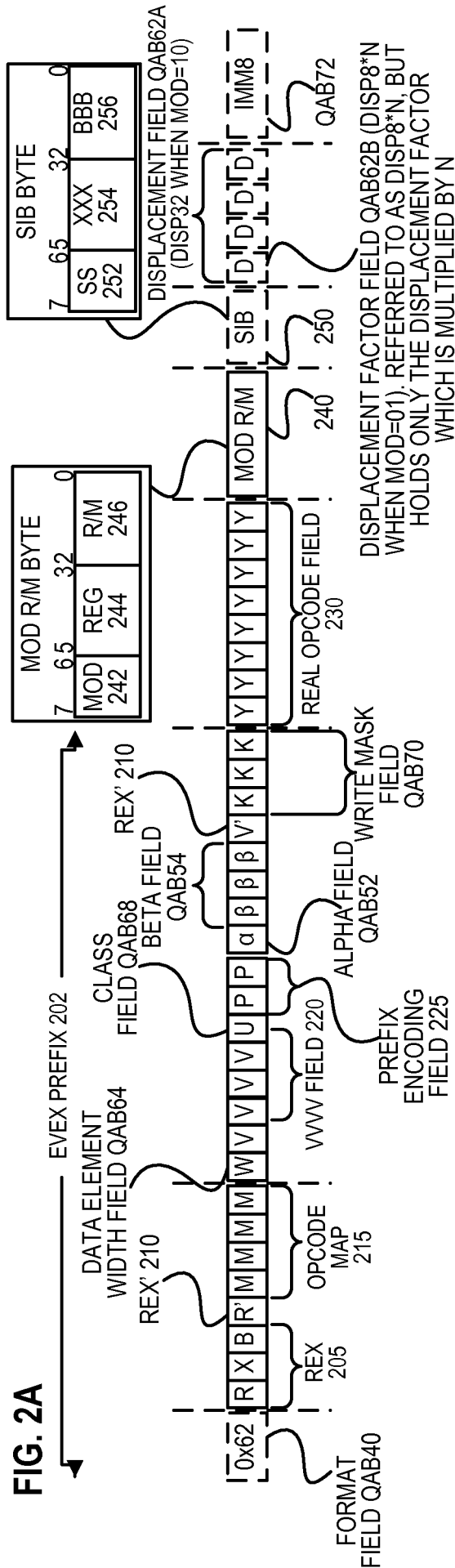


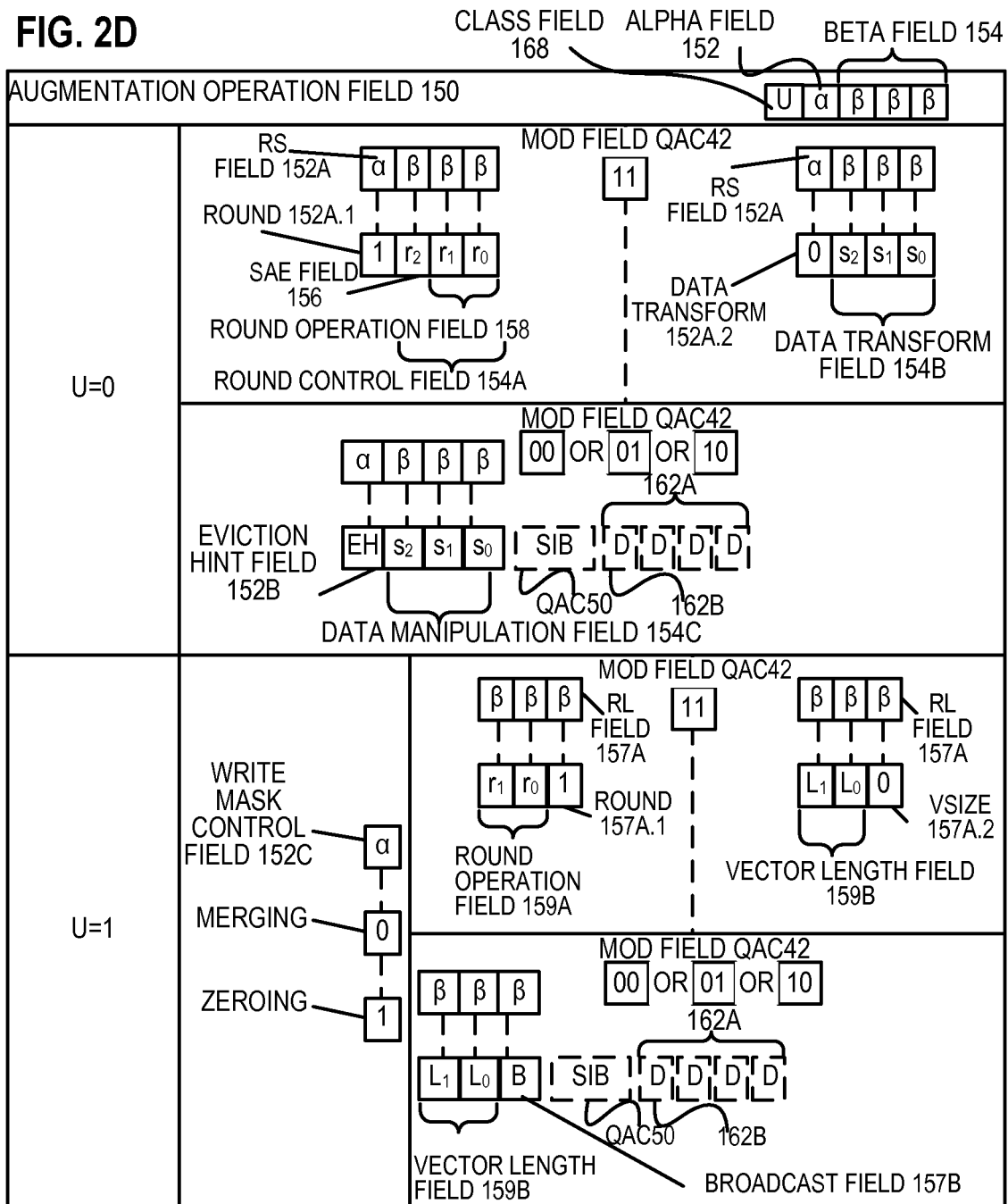
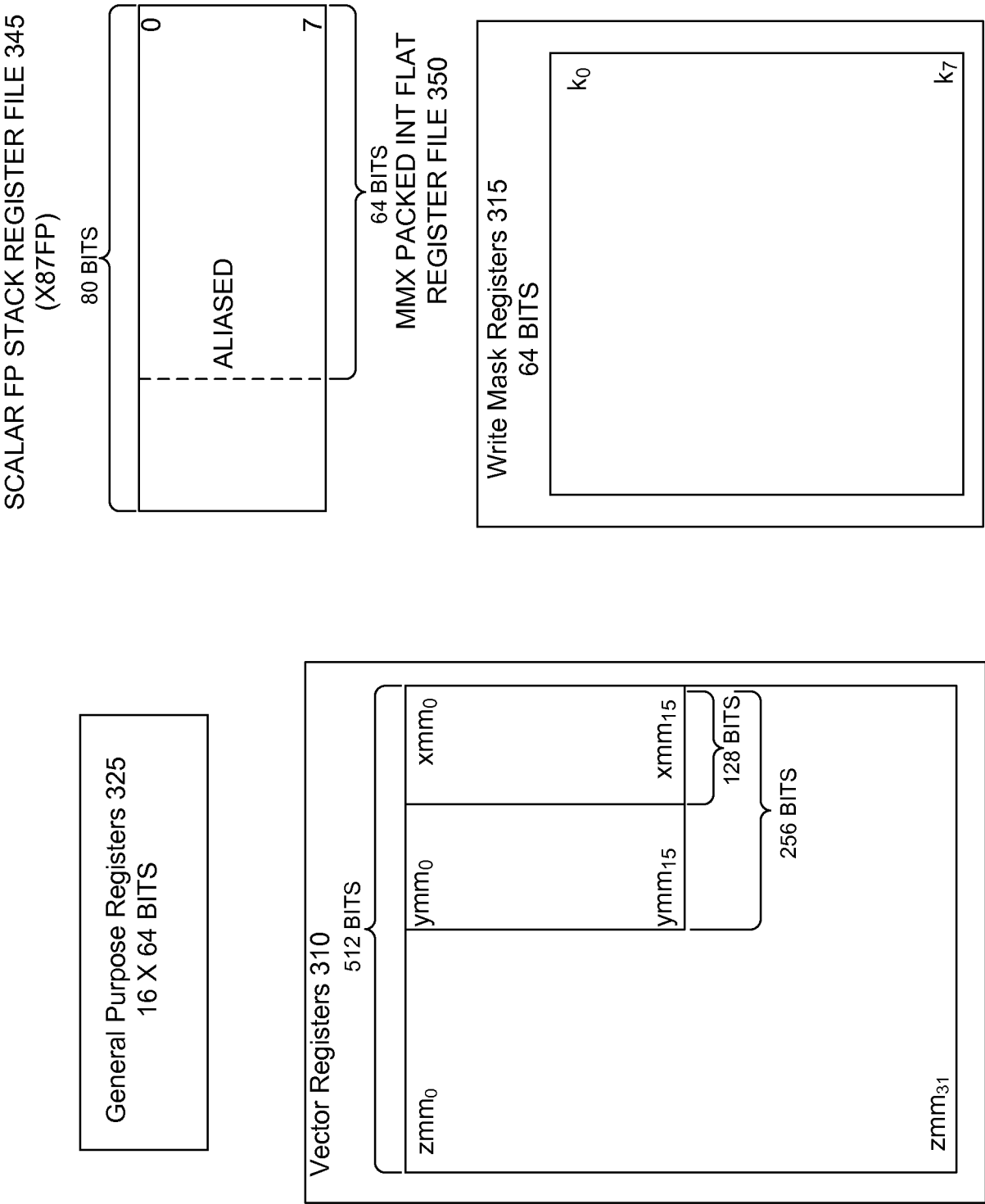
FIG. 2D

FIG. 3

REGISTER ARCHITECTURE 300



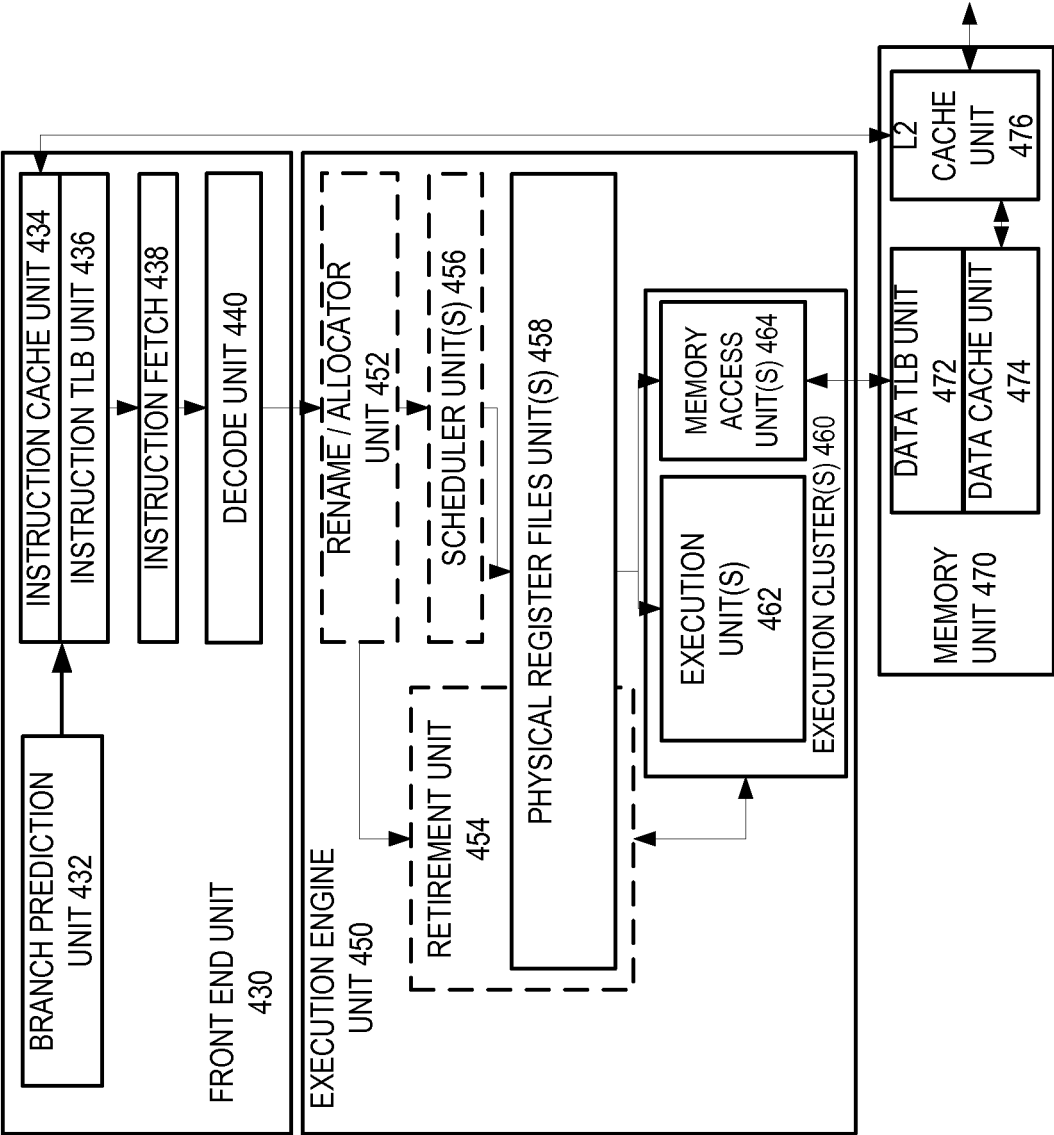
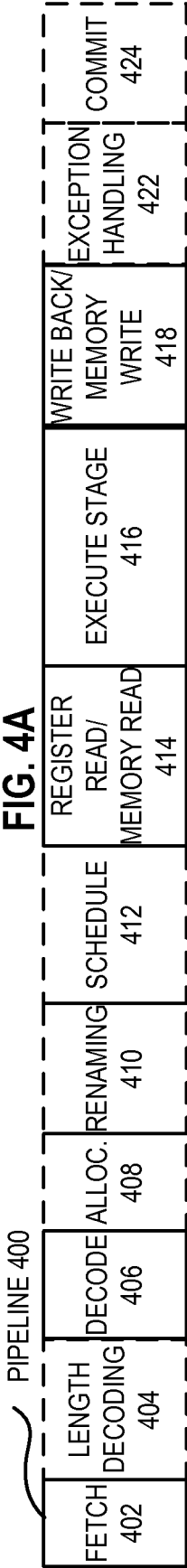


FIG. 5A

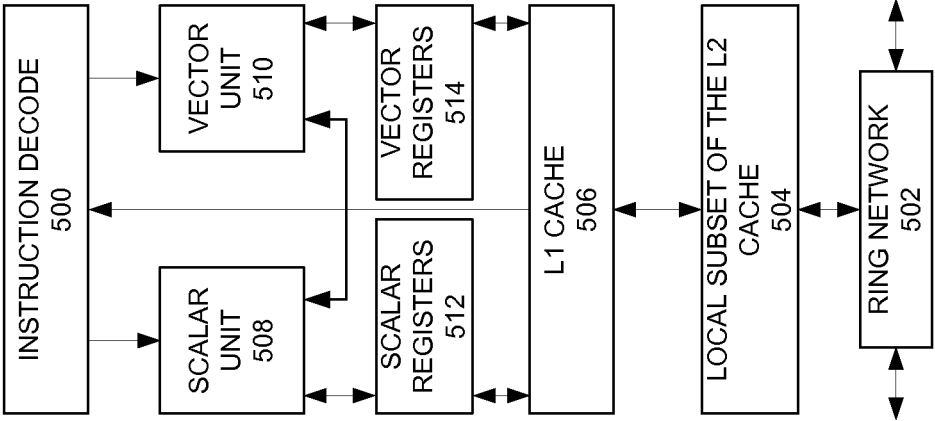
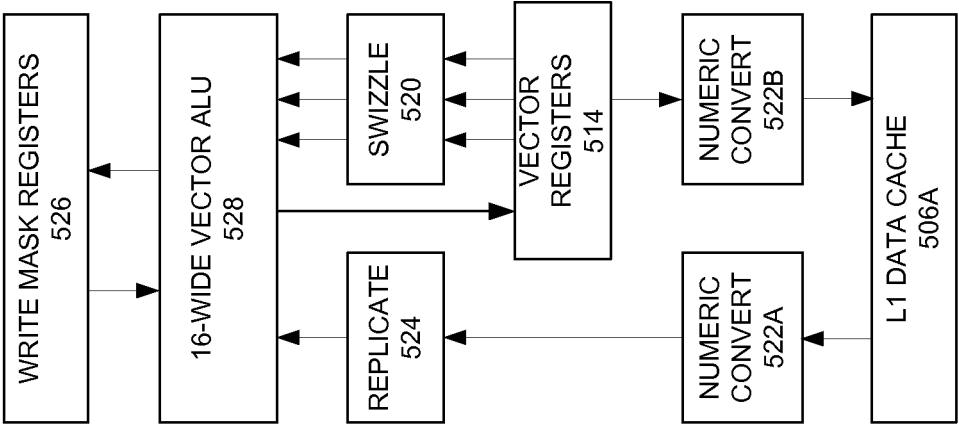
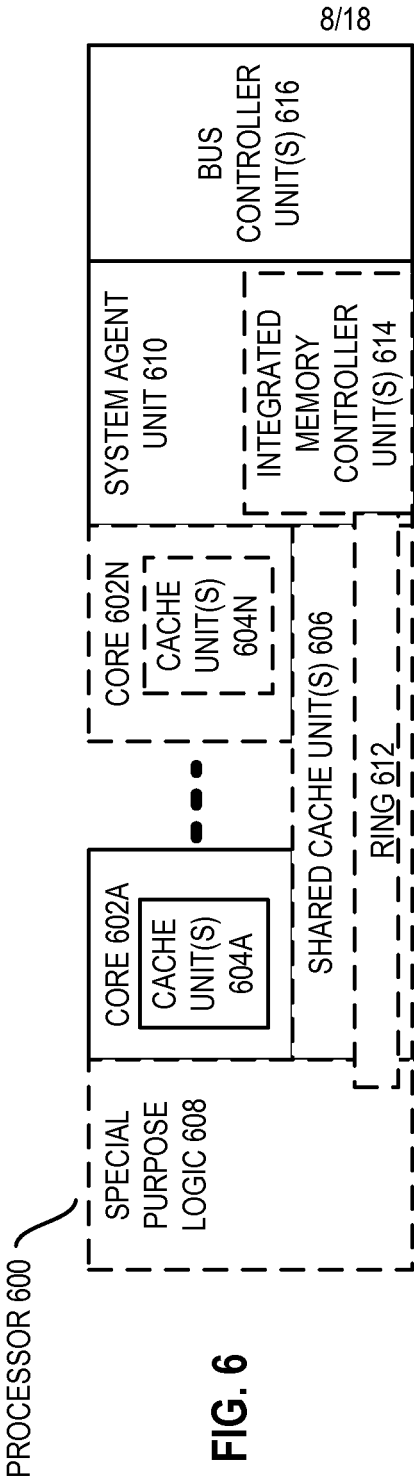


FIG. 5B





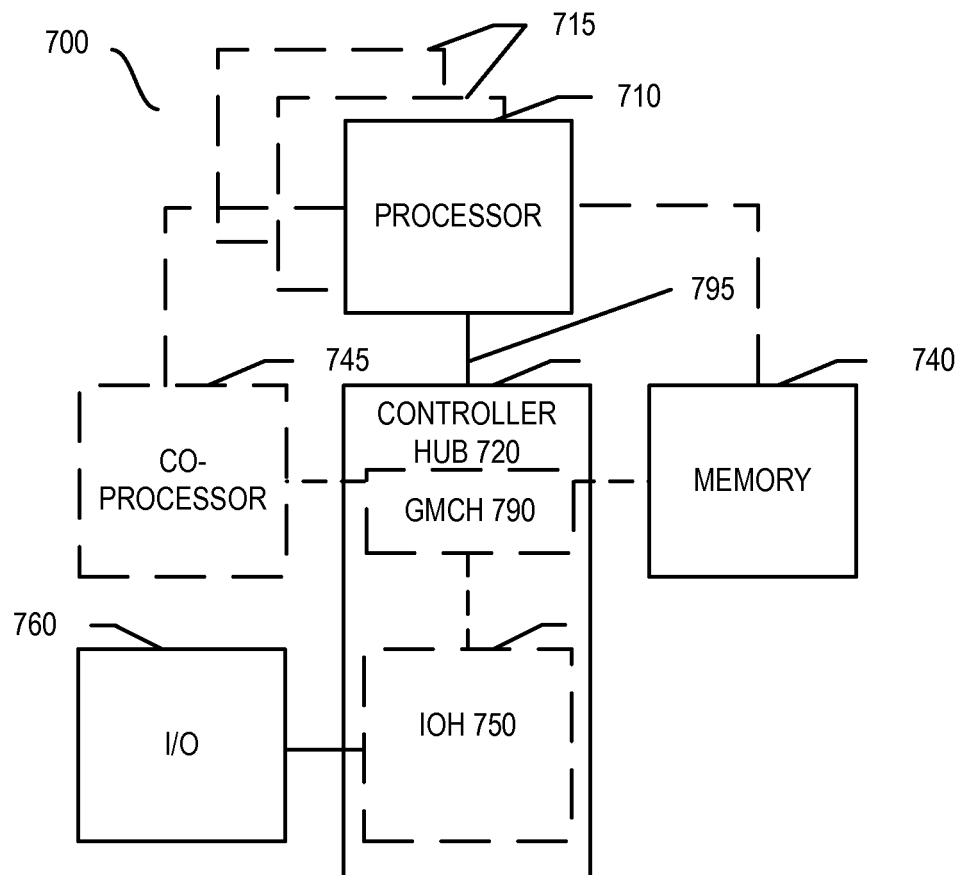


FIG. 7

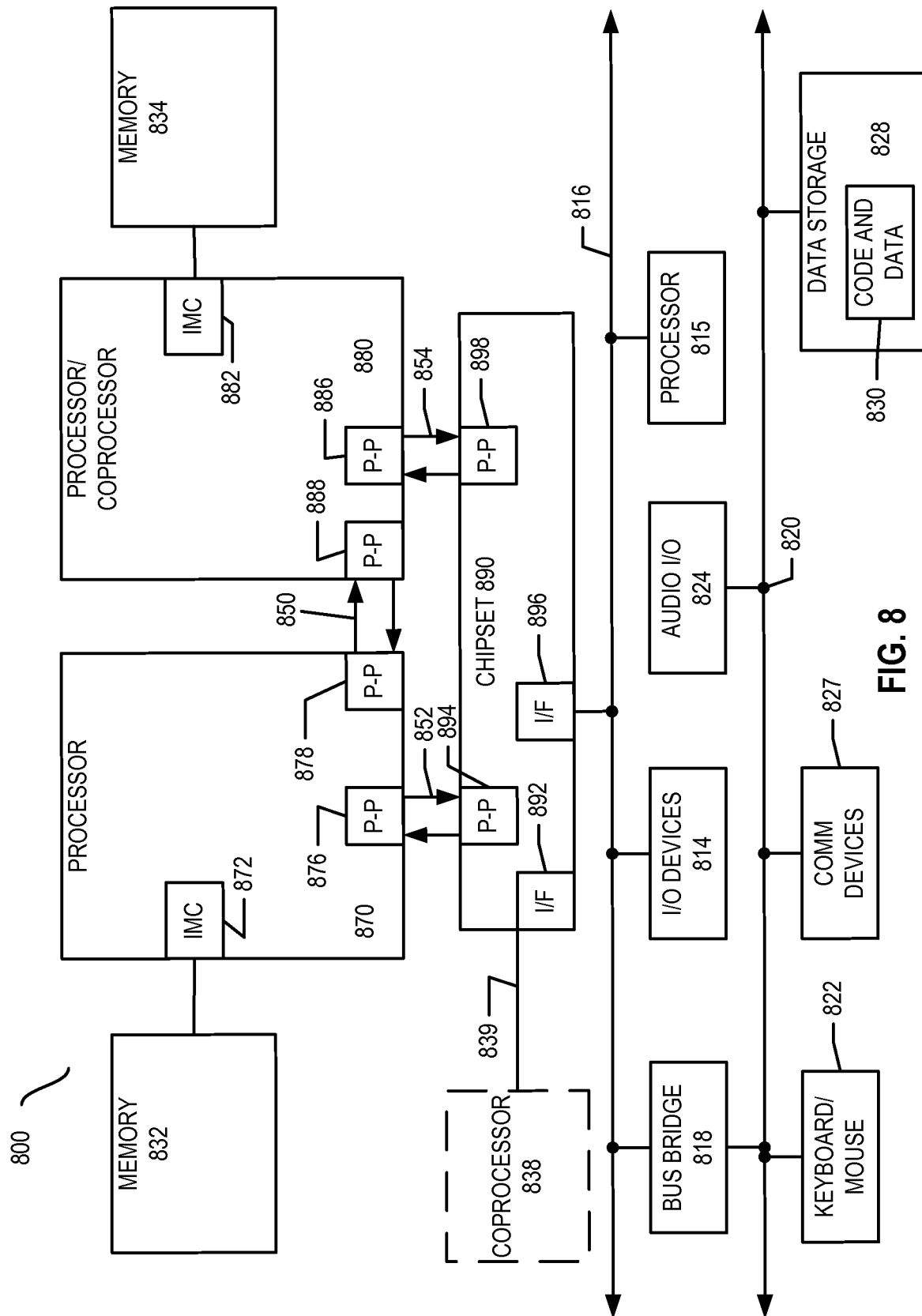


FIG. 8

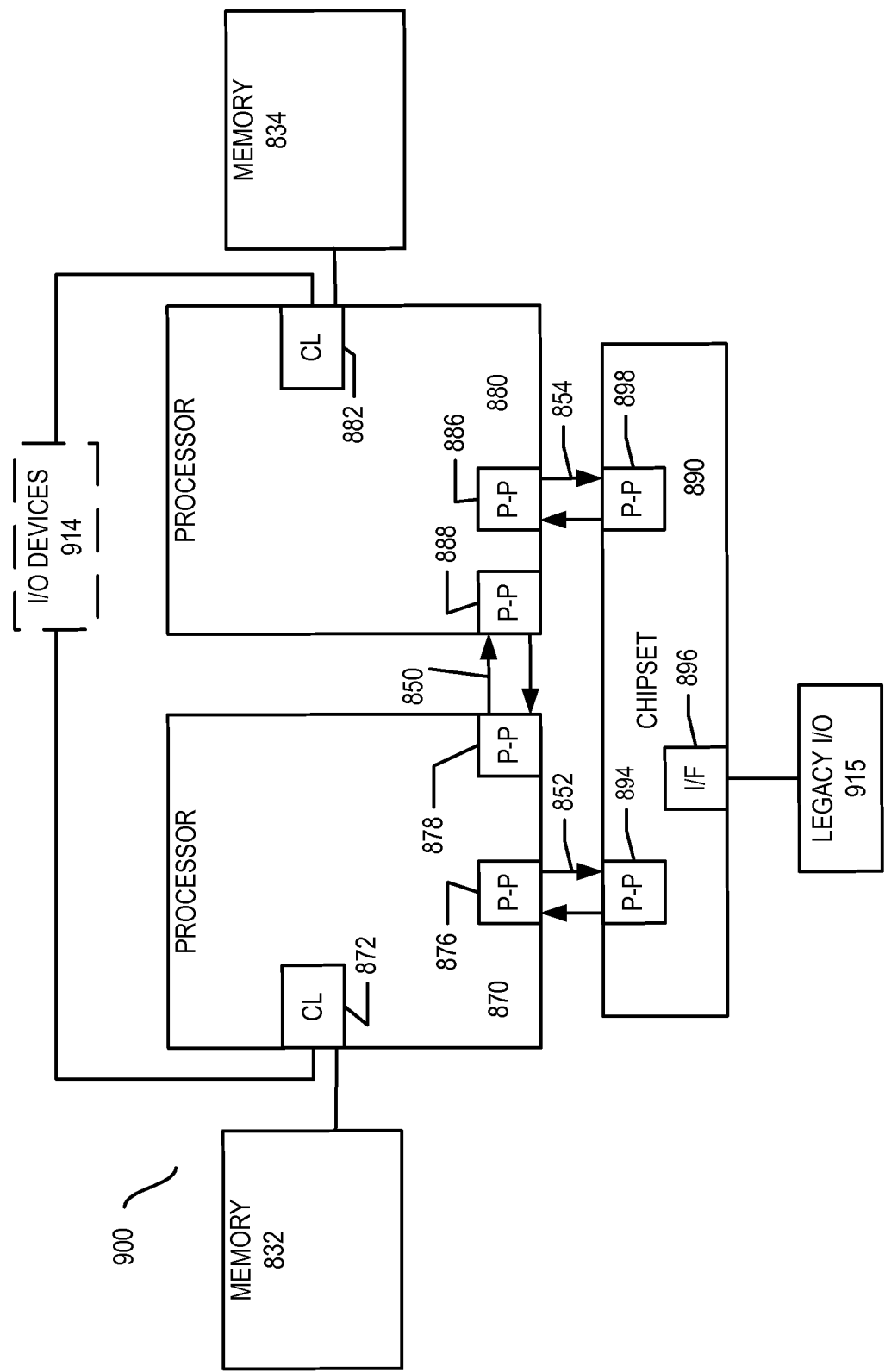


FIG. 9

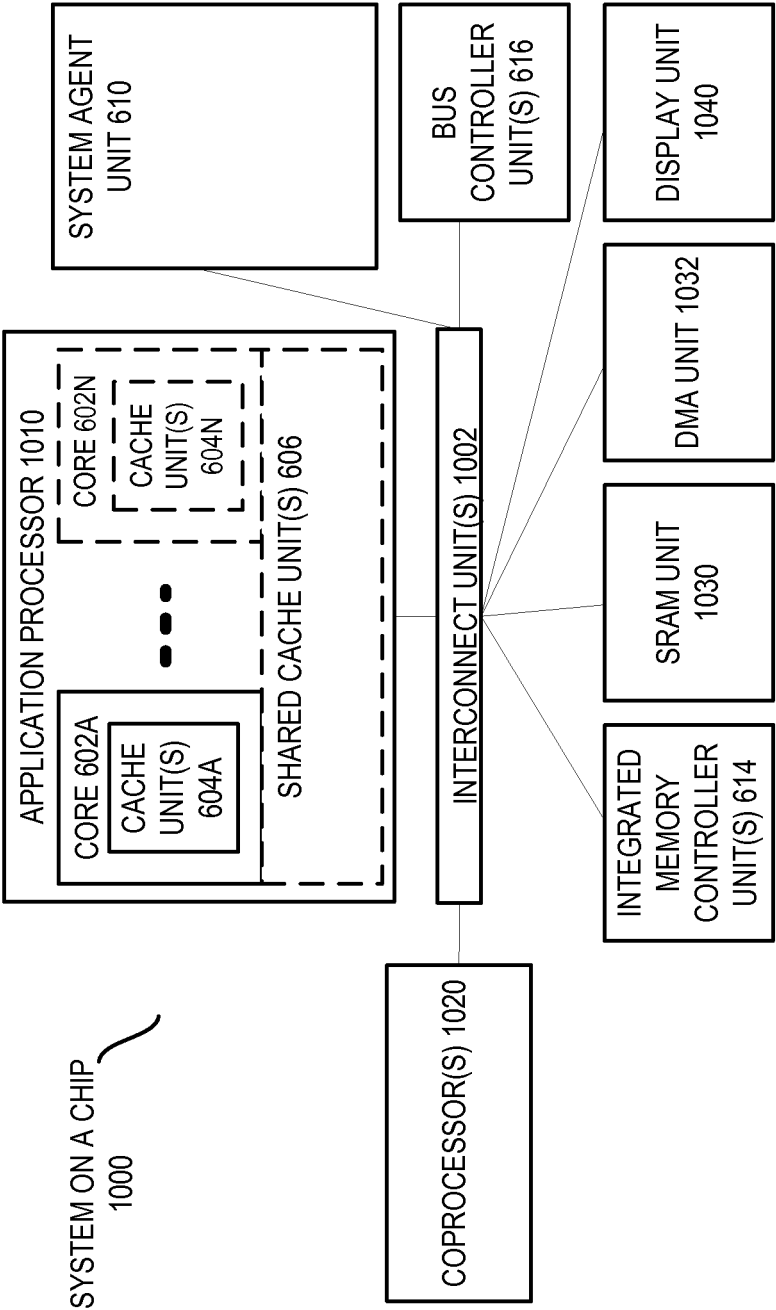
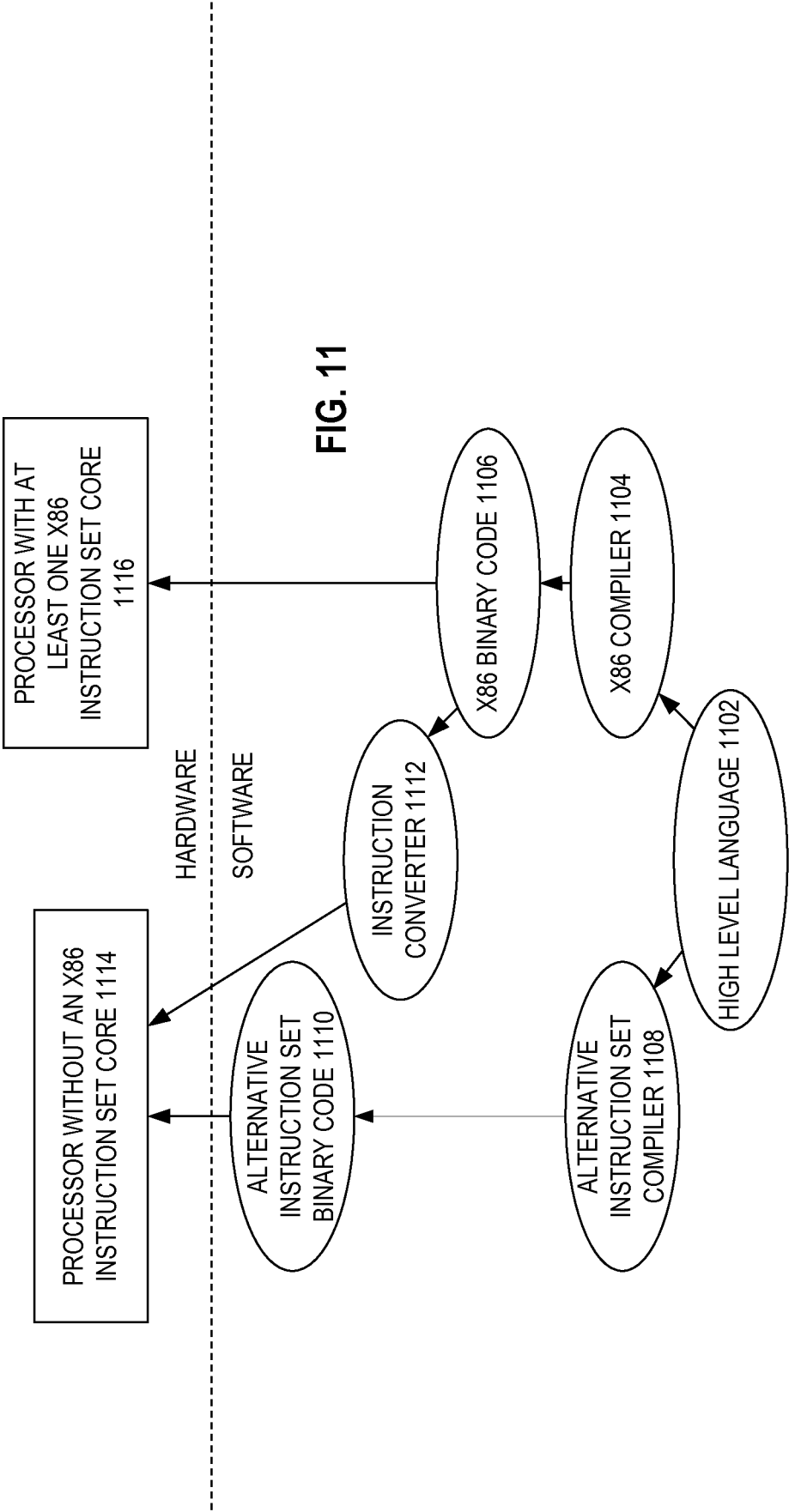


FIG. 10



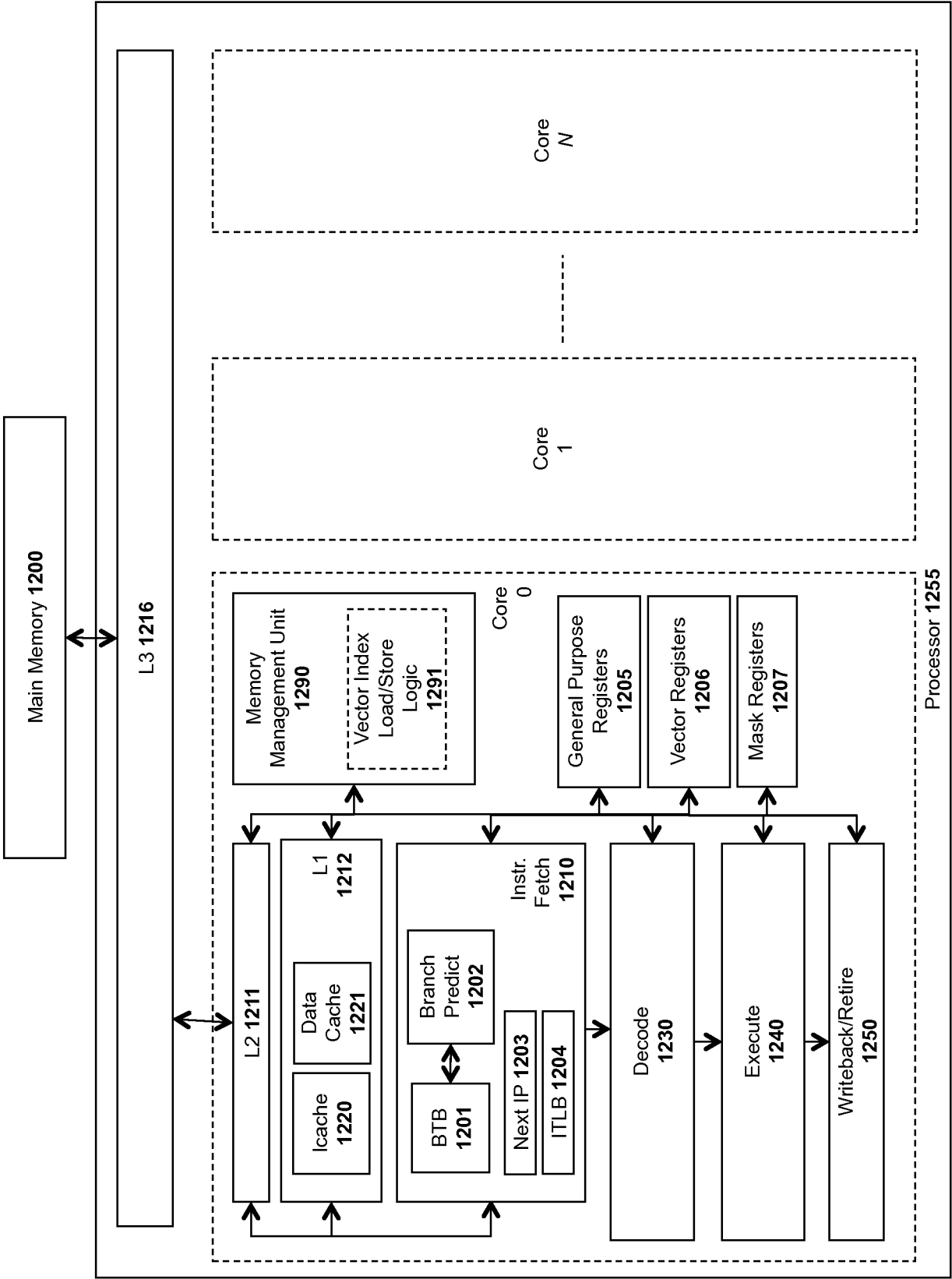


Fig. 12

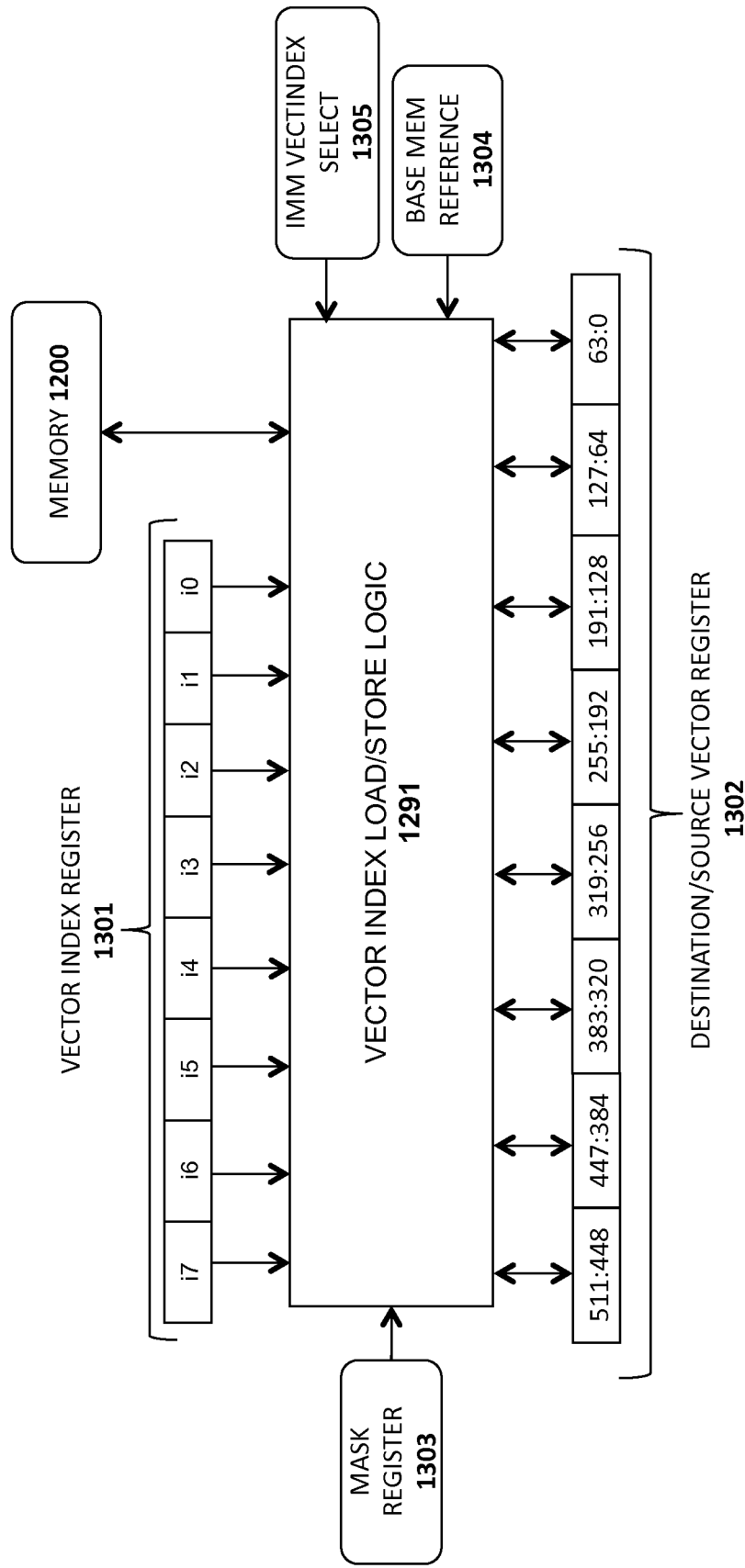


Fig. 13

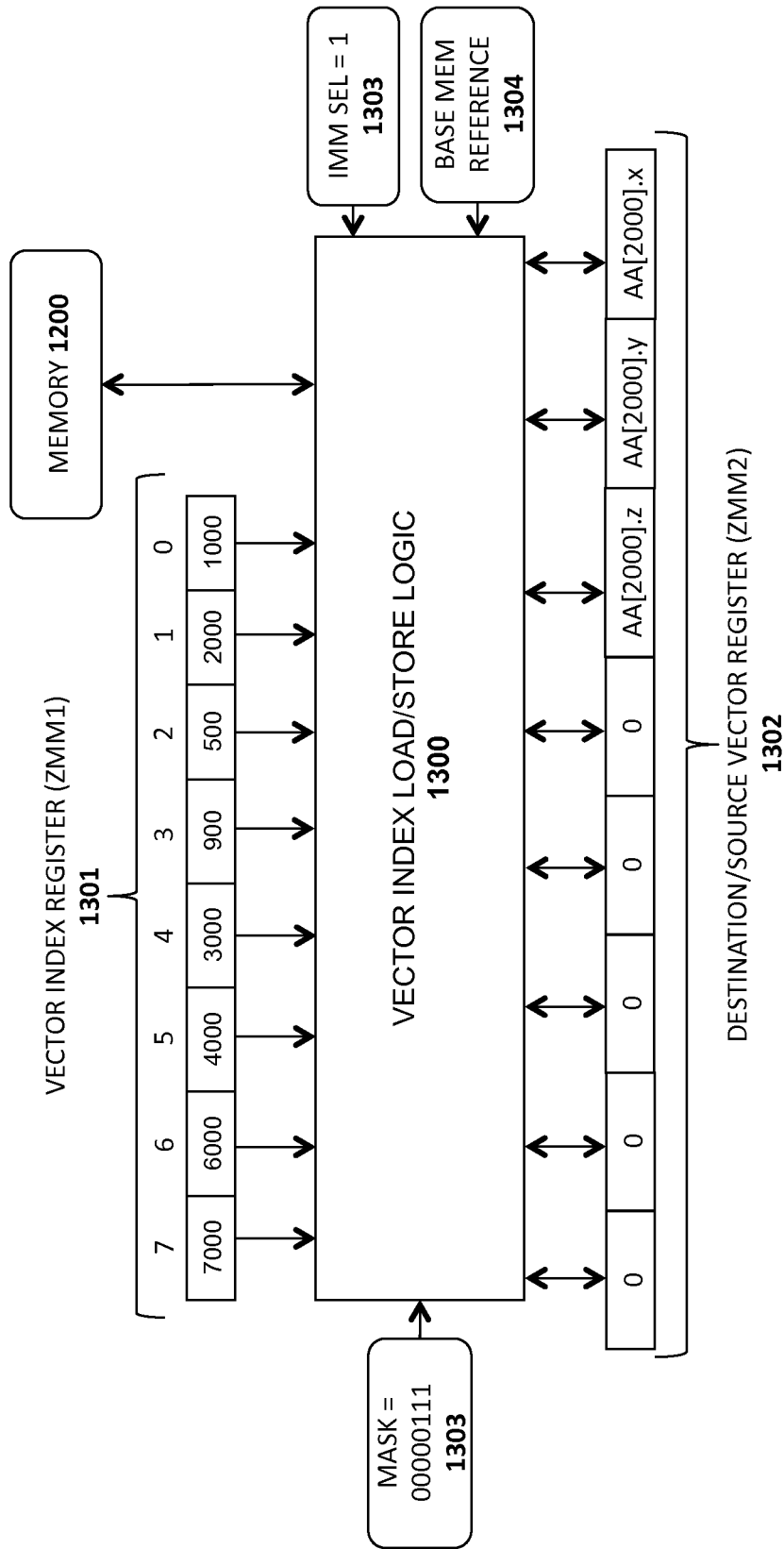
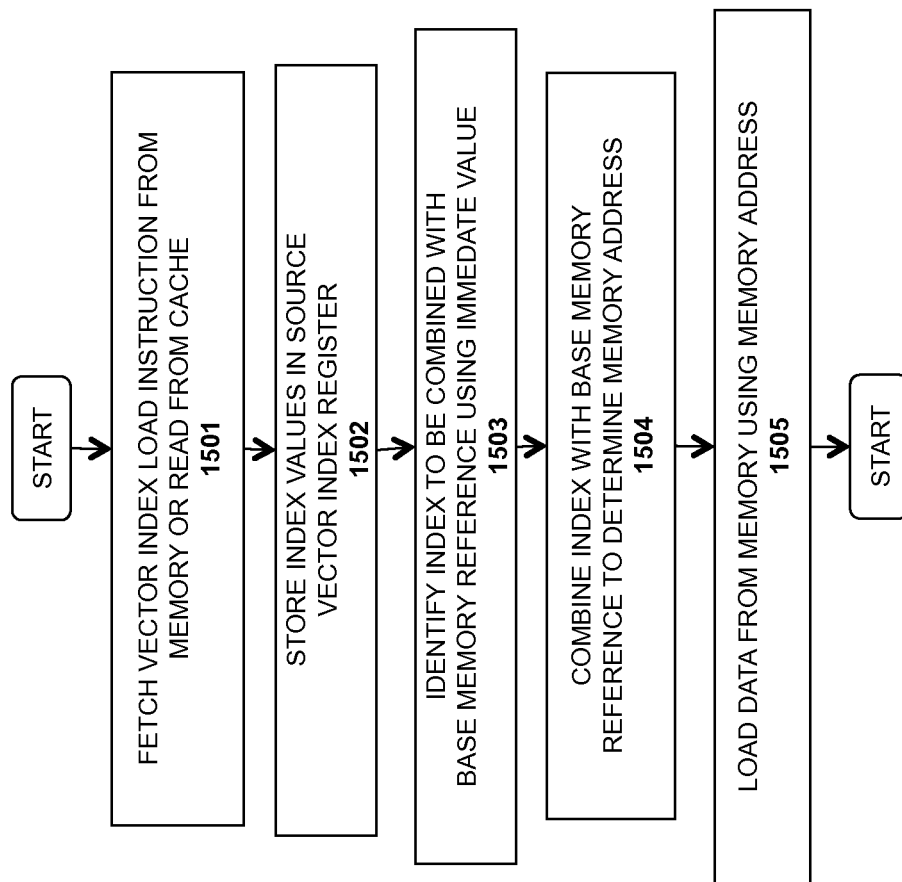
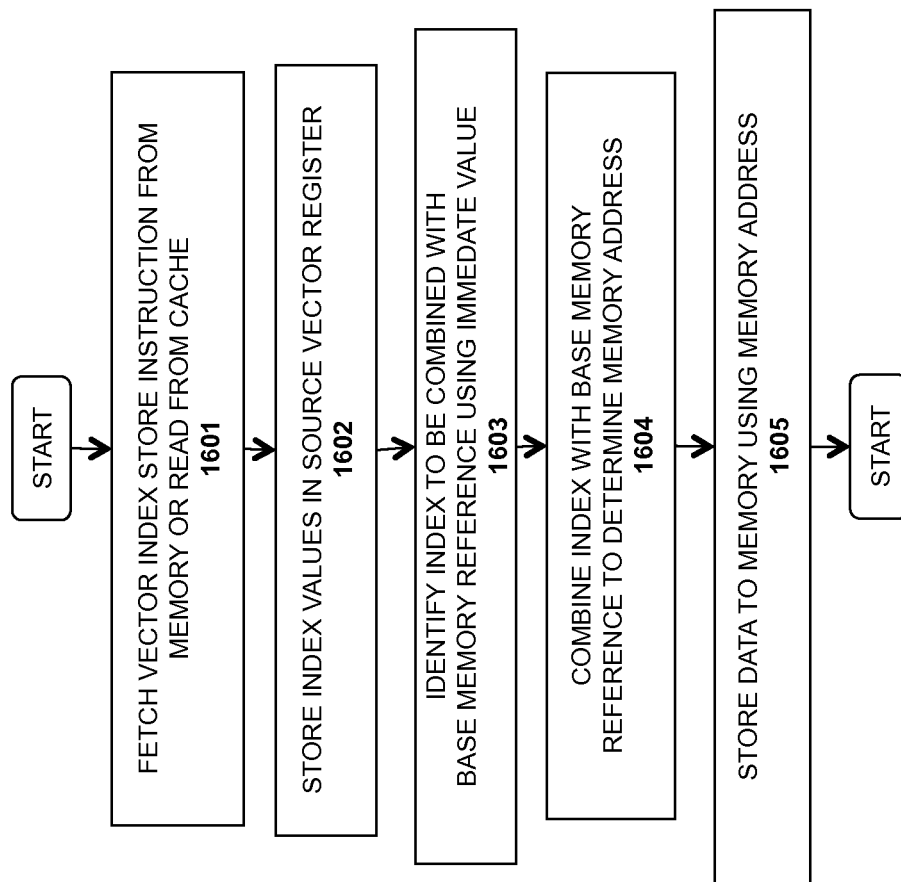


Fig. 14

**Fig. 15**

**Fig. 16**