

ABSTRACT

A received signal vector ($X(t)$) from an array antenna (#1 to #4) is subjected to frequency offset correction by a frequency offset correction part (6) and thereafter subjected to adaptive array processing. An error signal ($e(t)$) is referred to on the basis of an output signal ($y(t)$) from the adaptive array and a reference signal ($d(t)$) held in a memory (30) in a reference signal section or a replica signal ($d'(t)$) output from a forced phase-locking processing part (20) in a section having no reference signal, so that a frequency offset quantity ($\Delta\theta$) is extracted therefrom by an offset extraction part (100).



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