

ABSTRACT

DELAY LOCKED LOOP FOR USE IN A GPS SIGNAL RECEIVER

Delay locked loop intended to be used in a receiver of signals emitted by a GPS satellite, comprising a pseudo-random code generator (35), a local oscillator (34), a modulator (14) for modulating said pseudo-random code with the output signal of said local oscillator (34), a plurality of signal channels (C1, C2, C3) connected to the output of said modulator (14) and each comprising a pass-band filter (48 to 50).

A data processing unit (11) is adapted to control the central frequency of the output signal of said local oscillator as a function of the output signal of said signal detection circuit and to control the central frequencies and the bandwidth of said pass-band filters (48 to 50) in several steps during said acquisition phase, so that said pass-band filters divide up, at each step, one of the pass-bands of the preceding step into non-overlapping pass-bands.

(Figure 2)