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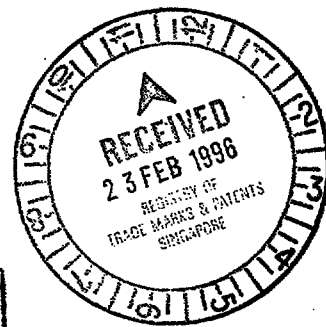
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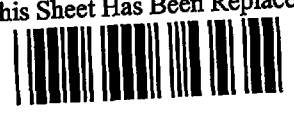
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(54) IMAGE PROCESSOR.

(57) The image processor includes a VRAM which stores image data of the original background image. A background address control circuit uses constants given by a CPU to calculate the address of the VRAM for a rotated, enlarged or reduced image of the original background. The color data of the rotated, enlarged or reduced image of the original background are read out from the calculated addresses of the VRAM, and video signals are formed based on the color data.

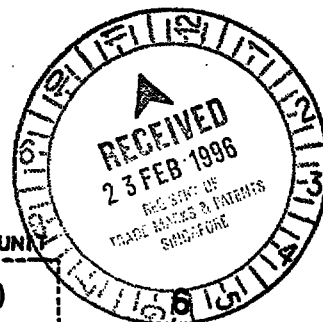
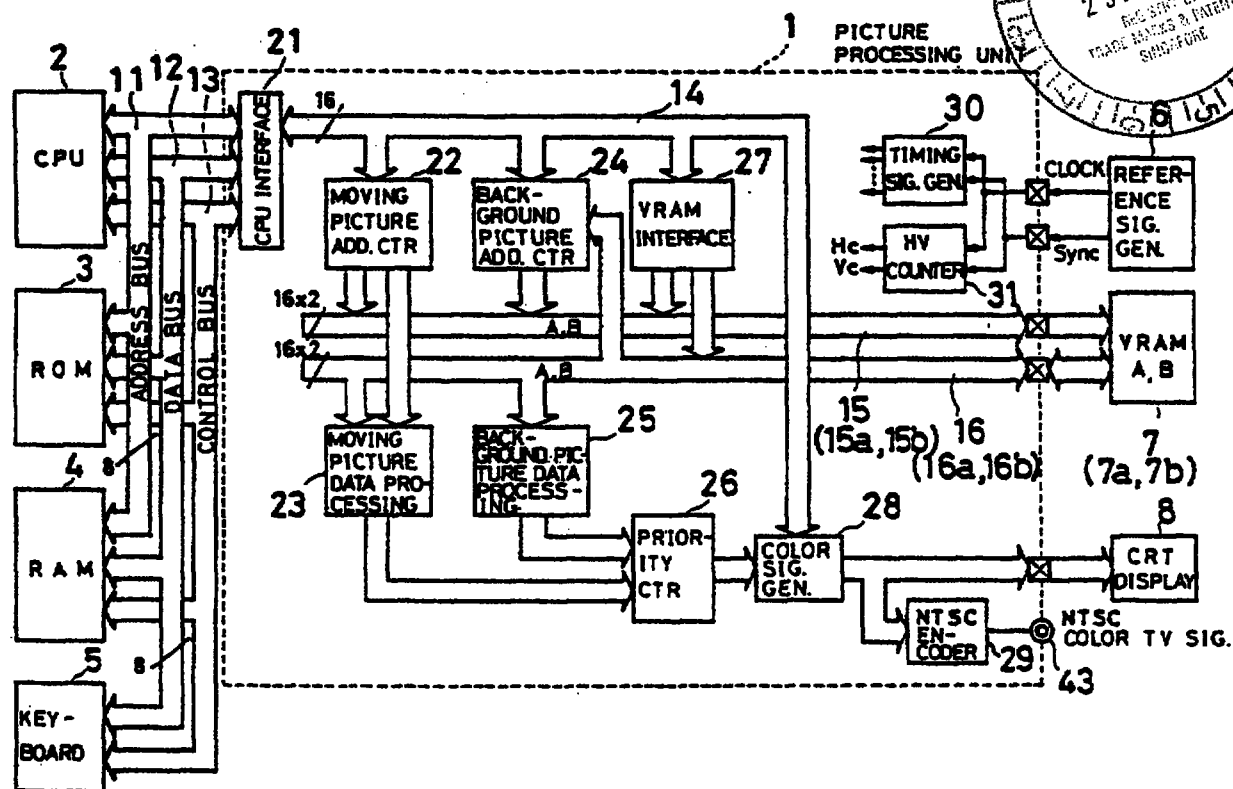
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FIG. 1



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