

ABSTRACT

The present application provides a sense amplifier, a memory, and a control method. The sense amplifier includes: an amplification module, configured to: when the sense amplifier is at an amplification stage, amplify a voltage difference between a bit line and a reference bit line; and a controlled power supply module, connected to the amplification module, and configured to: determine a drive parameter according to a first rated pull rate range and a second rated pull rate range, and supply power to the amplification module according to the drive parameter, to control the amplification module to pull a voltage of the bit line or a voltage of the reference bit line to a first preset value at a first rated pull rate at the amplification stage and pull the voltage of the reference bit line or the voltage of the bit line to a second preset value at a second rated pull rate at the amplification stage; where the first rated pull rate is within the first rated pull rate range, and the second rated pull rate is within the second rated pull rate range. The present solution improves a data read success rate and data read accuracy.

Fig. 3