

MEMORY PAGE PROCESSING METHOD AND DEVICE, APPARATUS, AND STORAGE MEDIUM

ABSTRACT

This application discloses a memory page processing method and device, an apparatus, and a storage medium, and pertains to the field of communication technologies. According to the method, after a memory page is allocated to a virtual address that causes a page fault, the allocated memory page is pre-padded with first data, and then the memory page padded with the first data is accessed. Subsequently, if data in the memory page is migrated to a cache, the first data padded in the memory page is also migrated to the cache. If the data in the memory page is evicted from the cache to a memory, the evicted cache data may also include the first data. Because the first data is data in a data sequence corresponding to a cache compression mode, when the cache data including the first data is evicted back to the memory, a probability that the cache data matches the cache compression mode can be increased. In this way, a compression ratio of the cache data can be increased.

Fig. 2