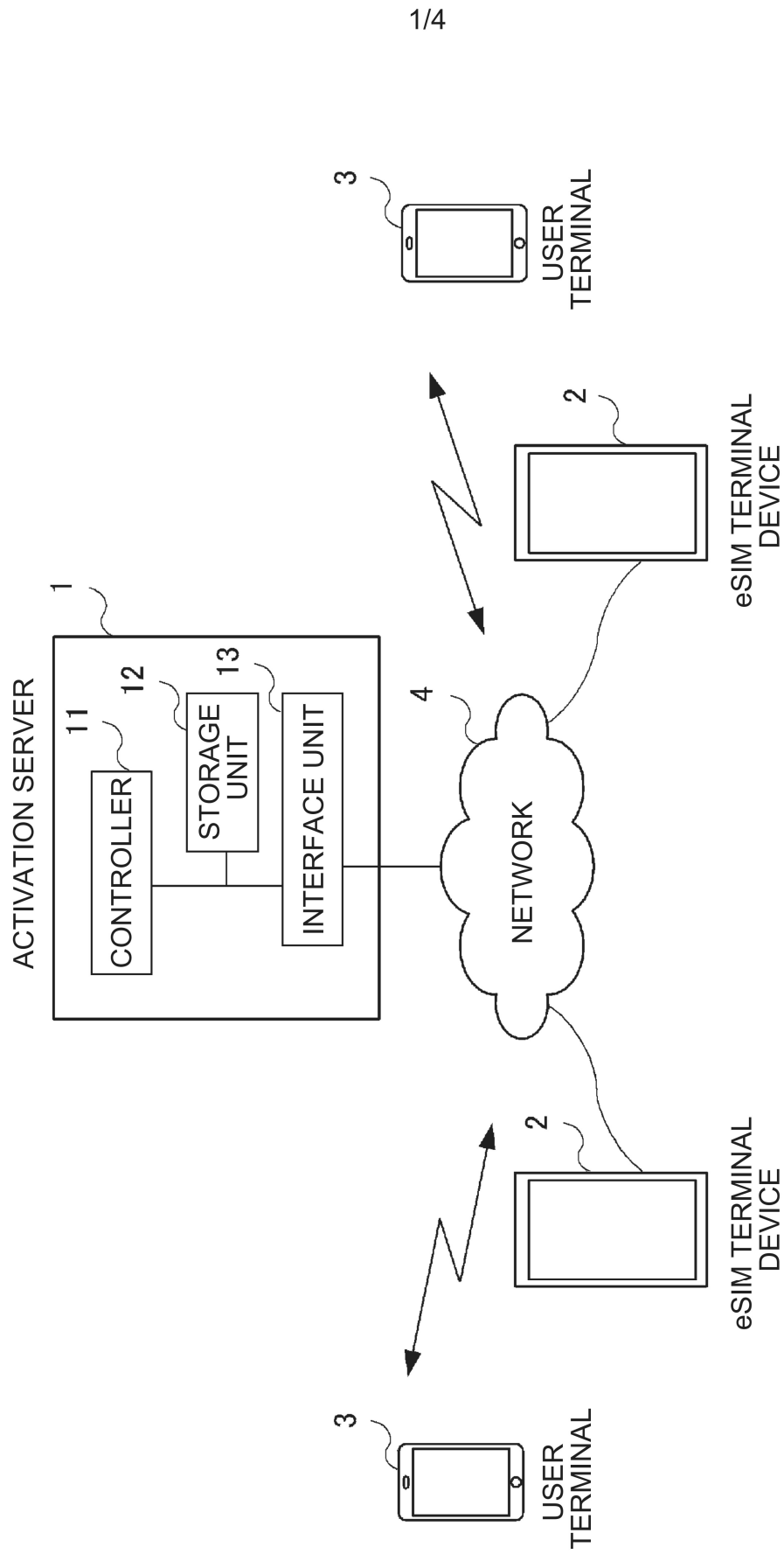
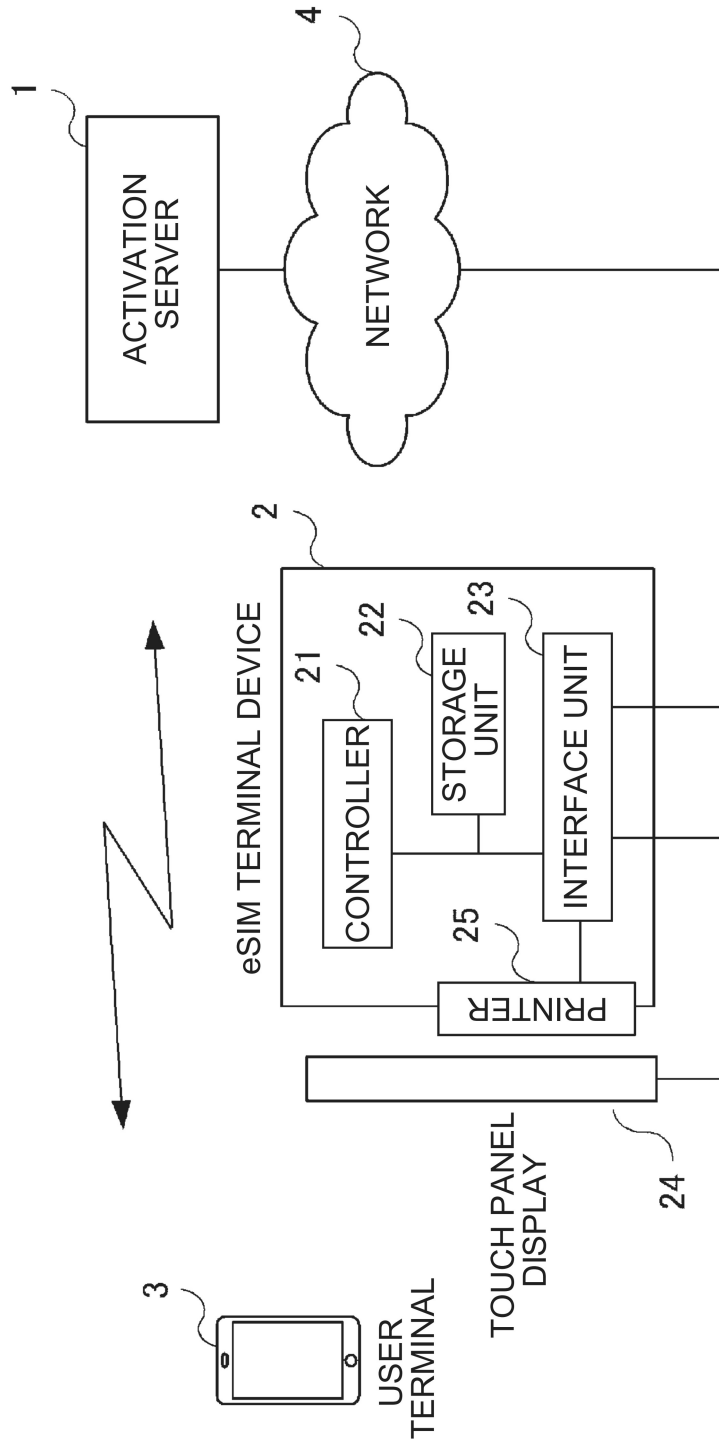


FIG. 1

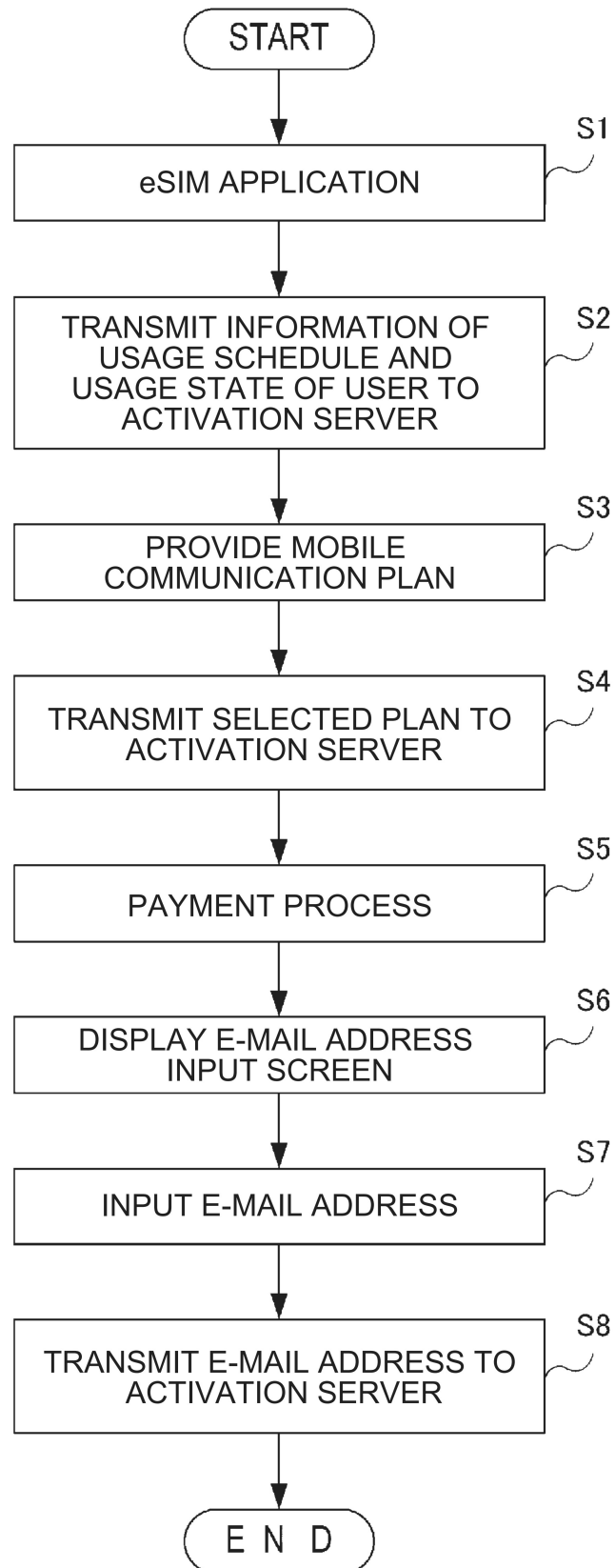


[FIG. 1] SCHEMATIC CONFIGURATION OF PRESENT SYSTEM

FIG. 2

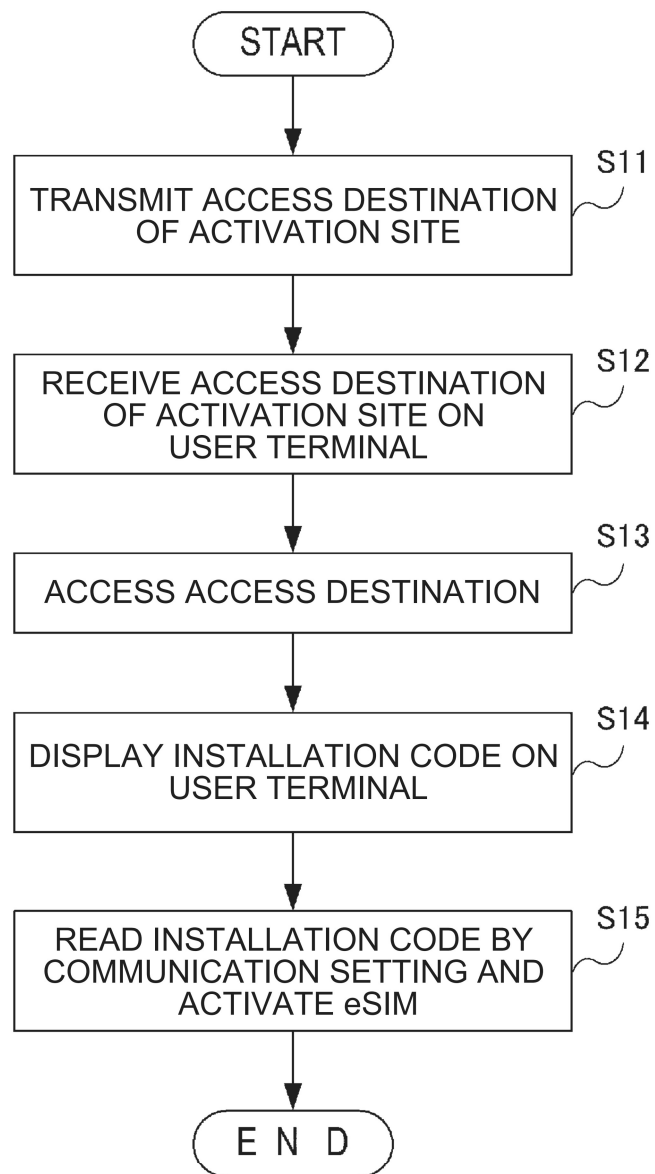


[FIG. 2] CONFIGURATION OF eSIM TERMINAL DEVICE

FIG. 3

[FIG. 3] FIRST HALF OF PROCESS

FIG. 4



[FIG. 4] LATTER HALF OF PROCESS