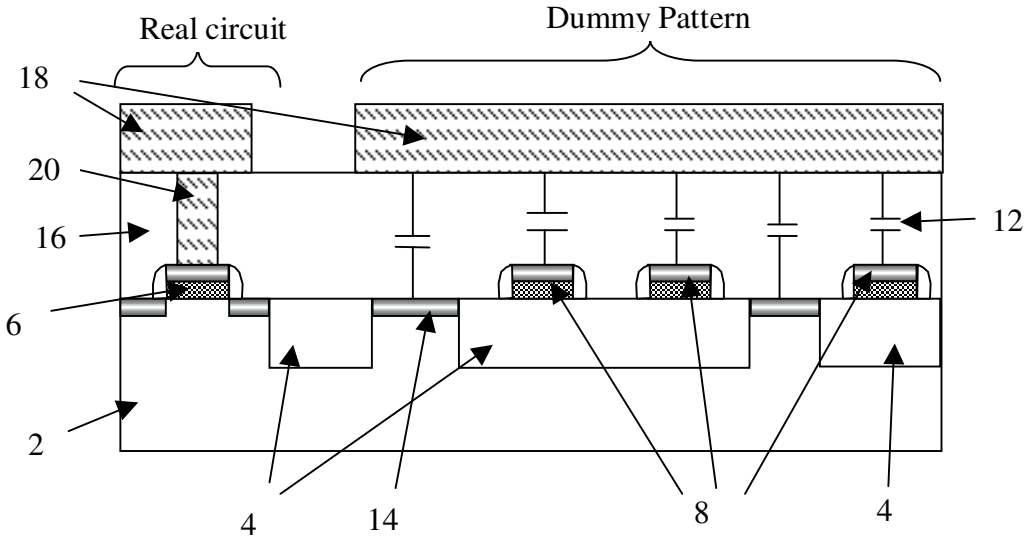

Real circuit
Dummy Pattern
18
20
16
12
6
2
4
14
8
4

Fig. 1

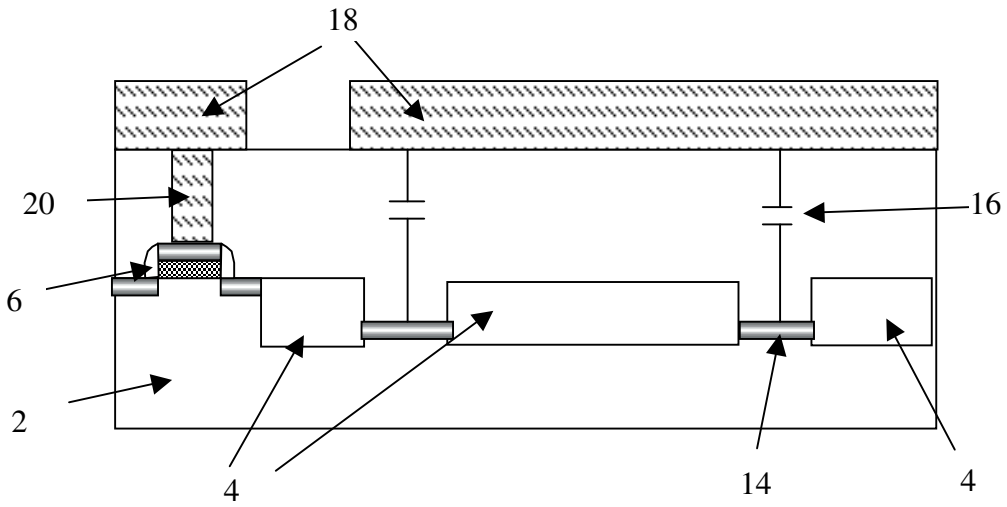

18
20
16
6
2
4
14
4

Fig. 2

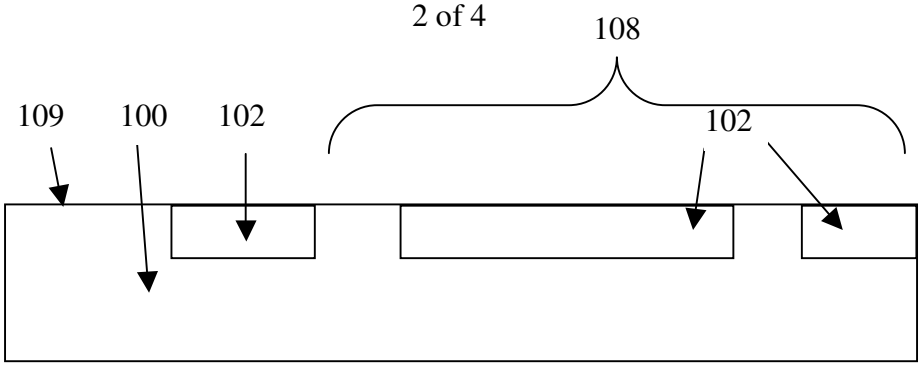


Fig. 3

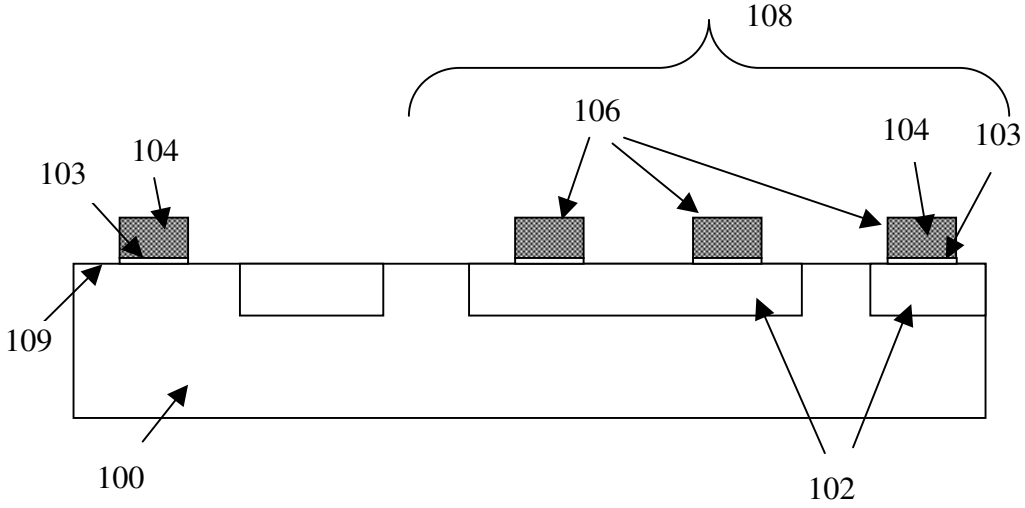


Fig. 4

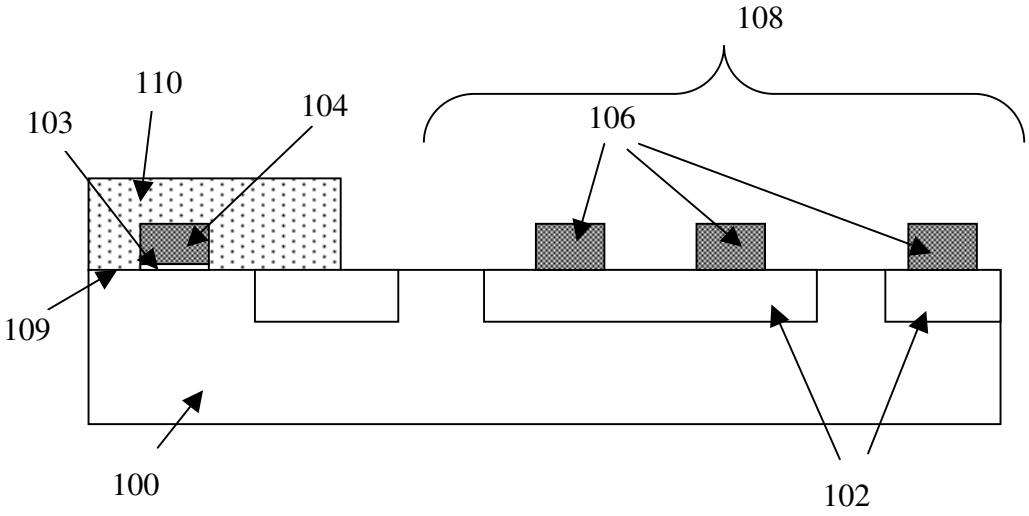


Fig. 5

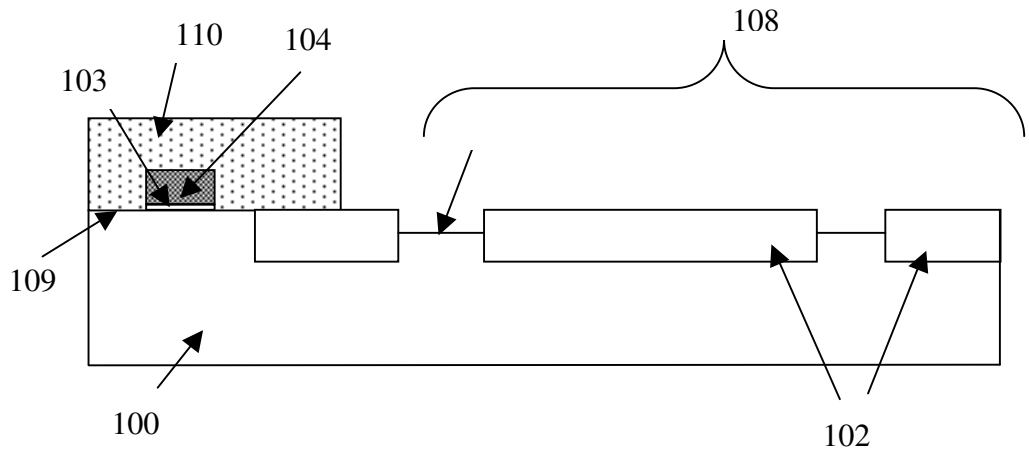


Fig. 6

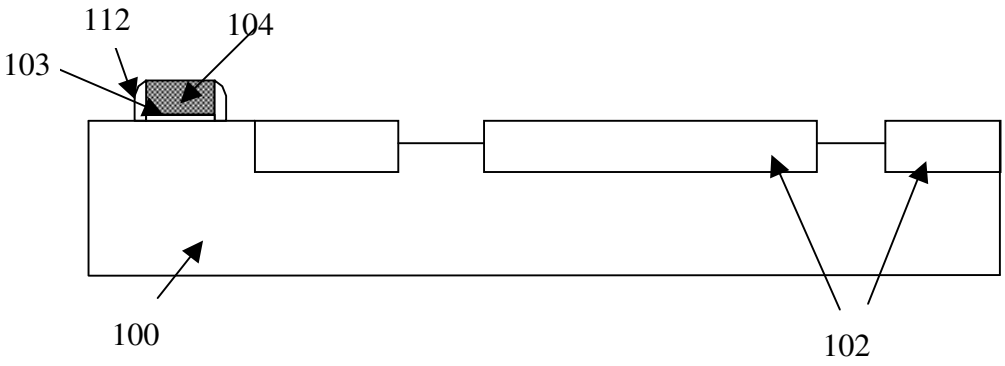


Fig. 7

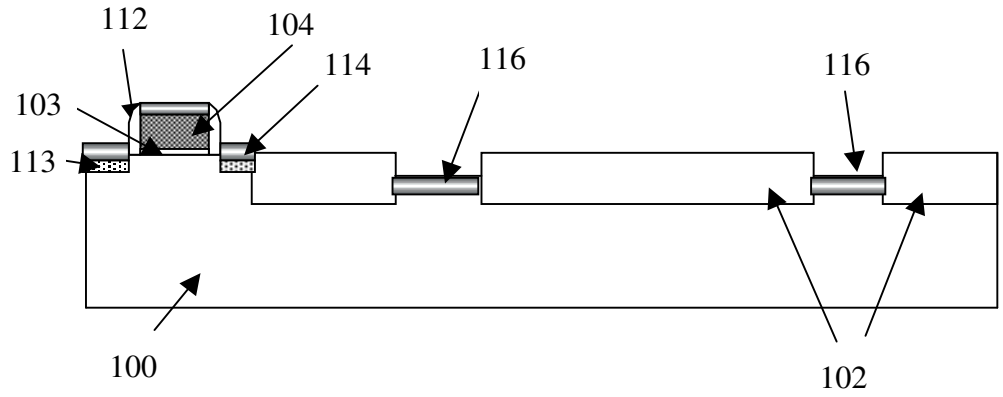


Fig. 8

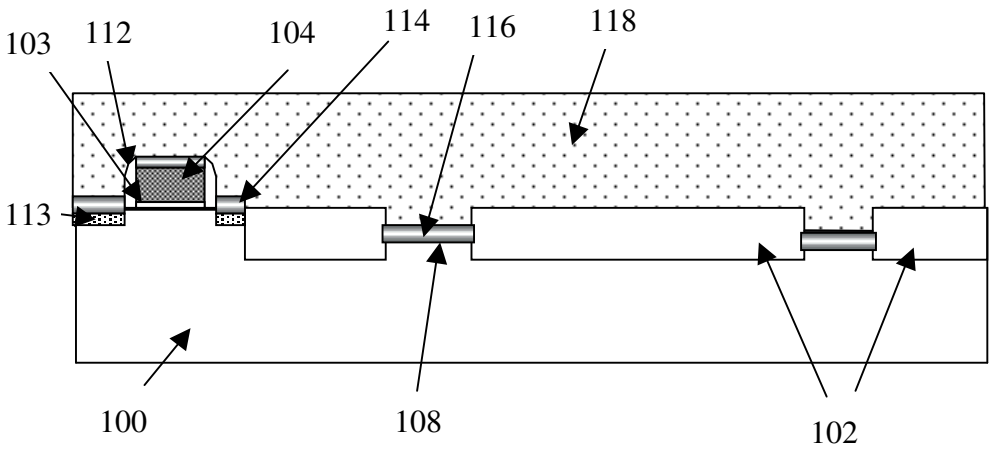


Fig. 9

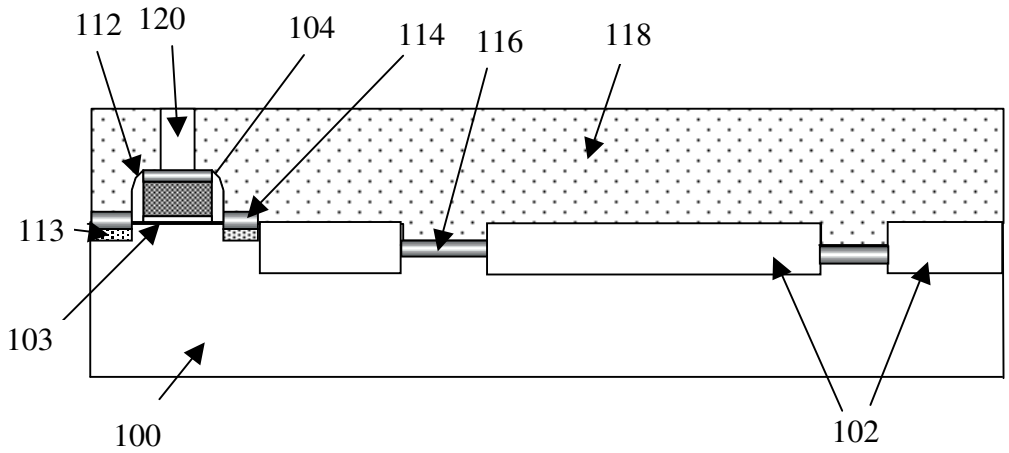


Fig. 10

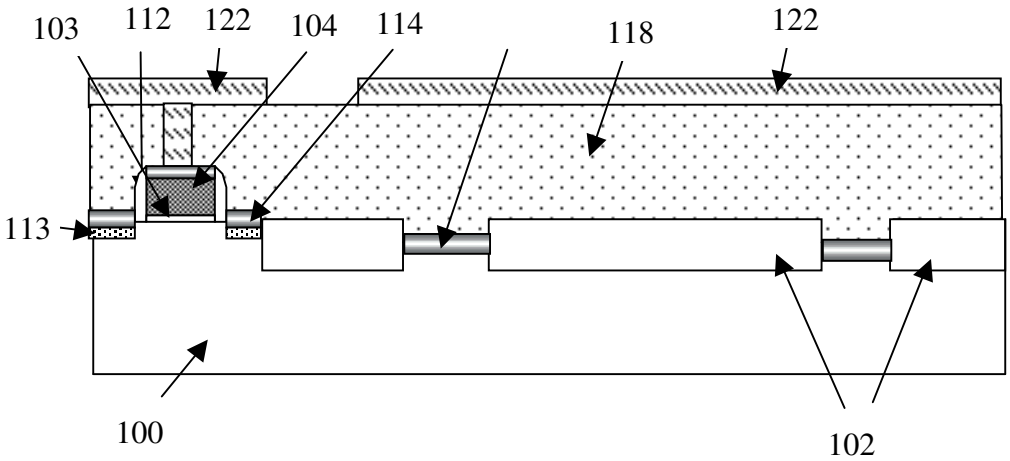


Fig. 11