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(71) Applicant *(for all designated States except US)*: **QUALCOMM Incorporated** [US/US]; 5775 Morehouse Drive, San Diego, California 92121 (US).

(72) Inventors; and

(75) Inventors/Applicants *(for US only)*: **CHEN, Peisong** [CN/US]; 10859 Caminito Alvarez, San Diego, California 92126 (US). **RAVEENDRAN, Vijayalakshmi R.** [IN/US]; 4272 Calle Mar De Ballenas, San Diego, California 92130 (US).

(74) Agents: **WADSWORTH, Philip R.** et al.; 5775 Morehouse Drive, San Diego, California 92121 (US).

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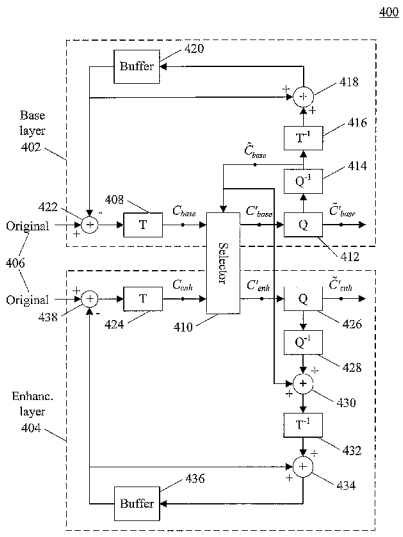
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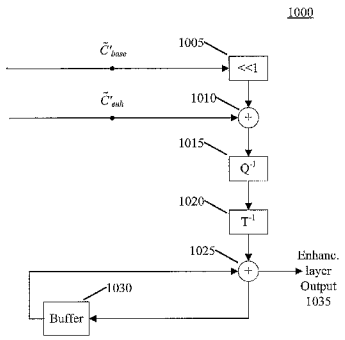
Declarations under Rule 4.17:

— *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, LY, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian*

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(54) **Title:** SCALABLE VIDEO CODING WITH TWO LAYER ENCODING AND SINGLE LAYER DECODING





(57) **Abstract:** Some embodiments comprise a method of decoding a video bitstream that include receiving a first layer of data and a second layer of data, combining the received first layer data and the received second layer data, and decoding the combined data. Also, a method of video encoding that includes selecting data for encoding in a first layer and a second layer so as to allow decoding of the data in a single combined layer, and encoding the selected data in the first layer and in the second layer by encoding a coefficient in the first layer and encoding a differential refinement to the first layer coefficient in the second layer. A reduction of the number of dequantization steps to one is achieved in order to support pipelined dequantization in the hardware core.

WO 2006/099221 A3

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