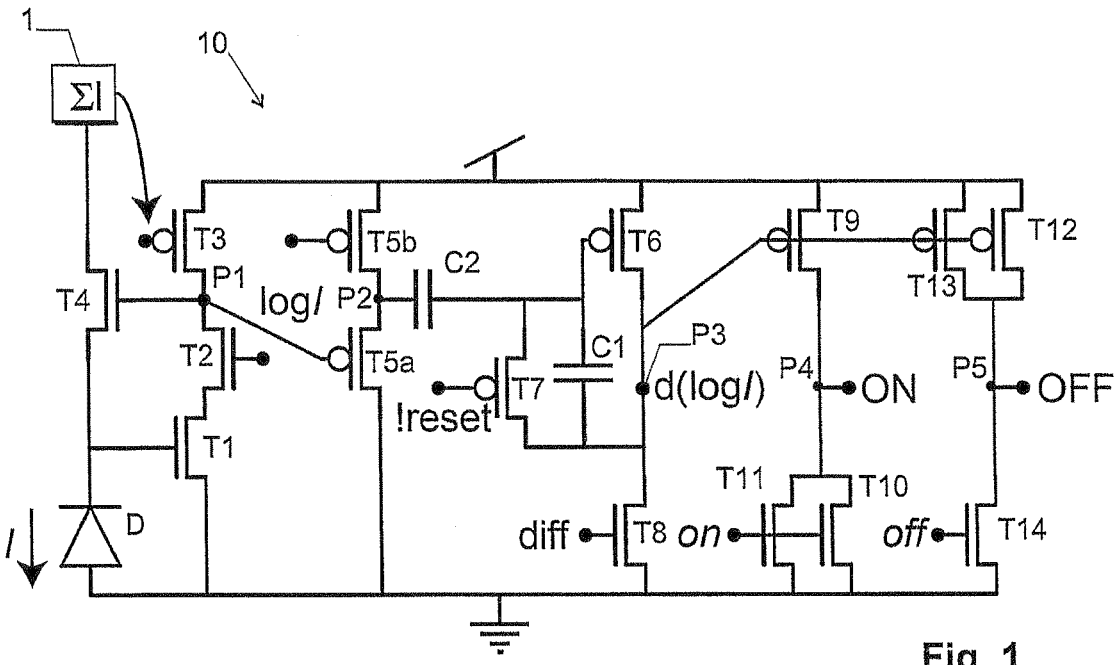

1
10
Σ|
T3
T5b
C2
T6
T9
T12
T13
P1
log/
P2
T4
T2
T5a
C1
P3
P4
ON
P5
OFF
!reset
T7
d(log/)
T1
T11
T10
D
I
diff
T8
on
off
T14

Fig. 1

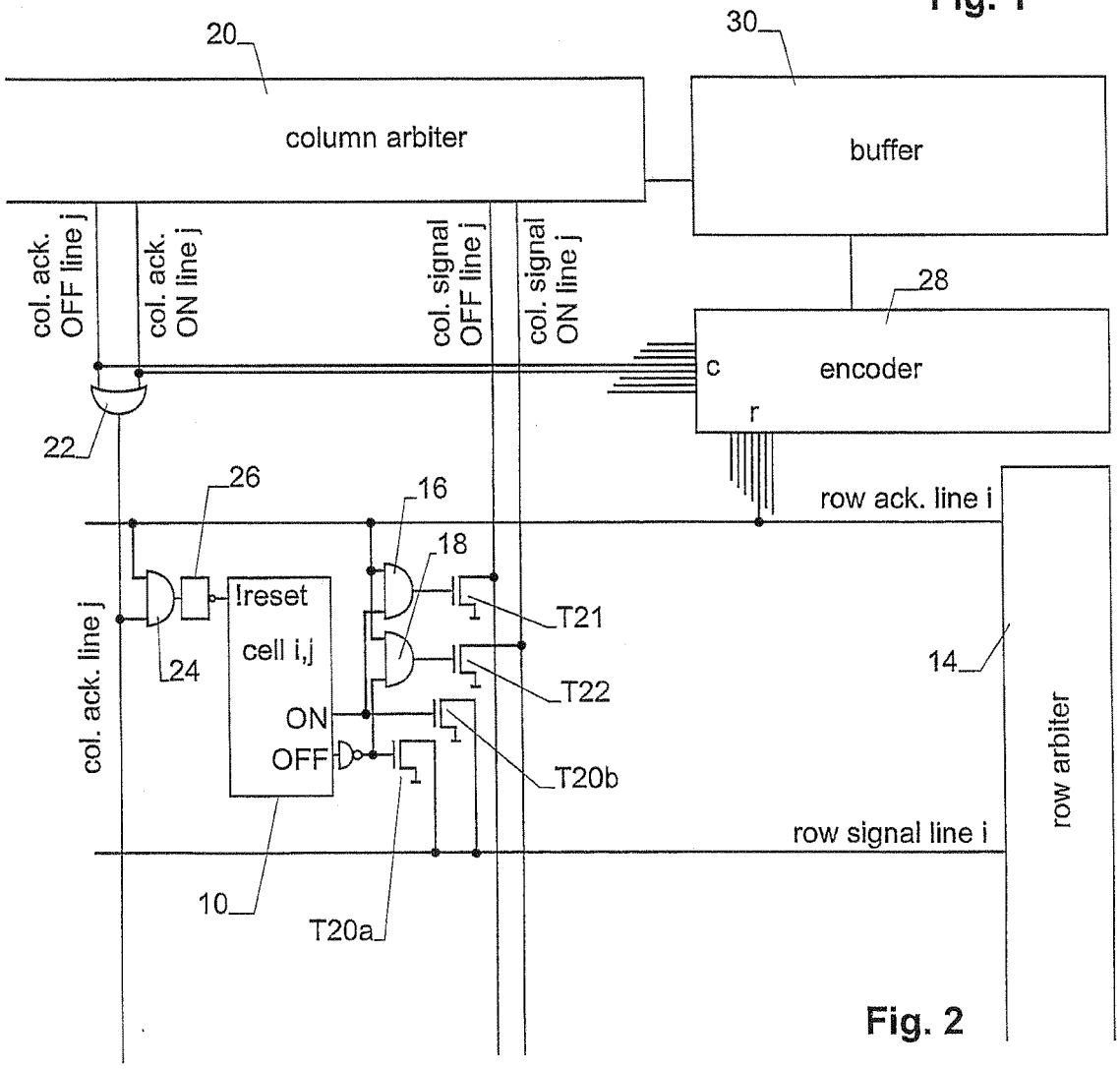

20
30
column arbiter
buffer
col. ack. OFF line j
col. ack. ON line j
col. signal OFF line j
col. signal ON line j
28
c
encoder
r
22
26
16
row ack. line i
18
!reset
cell i,j
T21
24
14
col. ack. line j
ON
T22
OFF
T20b
row arbiter
row signal line i
10
T20a

Fig. 2

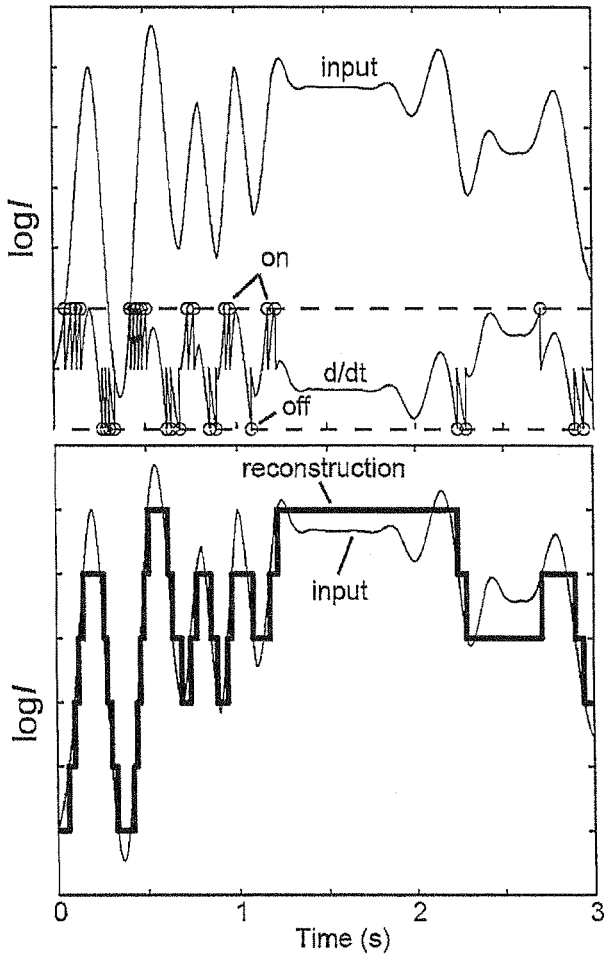


Fig. 3