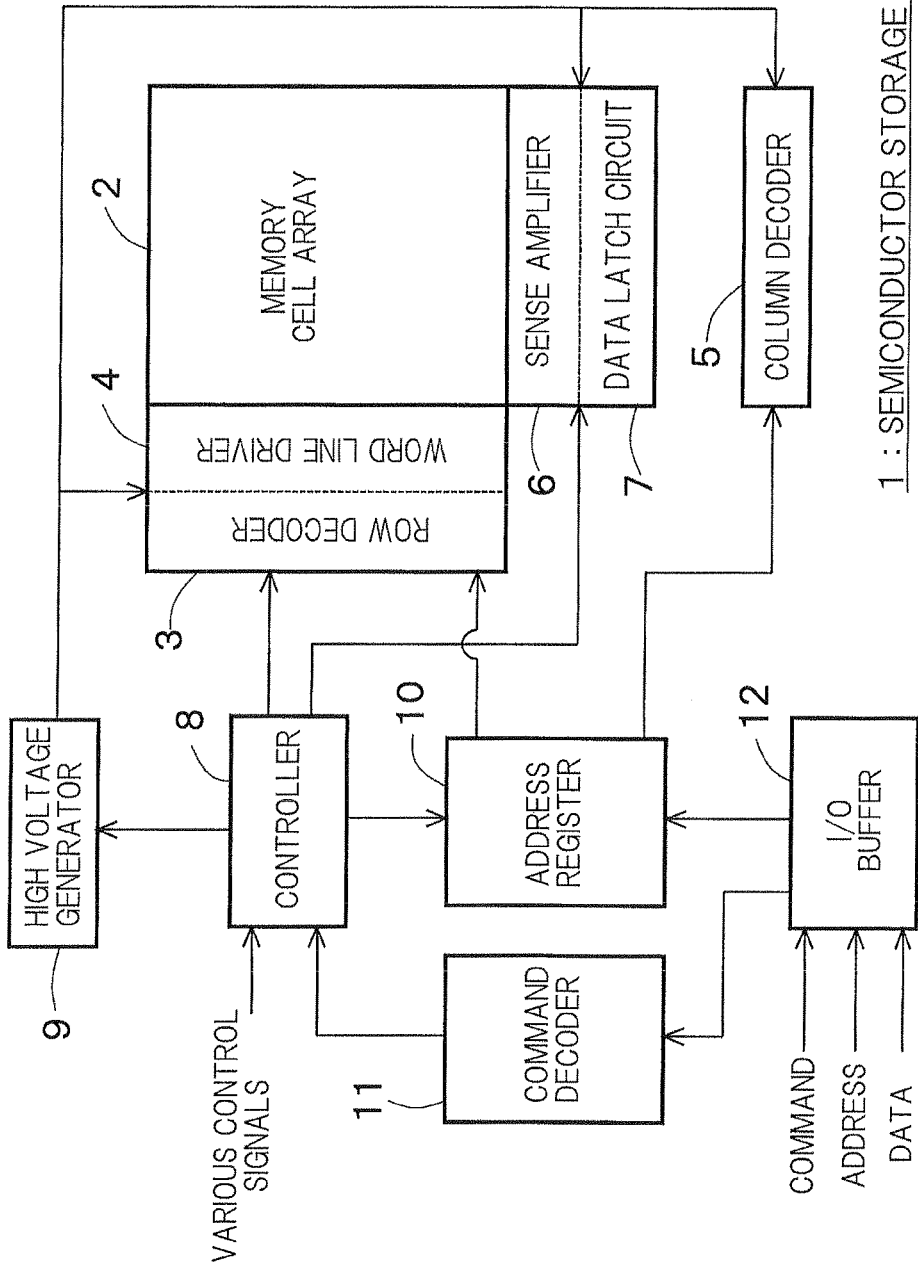




1 : SEMICONDUCTOR STORAGE DEVICE

1. G
L

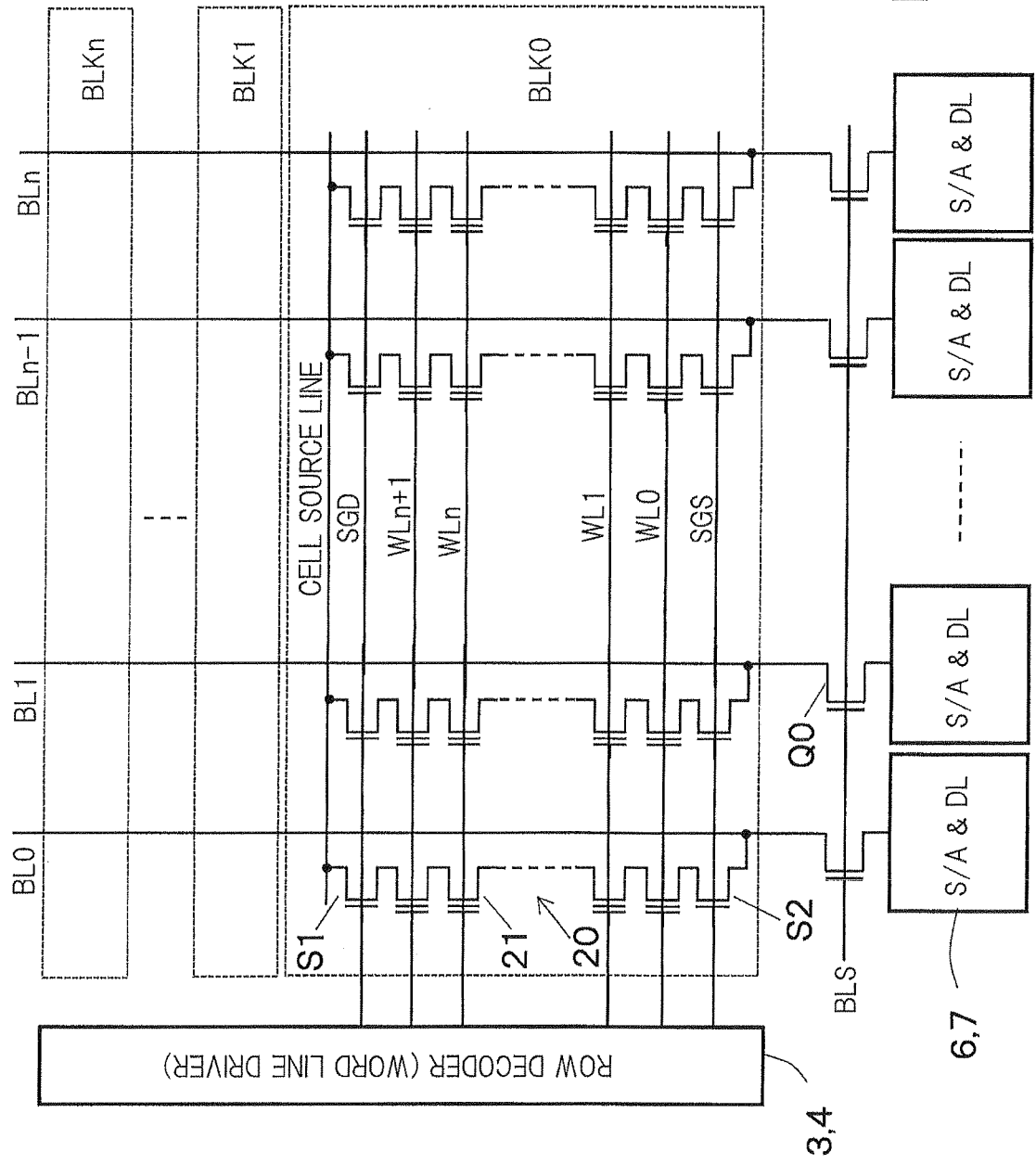


FIG. 2

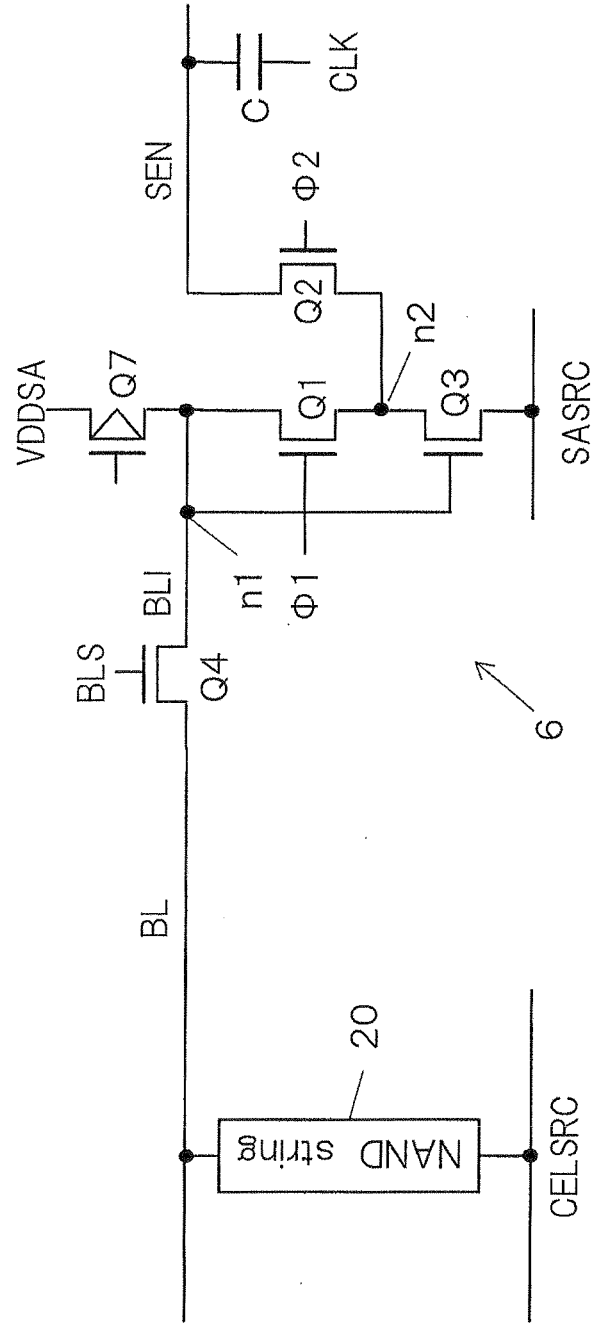


FIG. 3

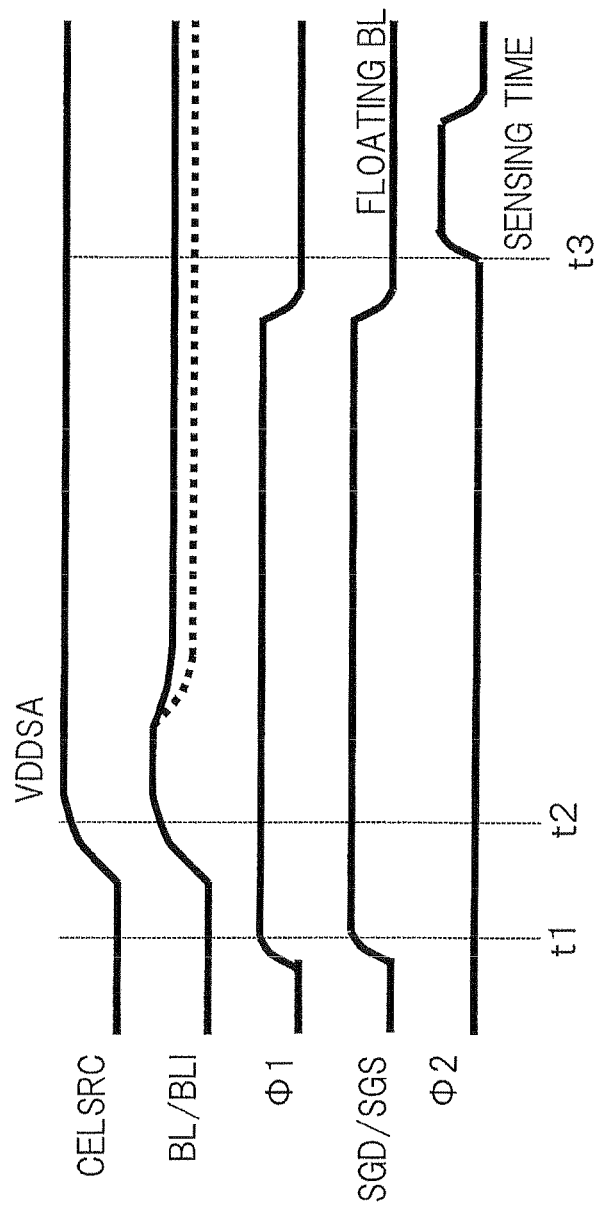
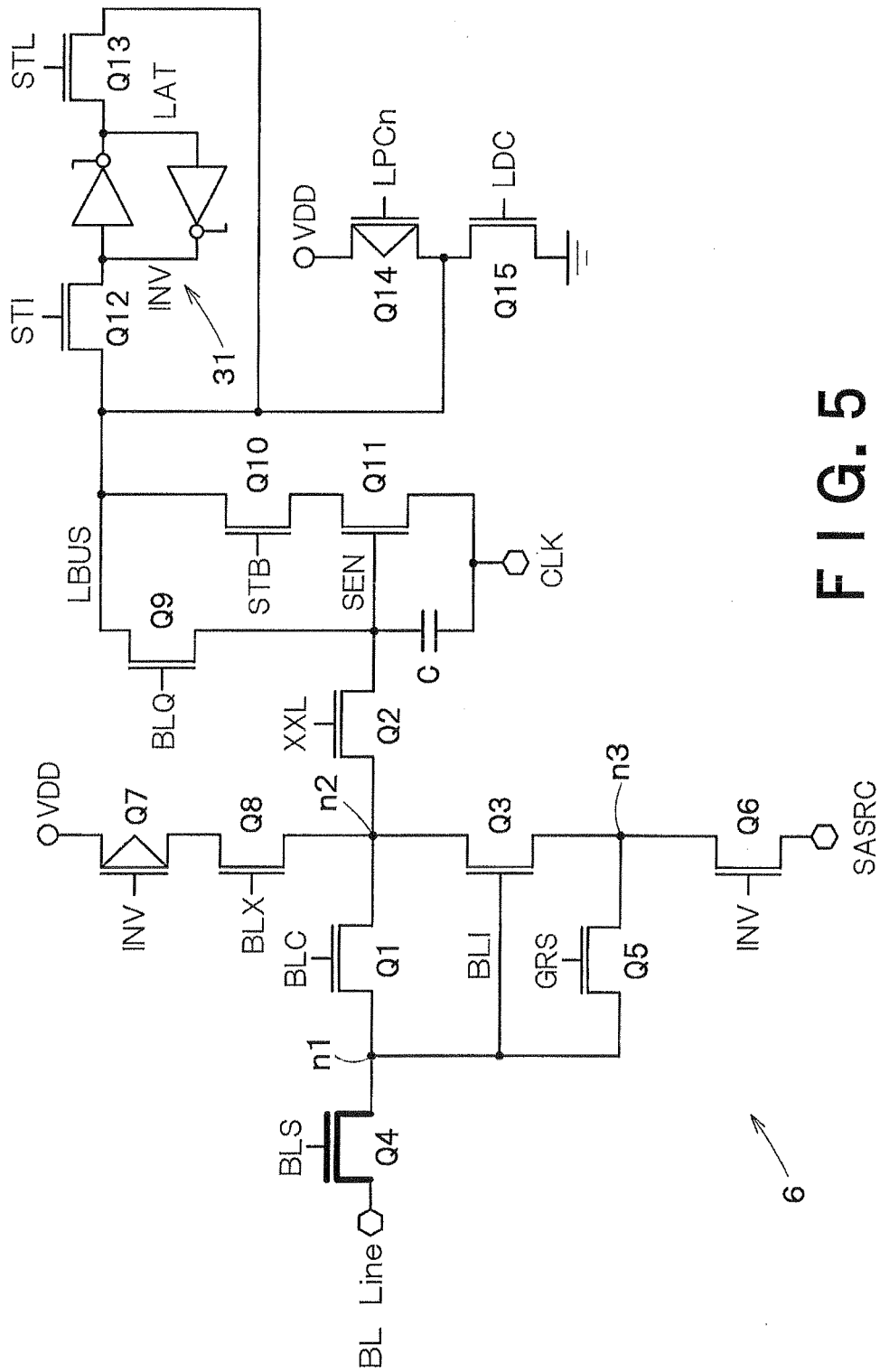


FIG.4



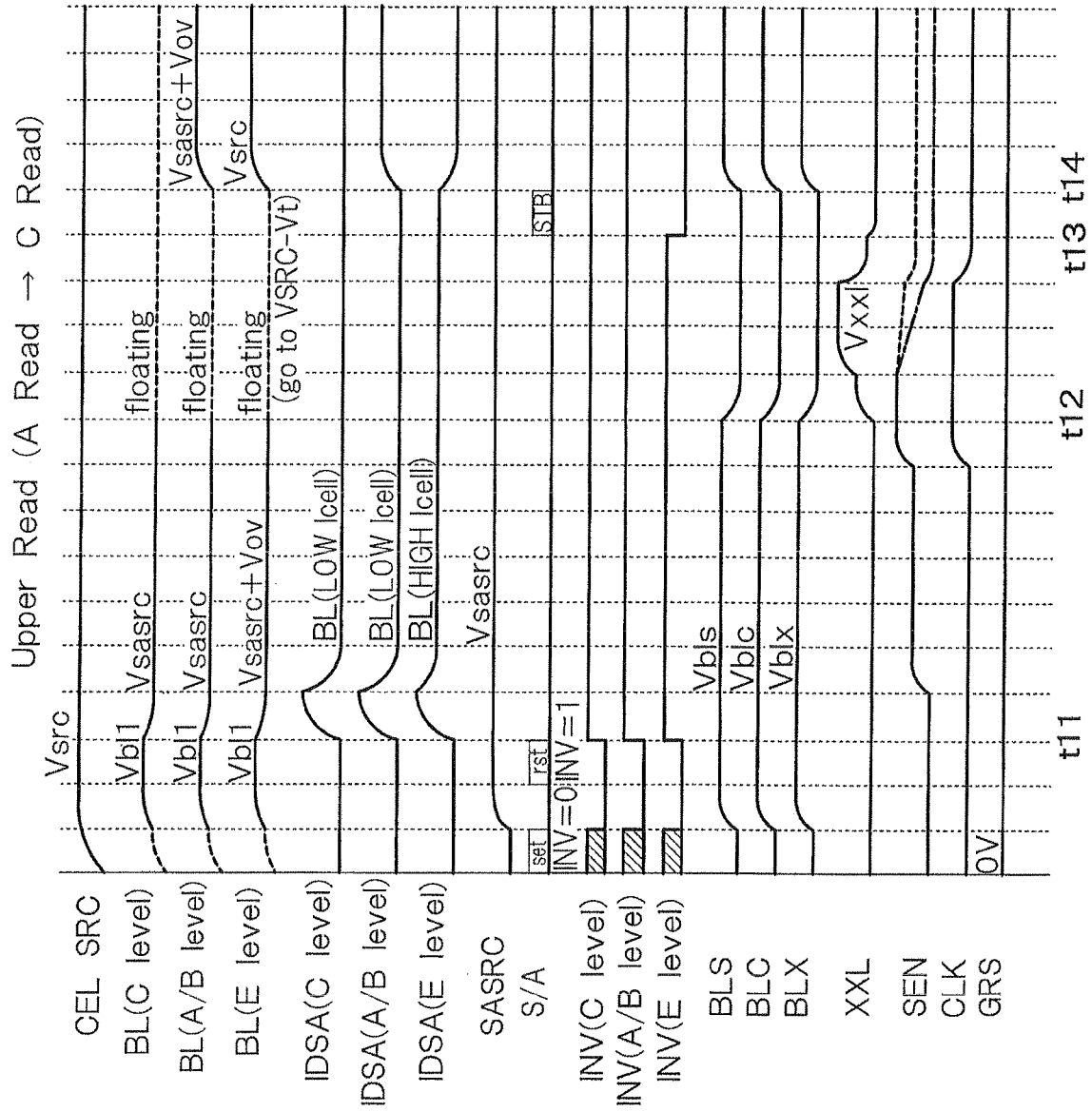


FIG. 6

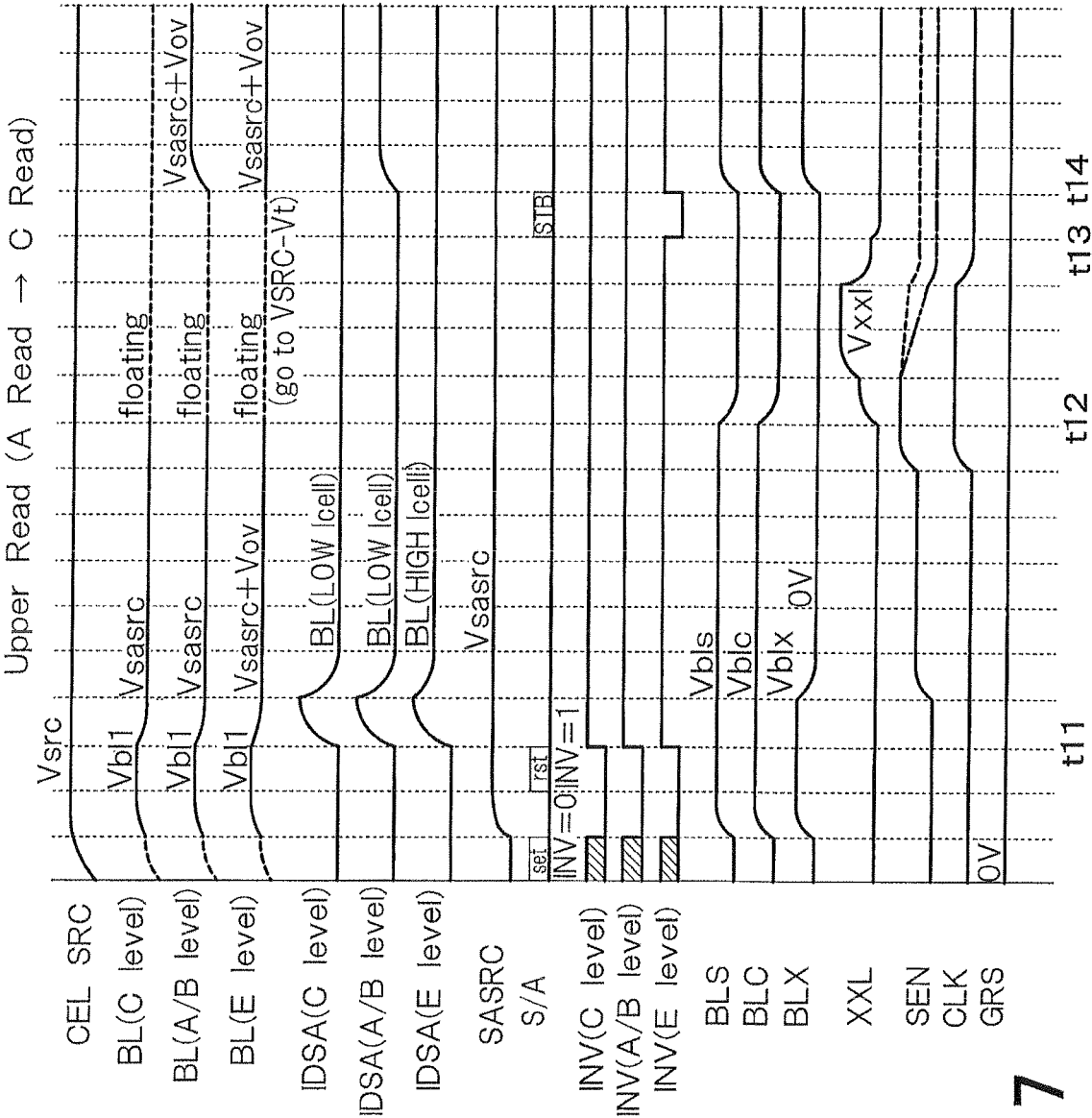
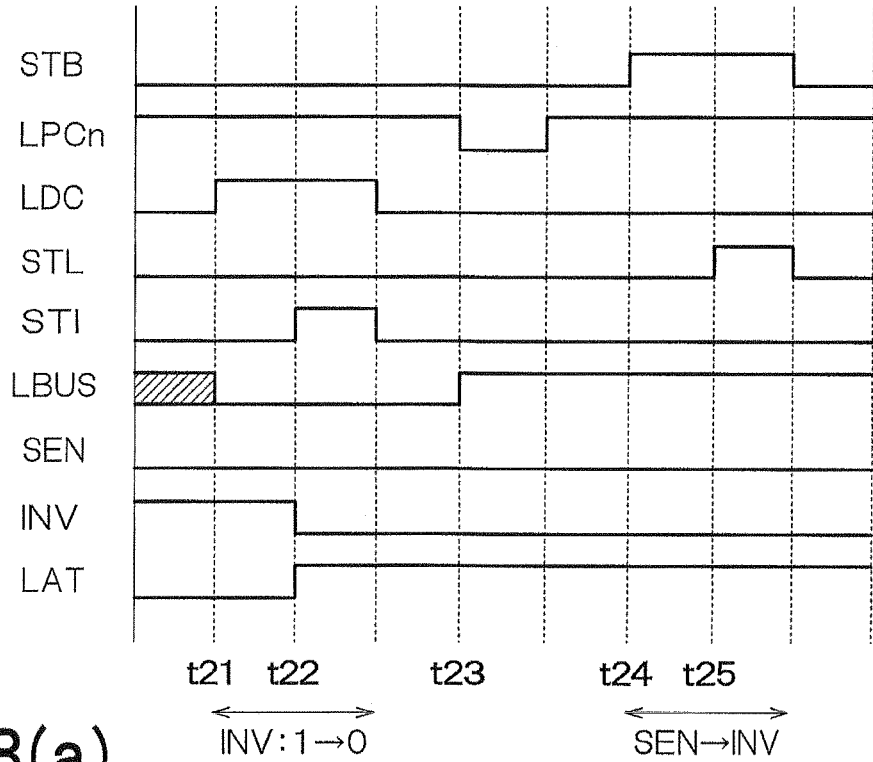


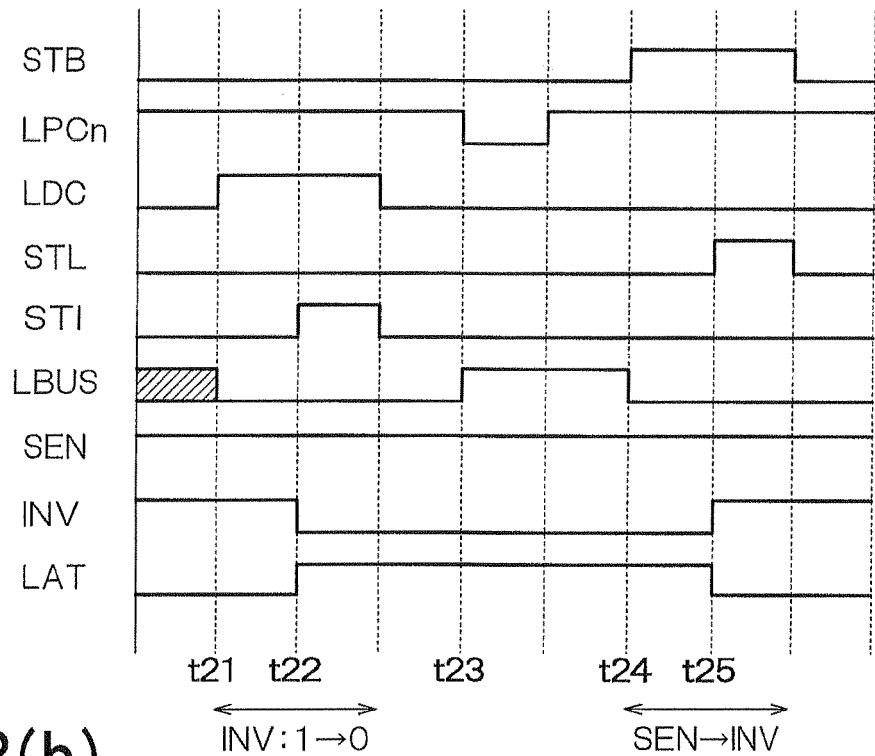
FIG. 7

SEN=0(On cell)



F I G. 8(a)

SEN=0(Off cell)



F I G. 8(b)

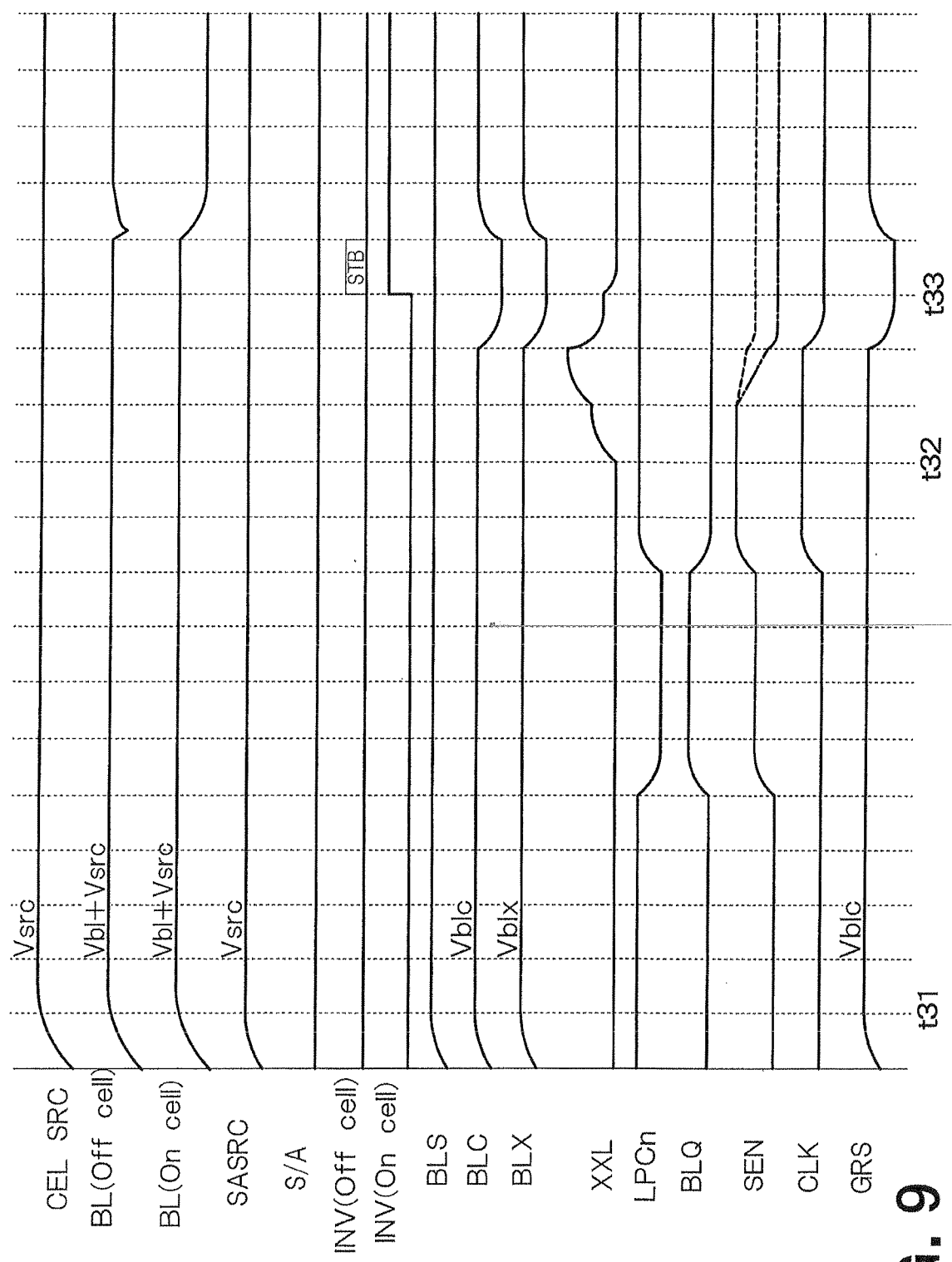
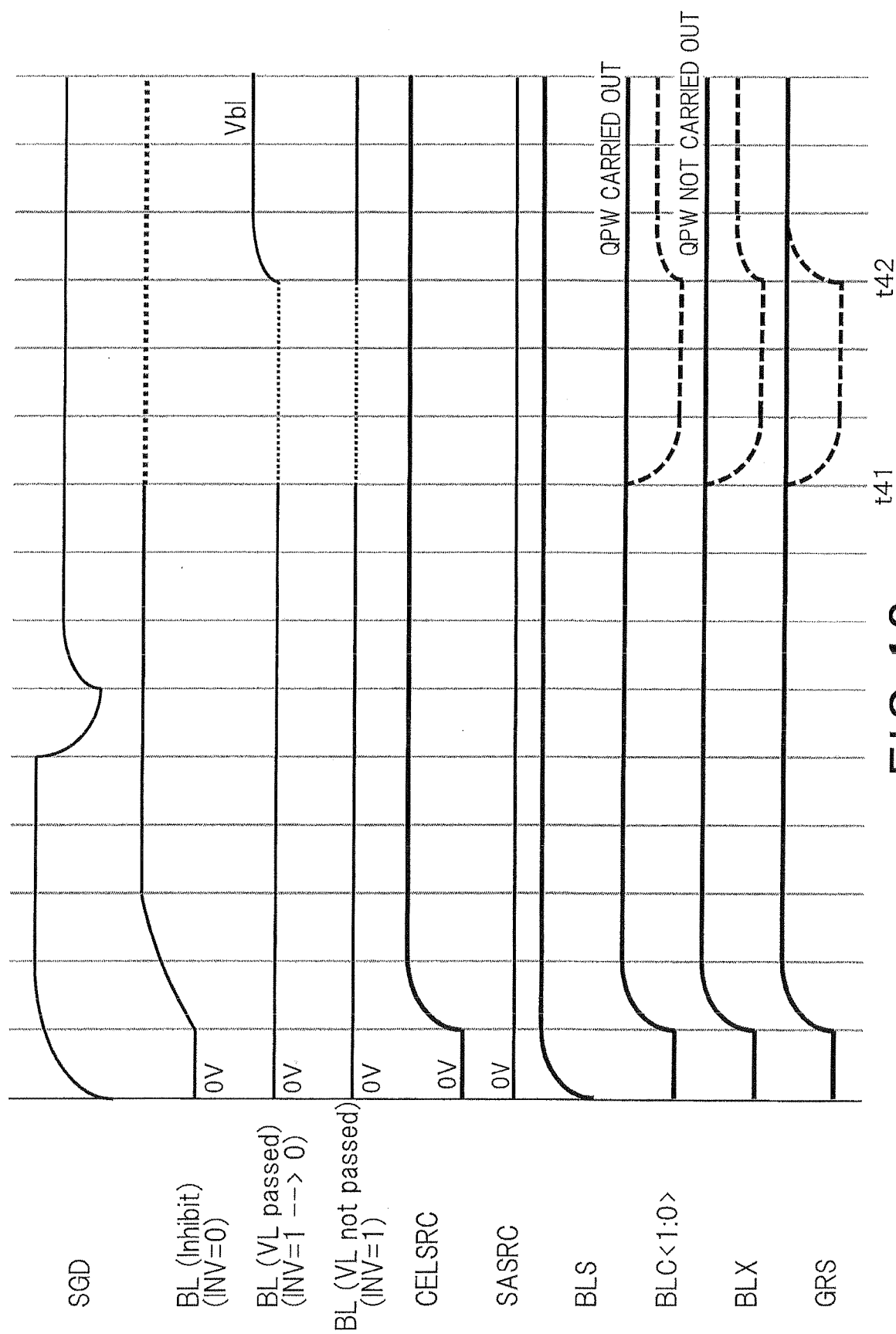


FIG. 9



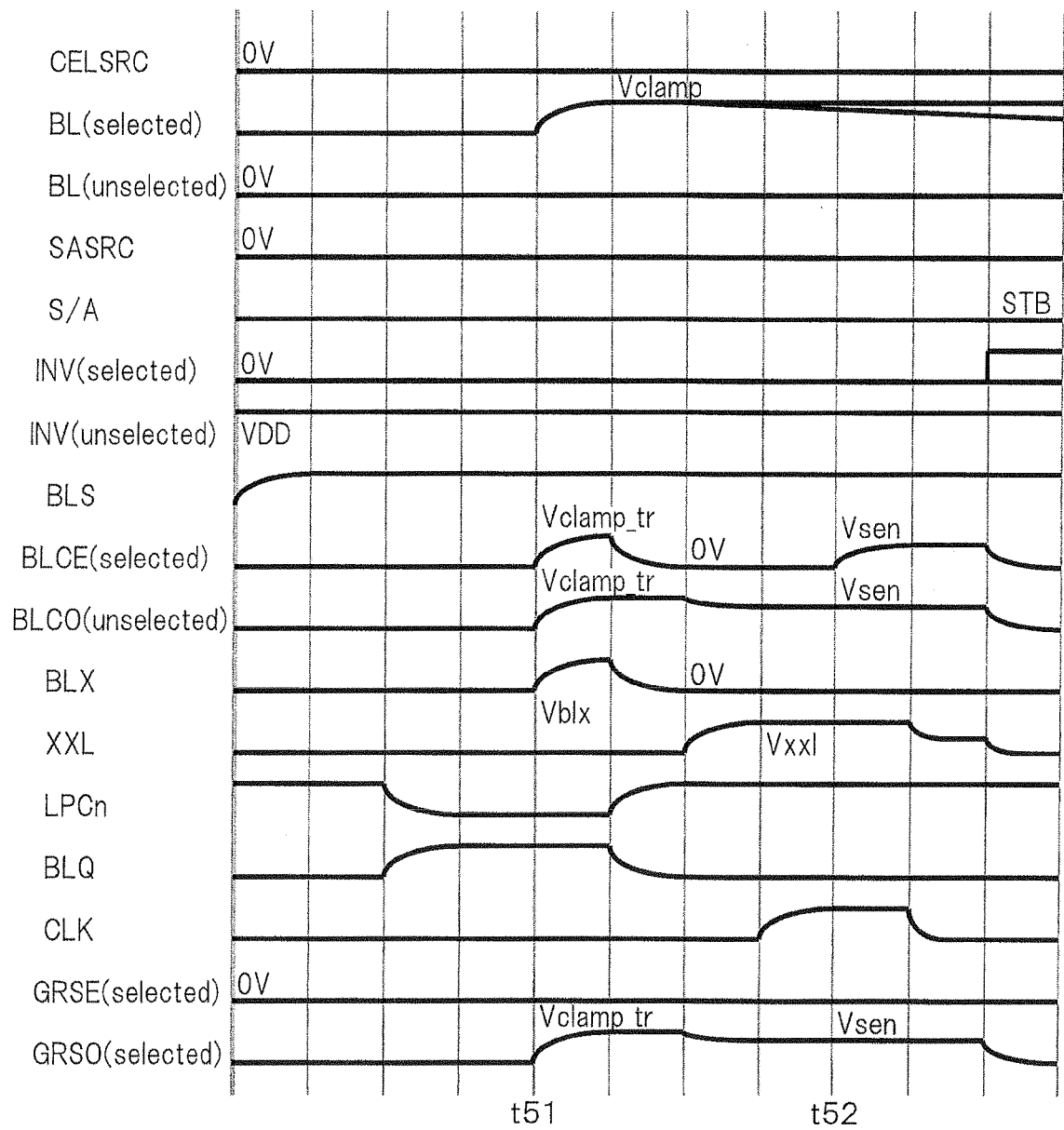


FIG.11



12
1
G.
—
L

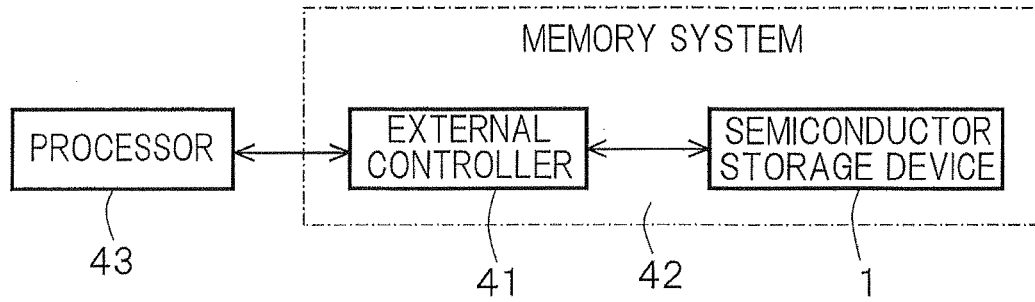


FIG. 13

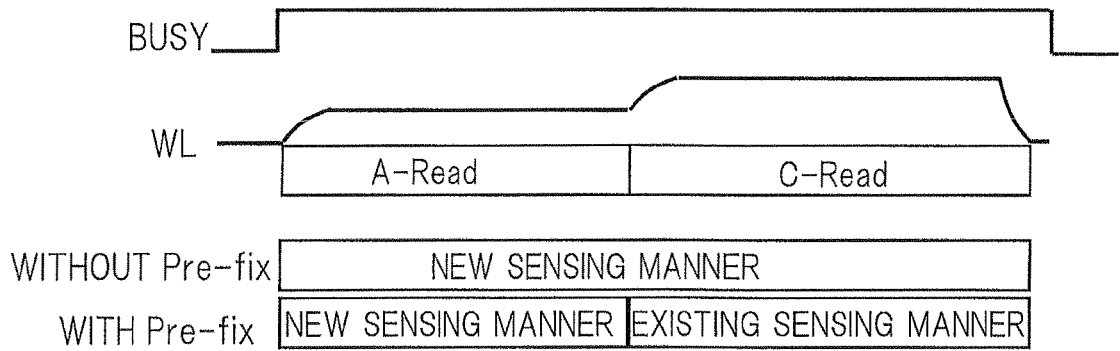


FIG. 14

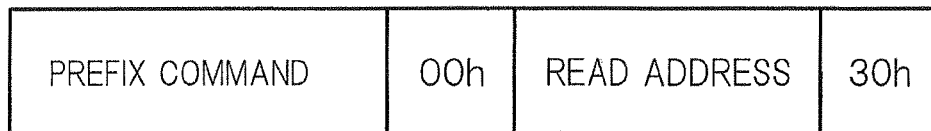


FIG. 15

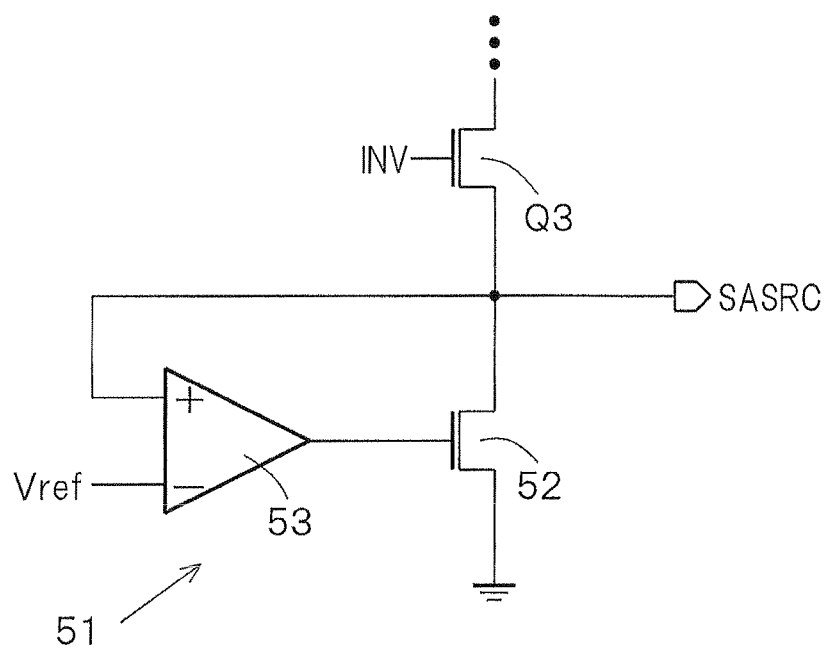


FIG.16

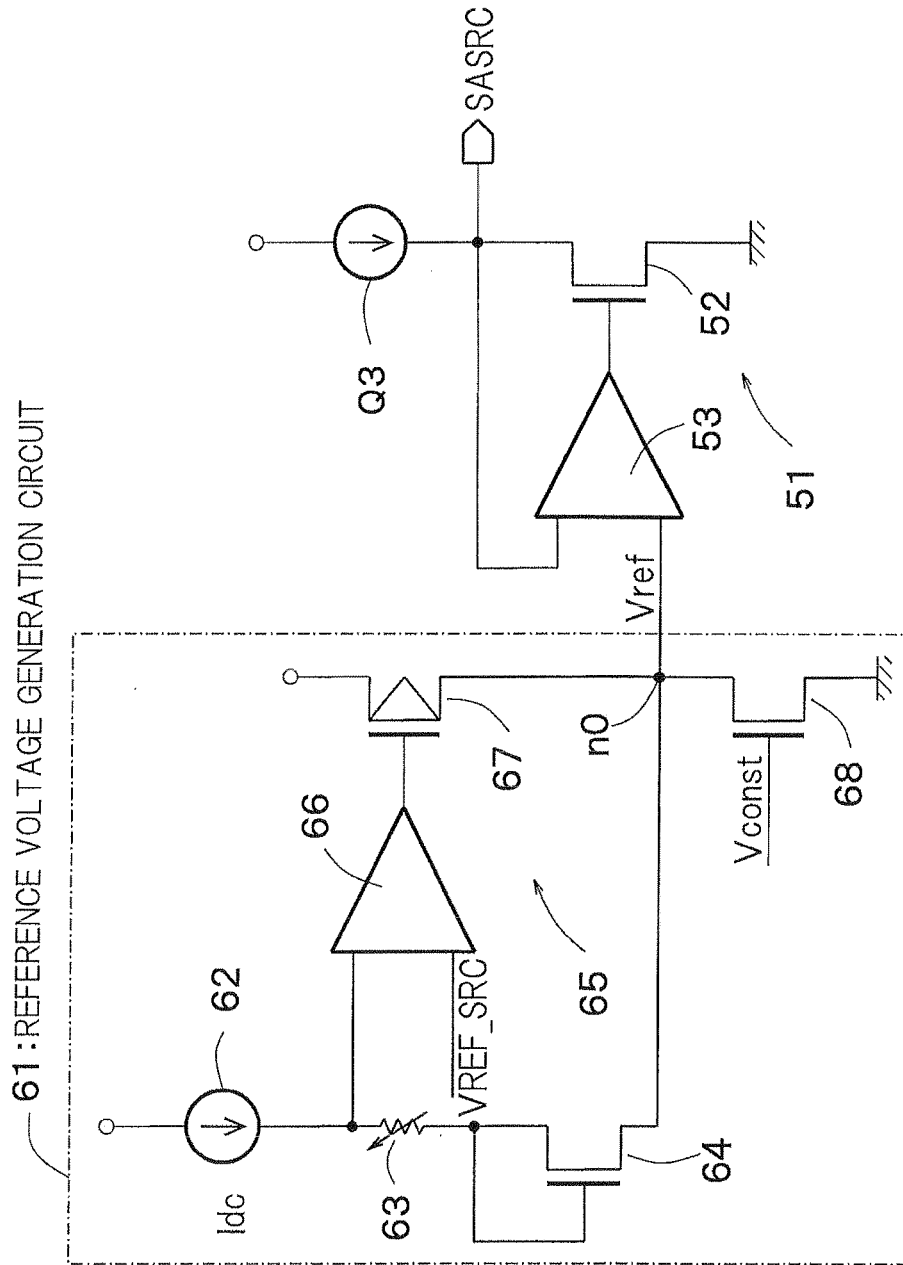


FIG.17